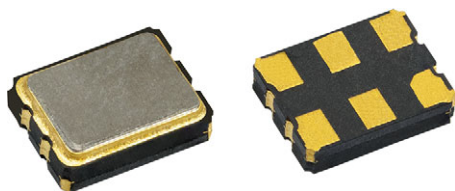


## Surface-Mount Oscillator



The XO-23P series is an ultra miniature package clock oscillator with dimensions 3.2 mm x 2.5 mm x 1.2 mm. It is specifically developed for telecommunications and network applications like OC-48 and OC-192.

### FEATURES

- Size: 3.2 x 2.5 x 1.2 (mm)
- Ultra small package
- Tri-state enable / disable
- LVPECL compatible
- Tape and reel packaging
- I<sub>R</sub> re-flow
- 2.5 V and 3.3 V input voltage
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

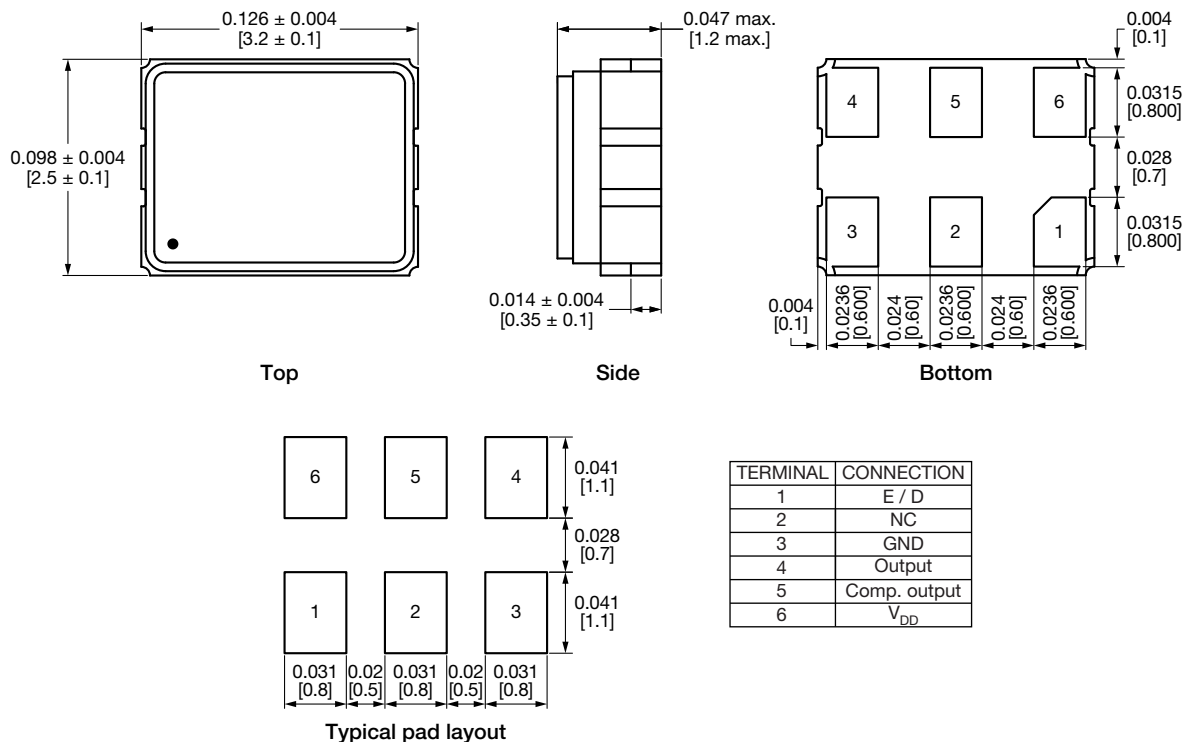


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

STANDARD ELECTRICAL SPECIFICATIONS			
PARAMETER	SYMBOL	CONDITION	VALUE
Frequency range	F <sub>O</sub>	-	77.76 MHz to 156.26 MHz
Frequency stability <sup>(1)</sup>		All conditions	± 25 ppm, ± 50 ppm, ± 100 ppm
Operating temperature range	T <sub>OPR</sub>	-	0 °C to 70 °C
			-40 °C to +85 °C (option)
Storage temperature range	T <sub>STG</sub>	-	-55 °C to +125 °C
Power supply voltage	V <sub>DD</sub>	Select desired voltage	2.5 V ± 10 %
	V <sub>DD</sub>		3.3 V ± 10 %
Aging (first year)		25 °C ± 3 °C	± 3 ppm
Supply current	I <sub>DD</sub>	-	100 mA max.
Output symmetry	Sym	At ½ V <sub>DD</sub>	45 % / 55 %
Rise time	t <sub>r</sub>	10 % V <sub>DD</sub> to 90 % V <sub>DD</sub>	1 ns max.
Rise/fall time	t <sub>f</sub>	90 % V <sub>DD</sub> to 10 % V <sub>DD</sub>	1 ns max.
Output voltage	V <sub>OH</sub>	-	V <sub>DD</sub> - 1.025 V min.
	V <sub>OL</sub>	-	V <sub>DD</sub> - 1.62 V max.
Output load	LVPECL	-	50 Ω to V <sub>DD</sub> - 2.0 V
Start-up time	t <sub>s</sub>	-	10 ms max.
Phase jitter	J	12 kHz to 20 MHz	< 1.0 pS <sub>RMS</sub>
Pin 1, tri-state function		-	Pin 1 = H or open (output active at pin 4) Pin 1 = L (high impedance at pin 4)

### Note

<sup>(1)</sup> Include: 25 °C tolerance, operating temperature range, input voltage change, aging, load change, shock and vibration

**DIMENSIONS** in inches [millimeters]

**Note**

- A 0.01  $\mu$ F bypass capacitor should be placed between V<sub>DD</sub> (pin 4) and GND (pin 2) to minimize power supply line noise

**PART NUMBER CONFIGURATIONS** (to be used on all New Designs)

X	O	2	3	P	3	D	R	E	H	T	1	6	M	1	2	8
Oscillator		Package / Size		Logic	Voltage	Stability	Temp.	Tri-State	Package	Options	Frequency					
		2	3	P = LVPECL	3 = 3.3 V 2 = 2.5 V	C = $\pm 100$ ppm D = $\pm 50$ ppm F = $\pm 35$ ppm G = $\pm 30$ ppm E = $\pm 25$ ppm H = $\pm 20$ ppm	S = -10 °C to +70 °C R = -40 °C to +85 °C	N = no connect E = Tri-State	H = tape and reel	X = no options T = 45/55 Contact factory for available options	Use "M" as decimal place holder  Frequency must be six digits - complete with "0" at the end					

**PART MARKING**

Line 1: V25.00 (frequency)

Line 2: YWWA (date code / factory)



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