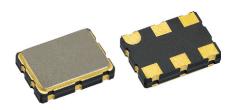




Surface-Mount Oscillator



The XO-57P series is an ultra miniature package clock oscillator with dimensions 7.0 mm x 5.0 mm x 1.8 mm. It is specifically developed for telecommunications and network applications like OC-48 and OC-192.

FEATURES

- Size: 7.0 x 5.0 x 1.8 (mm)
- Small package
- Tri-state enable / disable
- LVPECL compatible
- Tape and reel packaging
- I_R re-flow
- 2.5 V and 3.3 V input voltage
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

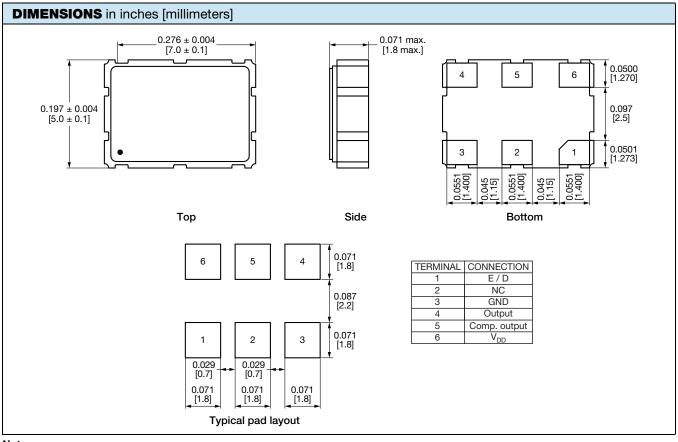


PARAMETER	SYMBOL	CONDITION	VALUE
Frequency range	F _O	-	77.76 MHz to 156.26 MHz
Frequency stability (1)		All conditions	± 25 ppm, ± 50 ppm, ± 100 ppm
Operating temperature range	T _{OPR}	-	0 °C to 70 °C
			-40 °C to +85 °C (option)
Storage temperature range	T _{STG}	-	-55 °C to +125 °C
Power supply voltage	V _{DD}	Select desired voltage	2.5 V ± 10 %
	V_{DD}		3.3 V ± 10 %
Aging (first year)		25 °C ± 3 °C	± 3 ppm
Supply current	I _{DD}	-	100 mA max.
Output symmetry	Sym	At ½ V _{DD}	45 % / 55 %
Rise time	t _r	10 % V _{DD} to 90 % V _{DD}	1 ns max.
Rise/fall time	t _f	90 % V _{DD} to 10 % V _{DD}	1 ns max.
Output voltage	V _{OH}	-	V _{DD} - 1.025 V min.
	V _{OL}	-	V _{DD} - 1.62 V max.
Output load	LVPECL	-	50 Ω to V _{DD} - 2.0 V
Start-up time	t _s	-	10 ms max.
Phase jitter	J	12 kHz to 20 MHz	< 1.0 pS _{RMS}
Pin 1, tri-state function			Pin 1 = H or open (output active at pin 4)
		-	Pin 1 = L (high impedance at pin 4)

Note

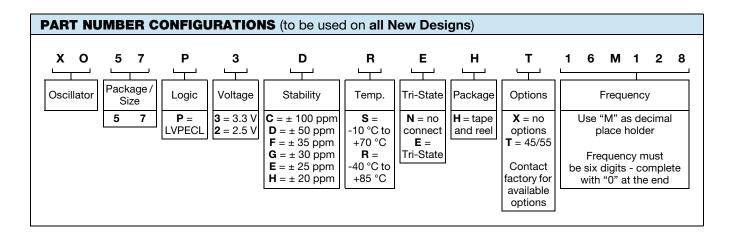
(1) Include: 25 °C tolerance, operating temperature range, input voltage change, aging, load change, shock and vibration





Note

A 0.01 μF bypass capacitor should be placed between V_{DD} (pin 4) and GND (pin 2) to minimize power supply line noise



PART MARKING		
Line 1:	V25.00 (frequency)	
Line 2:	YWWA (date code / factory)	



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