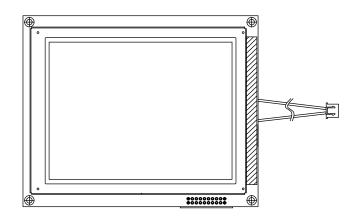




# 320 x 240 Graphic LCD



#### **FEATURES**

• Type: Graphic

Display format: 320 x 240 dotsBuilt-in controller: Epson S1D13700

Duty cycle: 1/240Built-in N.V.

• Touch screen option

• Temperature compensation option

• Compliant to RoHS directive 2002/95/EC



MECHANICAL DATA				
ITEM	STANDARD VALUE	UNIT		
Module Dimension	148.02 x 120.24			
Viewing Area	120.14 x 92.14			
Dot Size	0.34 x 0.34	mm		
Dot Pitch	0.36 x 0.36	mm		
Mounting Hole	139.98 x 112.2			
Character Size	N/a			

ABSOLUTE MAXIMUM RATINGS						
ITEM	SYMBOL	STAN	LINIT			
ITEM	STWIDOL	MIN.	TYP.	MAX.	UNIT	
Power Supply	$V_{DD}$ to $V_{SS}$	4.5	5.0	5.5	\/	
Input Voltage	VI	0	-	$V_{DD}$	V	

#### Note

•  $V_{SS} = 0 \text{ V}, V_{DD} = 5.0 \text{ V}$ 

ELECTRICAL CHARACTERISTICS							
ITEM	SYMBOL	CONDITION	STANDARD VALUE			UNIT	
IIEM	STWIBOL	CONDITION	MIN.	TYP.	MAX.	UNII	
Input Voltage	$V_{DD}$	-	4.75	5.0	5.25	V	
Supply Current	I <sub>DD</sub>	V <sub>DD</sub> = + 5.0 V	65.0	75.0	85.0	mA	
Recommended LC Driving Voltage for Normal Temperature Version Module		- 20 °C	-	-	24.4	V	
	$V_0$ to $V_{SS}$	25 °C	-	23.8	-		
		70 °C	23.4	-	-		
CCFL Starting Voltage	V <sub>FLS</sub>	25 °C	-	600	-	V <sub>RMS</sub>	
CCFL Driving Voltage	$V_{FLD}$	25 °C	-	270	-	V <sub>RMS</sub>	
CCFL Driving Current	I <sub>FLD</sub>	$V_{FQ} = 450 V_{RMS}$ , 30 kHz	4.8	5.3	5.5	mA <sub>RMS</sub>	
LED Forward Voltage	V <sub>F</sub>	25 °C	3.4	3.5	3.6	V	
LED Forward Current	I <sub>F</sub>	25 °C	140	160	200	mA	
EL Power Supply Current	I <sub>EF</sub>	V <sub>EL</sub> = 110 V <sub>AC</sub> , 400 Hz	-	-	5.0	mA	

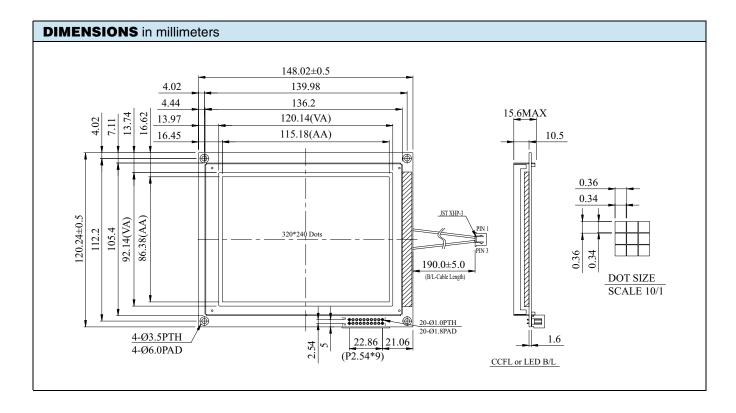
OPTIONS									
PROCESS COLOR						BACK	LIGHT		
TN	STN Gray	STN Yellow	STN Blue	FSTN B&W	STN Color	None	LED	EL	CCFL
	х	х	x	x		х	x	х	х

For detailed information, please see the "Product Numbering System" document.

### 320 x 240 Graphic LCD



INTERFACE PIN FUNCTION						
PIN NO.	SYMBOL	FUNCTION				
1	V <sub>SS</sub>	Ground				
2	$V_{DD}$	Power supply for logic				
3	V <sub>0</sub>	Driving voltage for LCD				
4	RD	8080 family: Read signal/6800 family: Enable clock				
5	WR	8080 family: Write signal/6800 family: R/W signal				
6	A <sub>0</sub>	RD = L, $WR = H$ ; $AO = L$ : Data read; $AO = H$ : Status read $RD = H$ , $WR = L$ ; $AO = L$ : Data write; $AO = H$ : Command write				
7	DB0	Date bus line				
8	DB1	Date bus line				
9	DB2	Date bus line				
10	DB3	Date bus line				
11	DB4	Date bus line				
12	DB5	Date bus line				
13	DB6	Date bus line				
14	DB7	Date bus line				
15	CS	Chip select, active L				
16	RES	Controller reset signal, active L				
17	V <sub>EE</sub>	Negative voltage output				
18	F <sub>GND</sub>	Frame ground				
19	DISPOFF	Display off				
20	WAIT	Check busy				





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