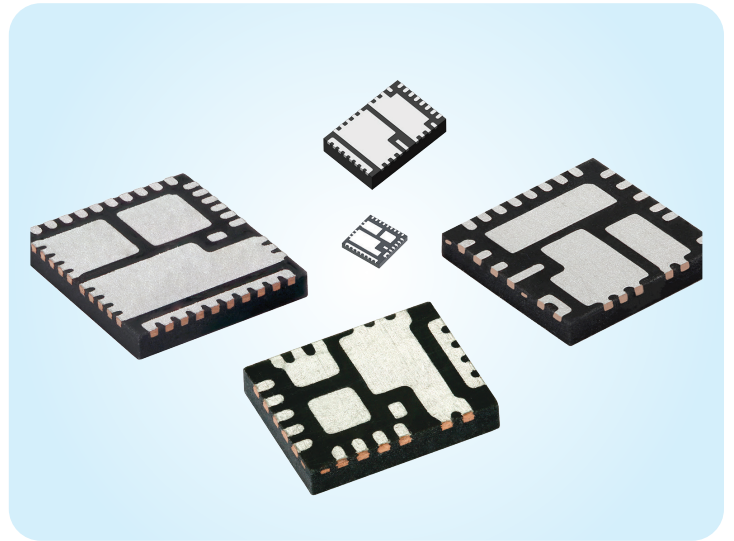


# INTEGRATED SPS AND DrMOS POWER STAGES Selector Guide

## FOR HIGH DENSITY, HIGH EFFICIENCY VOLTAGE REGULATOR DESIGNS

The Smart Power Stage (SPS) and DrMOS portfolios provide a comprehensive range of integrated power stages optimized for high density multiphase voltage regulation across computing, AI, telecom, and industrial applications.

SPS devices support **4.5 V to 16 V input**, deliver up to **100 A**, and feature  **$I_{MON}/T_{MON}$  telemetry, 3.3 V PWM compatibility**, and robust protections for precise, monitored power delivery. Complementing this lineup, the DrMOS family supports **2.5 V to 24 V input** with output currents up to **60 A**, offering fast switching performance, **3.3 V/5 V PWM support**, and efficient light load operation via **ZCD** or **PS4** modes. Together, these families provide scalable, footprint efficient solutions with integrated control, protection, and high efficiency operation, enabling reliable, compact VR designs across a wide range of power levels.





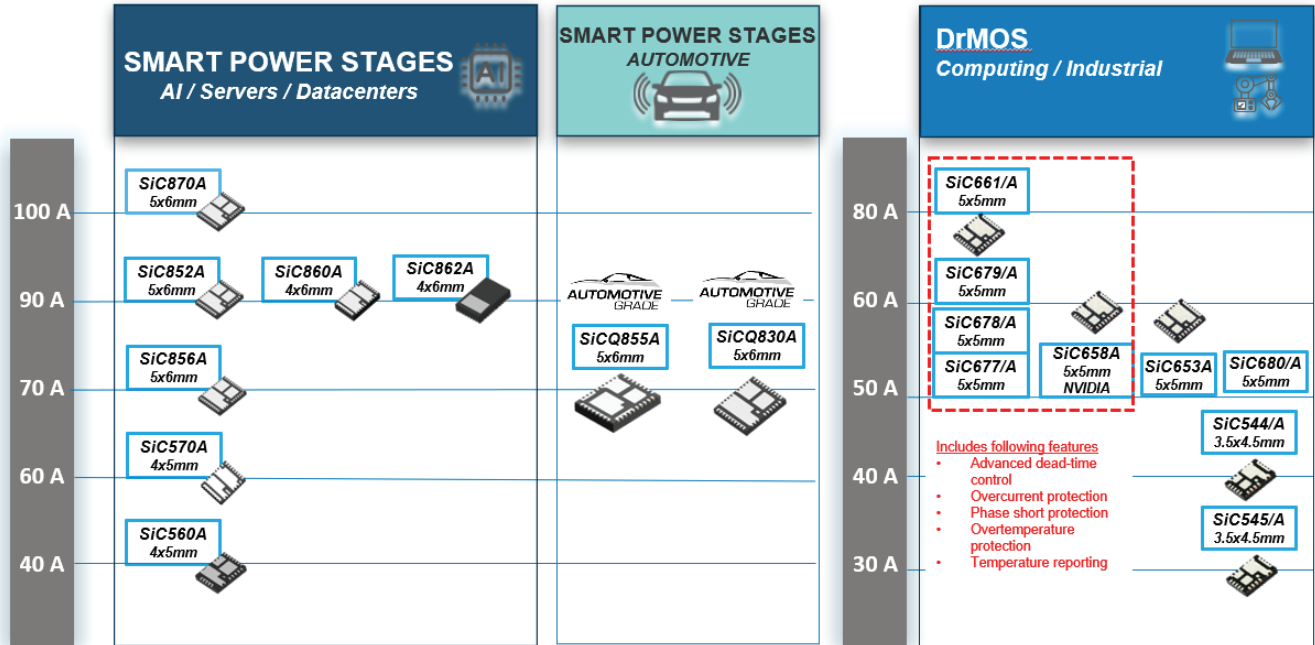
# INTEGRATED SPS AND DRMOS POWER STAGES

## Selector Guide

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### VISHAY POWER STAGE PORTFOLIO OVERVIEW

#### Current Capability of Vishay Smart Power Stages and DrMOS Devices



### Smart Power Stages (SPS)

Device	Package	V <sub>IN</sub>	Max. Current	PWM Level	Enable (Y/N)	Monitoring Features	I <sub>MON</sub> output	Protection
SiC870A	MLP 5x6	4.5 V to 16 V	100 A	3.3 V	Yes	I <sub>MON</sub> , T <sub>MON</sub>	Current mode	UVLO, OTP, OCP
SiC871A							Voltage mode	
SiC852A			90 A				Current mode	
SiC853A			Voltage mode					
SiC856A			70 A				Current mode	
SiC857A	Voltage mode							
SiC860A	MLP 4x6	90 A	Current mode					
SiC570A		60 A	Current mode					
SiC560A*	MLP 4x5	40 A	Current mode					

\*Contact [powerictechsupport@vishay.com](mailto:powerictechsupport@vishay.com) for product datasheet.



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### VRPOWER (DrMOS) Power Stages

Device	Package	V <sub>IN</sub>	Max. Current	PWM Level	Enable (Y/N)	Monitoring Features	Light-Load Modes	Protection
<a href="#">SIC679/SIC679A</a>	MLP 5x5	2.5 V to 16 V	60 A	5 V / 3.3 V	Yes	None	ZCD	UVLO, OTP, OCP
<a href="#">SIC678/SIC678A</a>		2.5 V to 24 V	55 A					
<a href="#">SIC677/SIC677A</a>			50 A	3.3 V				
<a href="#">SIC680/SIC680A</a>								
<a href="#">SiC658A</a>	MLP 4.5x3.5	4.5 V to 24 V	45 A	5 V / 3.3 V	No		ZCD and PS4	VDRV, UVLO, OTP, OCP, high-side short
<a href="#">SIC544/SIC544A</a>			35 A					
<a href="#">SIC545/SIC545A</a>								

### Automotive Power Stages

Device	Package	V <sub>IN</sub>	Max. Current	PWM Level	Enable (Y/N)	Monitoring Features	I <sub>MON</sub> output	Protection
<a href="#">SICQ830A</a>	MLP 5x6	4.5 V to 16 V	80 A	3.3 V	Yes	I <sub>MON</sub> , T <sub>MON</sub>	Current mode	UVLO, OTP, OCP
<a href="#">SICQ855A</a>		4.5 V to 21 V			Yes			
<a href="#">SICQ670</a>	MLP 5x5	2.5 V to 24 V	50 A	5 V	Yes	T <sub>MON</sub> /FAULT Monitor		UVLO, OTP, OCP, HSS