



# SiZF4800LDT 80 V Symmetric Dual MOSFETs

## Best in Class $R_{DS(on)}$ in PowerPAIR® 3 x 3FS

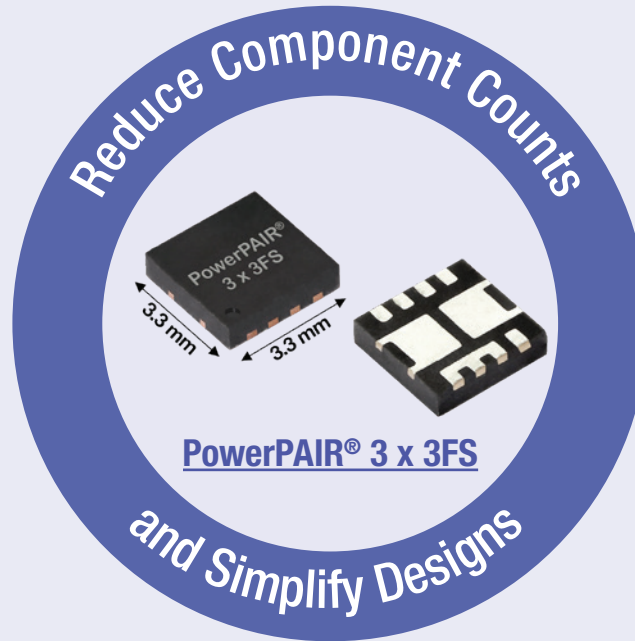
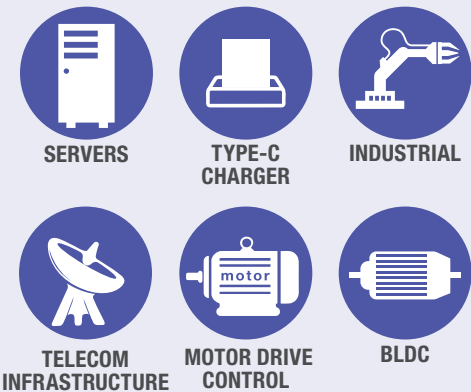
### KEY FEATURES

- Gen IV MOSFETs integrated in a single 3.3 mm x 3.3 mm package
- Low  $R_{DS(on)}$  to 18.5 mΩ
- Ultra low  $R_{DS} - Q_g$  FOM
- Logic level input suitable for most driver solutions
- Flip-chip technology enhances thermal dissipation
- Optimal 50 % duty cycle design

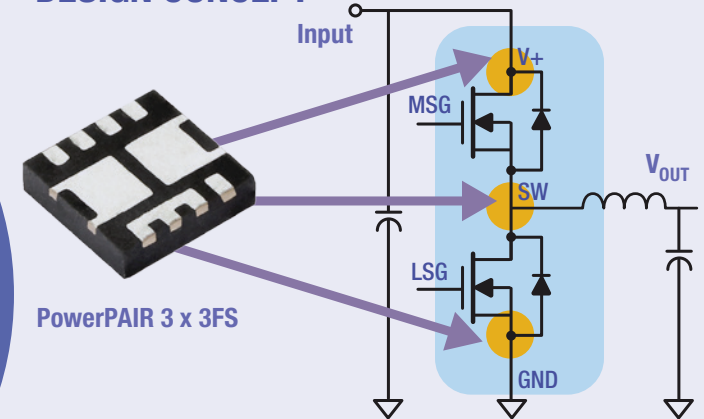
### CIRCUIT APPLICATIONS

- Synchronous buck DC/DC
- Buck-boost DC/DC
- Half-bridge topologies
- Point of load (POL) conversion

### END PRODUCTS



### DESIGN CONCEPT



### EFFICIENCY AND THERMAL PERFORMANCE

The high and low side MOSFETs of the SiZF4800LDT form an optimized combination for 50 % duty cycles and best in class efficiency, in particular from 1 A to 5 A, making this device suitable for high switching frequencies and delivering high efficiency up to 97 %.

### COMPARISON TO DISCRETE PowerPAK 1212 SOLUTION

Package size	50 % ↓
FOM channel	18 % ↓
Efficiency at 20 % load	0.62 % ↑
Efficiency at 100 % load	0.2 % ↑

