

## **Very Fast Acting Thin Film Chip Fuses**



#### **FEATURES**

- 3 sizes: 0402, 0603, and 1206
- Maximal protection as fuses are designed to open in t < 5 s at 200 % overload</li>



- UL 248-14 compliant
- From 0.5 A to 7 A
- Low resistance
- Body temperature rising < 75 °C at 100 % rated current</li>
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

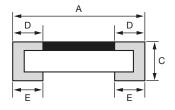
### **LINKS TO ADDITIONAL RESOURCES**



S3F thin film chip fuses are designed to ensure circuit continuity with minimal resistance and reliable interruption under overload condition. Particularly adapted to be compatible with numerous applications in the sector of electronics. They comply with industrial and government standards, as well as Vishay quality and reliability requirements.

TECHNICAL SPECIFICATIONS							
DESCRIPTION	S3F0402	S3F0603	S3F1206				
Imperial size	0402	0603	1206				
Rated current I <sub>N</sub> range	0.50 A to 4.00 A	0.50 A to 5.00 A	0.50 A to 7.00 A				
Rated voltage U <sub>max.</sub> DC	32 V	32 V and 50 V	32 V and 63 V				
Cold resistance at <i>I</i> ≤ 0.1 x <i>I</i> <sub>N</sub>	16 mΩ to 380 mΩ	10 m $\Omega$ to 260 m $\Omega$	9 m $\Omega$ to 527.5 m $\Omega$				
Fusing time	Op	en within 5 s at 200 % rated po	wer				
Body temperature rise	< 75 °C at 100 % rated current						
Operating temperature range	-25 °C to 125 °C with proper derating factor						
Approved UL 248-14 recognition file		E548286					

### **DIMENSIONS**





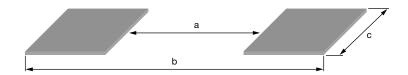
DIMENSIONS AND TOLERANCES in millimeters (inches)								
TYPE	Α	В	С	D	E			
S3F0402	1.00 ± 0.10	$0.52 \pm 0.05$	0.35 ± 0.05	0.20 ± 0.10	0.25 ± 0.10			
53F0402	$(0.039 \pm 0.004)$	$(0.020 \pm 0.002)$	$(0.014 \pm 0.002)$	$(0.008 \pm 0.004)$	$(0.010 \pm 0.004)$			
S3F0603	1.60 ± 0.10	$0.80 \pm 0.10$	0.45 ± 0.10	$0.30 \pm 0.20$	$0.35 \pm 0.20$			
331 0003	$(0.063 \pm 0.004)$	$(0.031 \pm 0.004)$	$(0.018 \pm 0.004)$	$(0.012 \pm 0.008)$	$(0.014 \pm 0.008)$			
S3F1206	3.10 ± 0.10	1.55 ± 0.10	0.60 ± 0.10	$0.50 \pm 0.30$	0.50 ± 0.20			
3371200	$(0.122 \pm 0.004)$	$(0.061 \pm 0.004)$	$(0.024 \pm 0.004)$	$(0.020 \pm 0.012)$	$(0.020 \pm 0.008)$			



# Vishay Sfernice

MECHANICAL SPECIFICATIONS				
Substrate	Ceramic			
Technology	Thin film			
Termination	Cu / Ni plating			

### **LAND PATTERN DIMENSIONS**

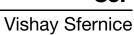


SUGGESTED LAND PATTERN DIMENSIONS in millimeters (inches)									
TYPE	TYPE a b c								
S3F0402	0.55 to 0.65	1.40 to 1.60	0.74 to 0.94						
	(0.022 to 0.026)	(0.055 to 0.063)	(0.029 to 0.037)						
S3F0603	0.85 to 0.95	2.00 to 2.20	1.50 to 1.70						
	(0.033 to 0.037)	(0.079 to 0.087)	(0.059 to 0.067)						
S3F1206	0.95 to 1.05	4.40 to 5.00	2.30 to 2.50						
	(0.037 to 0.041)	(0.173 to 0.197)	(0.091 to 0.098)						

S3F0402 RATII	S3F0402 RATING							
PART DESIGNATION	MARKING	RATED CURRENT (A)	FUSING TIME	RESISTANCE <sup>(1)</sup> (m $\Omega$ ), TOLERANCE: ± 25 %	RATED VOLTAGE	BREAKING CAPACITY	BODY TEMPERATURE RISING	
S3F040232VA500TT	F	0.50		380				
S3F040232VA800TT	K	0.80		120			1	
S3F040232V1A00TT	L	1.00	Open within	95				
S3F040232V1A25TT	<u>M</u>	1.25		67				
S3F040232V1A50TT	Р	1.50		Open within	51			< 75 °C
S3F040232V1A60TT	N	1.60	5 s at 200 %	46	32 V <sub>DC</sub>	32 V <sub>DC</sub> , 35 A	at 100 %	
S3F040232V2A00TT	S	2.00	rated current	33			rated current	
S3F040232V2A50TT	Т	2.50		22.5				
S3F040232V3A00TT	3	3.00		20				
S3F040232V3A15TT	U	3.15		19				
S3F040232V4A00TT	W	4.00		16				

### Note

<sup>(1)</sup> Resistance value was measured with less than 10 % of rated current





S3F0603 RATING							
PART DESIGNATION	MARKING	RATED CURRENT (A)	FUSING TIME	RESISTANCE <sup>(1)</sup> (m $\Omega$ ), TOLERANCE: ± 25 %	RATED VOLTAGE	BREAKING CAPACITY	BODY TEMPERATURE RISING
S3F060350VA500TT	F	0.50		260	50 V <sub>DC</sub>	50 V <sub>DC</sub> , 50 A	
S3F060332VA630TT	I	0.63		218			
S3F060332VA800TT	K	0.80		132	•		
S3F060332V1A00TT	L	1.00		84.5	1		
S3F060332V1A25TT	<u>M</u>	1.25		63	1		
S3F060332V1A50TT	Р	1.50	Open within	50.5	1		< 75 °C
S3F060332V1A60TT	N	1.60	5 s at 200 %	46	00.1/	32 V <sub>AC/DC</sub> ,	at 100 %
S3F060332V2A00TT	S	2.00	rated current	32	32 V <sub>AC/DC</sub>	50 Å	rated current
S3F060332V2A50TT	Т	2.50		25.5			
S3F060332V3A00TT	3	3.00		20			
S3F060332V3A15TT	U	3.15		19	1		
S3F060332V4A00TT	W	4.00		13	1		
S3F060332V5A00TT	Y	5.00		10	]		

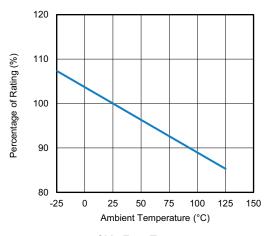
S3F1206 RATING							
PART DESIGNATION	MARKING	RATED CURRENT (A)	FUSING TIME	RESISTANCE <sup>(1)</sup> (m $\Omega$ ), TOLERANCE: ± 25 %	RATED VOLTAGE	BREAKING CAPACITY	BODY TEMPERATURE RISING
S3F120663VA500TT	F	0.50		527.5			
S3F120663VA800TT	K	0.80		211	63 V <sub>DC</sub>	63 V <sub>DC</sub> , 50 A	< 75 °C
S3F120663V1A00TT	L	1.00		145.5			
S3F120663V1A25TT	M	1.25		90	03 VDC		
S3F120663V1A50TT	Р	1.50	Open within	83	ļ		
S3F120663V2A00TT	S	2.00	5 s at 200 %	57			at 100 %
S3F120632V2A50TT	T	2.50	rated current	37			rated current
S3F120632V3A00TT	3	3.00		26			
S3F120632V4A00TT	W	4.00		18	32 V <sub>DC</sub>	32 V <sub>DC</sub> , 50 A	
S3F120632V5A00TT	Y	5.00		13			
S3F120632V7A00TT	Z	7.00	]	9			

### Note

### **TEMPERATURE DERATING**

Normal ambient temperature: 25 °C

Operating temperature range: -25 °C to 125 °C, with proper derating factor as below:



 $<sup>^{(1)}\,</sup>$  Resistance value was measured with less than 10 % of rated current



### Vishay Sfernice

PERFORMANCES						
TESTS	CONDITIONS	REQUIREMENTS PERMISSIBLE CHANGES				
Carrying capacity	Rated current, 4 h	No fusing				
Fusing time	200 % of its rated current	Within 5 s				
Interrupting ability	After the fuse is interrupted, rated voltage applied for 30 s again	No mechanical damages				
Bending test	Distance between holding points: 90 mm Bending: 3 mm, 1 time, 30 s	No mechanical damages				
Resistance to solder heat	260 °C ± 5 °C, 10 s ± 1 s	± 20 %				
Solderability	235 °C ± 5 °C, 2 s ± 0.5 s 245 °C ± 5 °C, 2 s ± 0.5 s (lead (Pb)-free)	95 % coverage minimum				
Temperature rise	100 % of its rated current, measurement of surface temperature	< 75 °C				
Resistance to dry heat	105 °C ± 5 °C, 1000 h	± 20 %				
Resistance to solvent	23 °C ± 5 °C in isopropyl alcohol, 90 s	No evident damages on protective coating and marking				
Residual resistance	DC measurement after fusing	10 kΩ and more				
Thermal shock	-25 °C / 25 °C / 125 °C / 25 °C, 10 cycles	$\frac{\Delta R}{R}$ < 10 %				

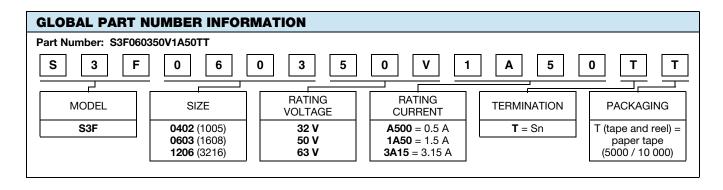
PACKAGING				
SIZE	MOQ	TAPE WIDTH	TAPE PITCH	PACKAGING DIMENSIONS
0402	10 000		2 mm	Ø 180 mm / 7"
0603	5000	8 mm	4 mm	Ø 180 mm / 7"
1206	5000		4 mm	Ø 180 mm / 7"

#### Note

• Packaging is compliant to EIA-481 Rev. D (IEC 60286, part 3) standard

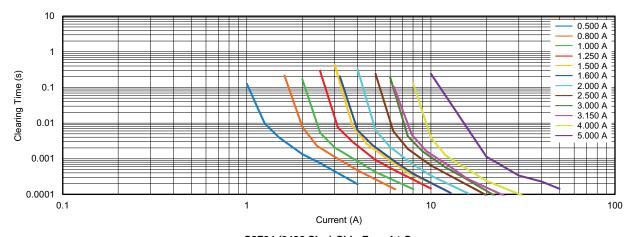
### **SOLDERING / MOUNTING PARAMETERS**

Please see the application note (<u>www.vishay.com/doc?52029</u>). The recommended reflow solder process parameters are displayed in pages 4 and 5.

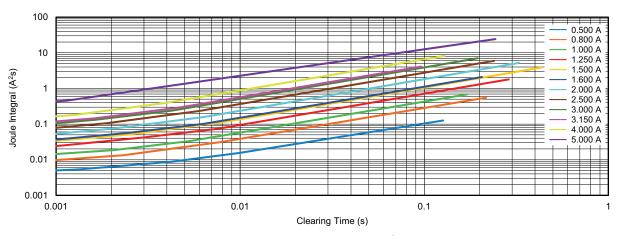




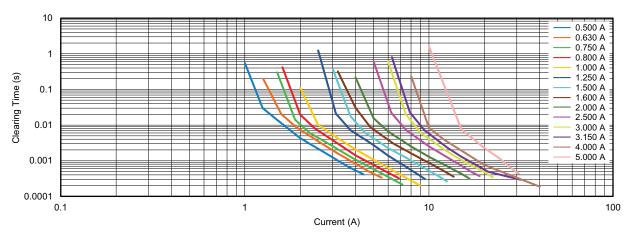
### **FUNCTIONAL PERFORMANCE**



S3F04 (0402 Size) Chip Fuse I-t Curve

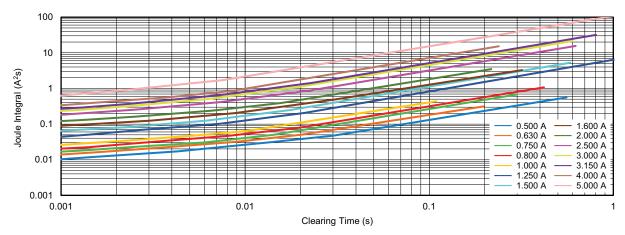


S3F04 (0402 Size) Chip Fuse t-I2t Curve

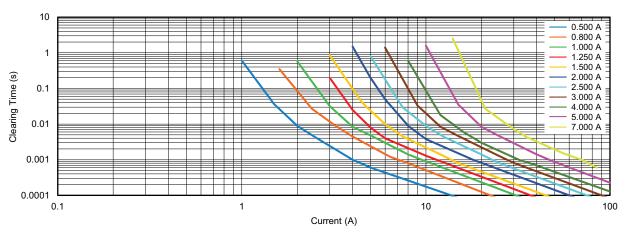


S3F06 (0603 Size) Chip Fuse I-t Curve

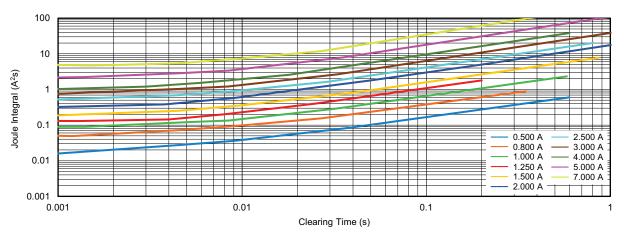




S3F06 (0603 Size) Chip Fuse t-I2t Curve



S3F12 (1206 Size) Chip Fuse I-t Curve



S3F12 (1206 Size) Chip Fuse t-I2t Curve

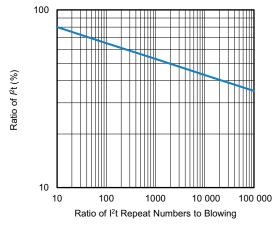
### www.vishay.com

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Typical Joule integral values  $(\int_{t=0}^{t} i^2 . dt)$ , measured at 10 times rated current, are displayed in the following table:

S3F FUSE I2t (A2s)					
PART NUMBER	TYPICAL I <sup>2</sup> t (A <sup>2</sup> s)	PART NUMBER	TYPICAL I <sup>2</sup> t (A <sup>2</sup> s)	PART NUMBER	TYPICAL I <sup>2</sup> t (A <sup>2</sup> s)
S3F040232VA500TT	0.00317	S3F060350VA500TT	0.0088	S3F120663VA500TT	0.015
S3F040232VA800TT	0.00532	S3F060332VA630TT	0.0125	S3F120663VA800TT	0.040
S3F040232V1A00TT	0.00724	S3F060332VA800TT	0.0206	S3F120663V1A00TT	0.062
S3F040232V1A25TT	0.01344	S3F060332V1A00TT	0.0211	S3F120663V1A25TT	0.094
S3F040232V1A50TT	0.01356	S3F060332V1A25TT	0.0472	S3F120663V1A50TT	0.141
S3F040232V1A60TT	0.01672	S3F060332V1A50TT	0.0623	S3F120663V2A00TT	0.285
S3F040232V2A00TT	0.01983	S3F060332V1A60TT	0.0817	S3F120632V2A50TT	0.445
S3F040232V2A50TT	0.03763	S3F060332V2A00TT	0.1176	S3F120632V3A00TT	0.639
S3F040232V3A00TT	0.05427	S3F060332V2A50TT	0.1807	S3F120632V4A00TT	1.109
S3F040232V3A15TT	0.06304	S3F060332V3A00TT	0.3177	S3F120632V5A00TT	1.710
S3F040232V4A00TT	0.08960	S3F060332V3A15TT	0.3615	S3F120632V7A00TT	3.300
		S3F060332V4A00TT	0.5348		
		S3F060332V5A00TT	0.7726		

Furthermore, for repeated numbers of inrush current, another specific derating must be applied:



**Chip Fuse Clearing by Repeated Rush Current** 



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