High Temperature (230 °C) Zero Ohm Jumper
SMD Thin Film Chip Resistor (Strap)

FEATURES
- Zero Ohm jumper
- SMD wraparound
- Sizes available: 02016 to 2512
- Thin film technology
- Terminations: Gold or tin/silver
- Resistance value < 30 mΩ
- Maximum current through resistor: 0.5 A to 6.3 A
- Temperature range: -55 °C to +230 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

STANDARD ELECTRICAL SPECIFICATIONS

<table>
<thead>
<tr>
<th>MODEL</th>
<th>SIZE</th>
<th>RESISTANCE RANGE Ω</th>
<th>RATED POWER W AT 215°C</th>
<th>MAXIMUM CURRENT A AT 215°C</th>
</tr>
</thead>
<tbody>
<tr>
<td>PZHT02016</td>
<td>02016</td>
<td>0 (max. 0.02)</td>
<td>0.002</td>
<td>0.28</td>
</tr>
<tr>
<td>PZHT0402</td>
<td>0402</td>
<td>0 (max. 0.02)</td>
<td>0.004</td>
<td>0.4</td>
</tr>
<tr>
<td>PZHT0603</td>
<td>0603</td>
<td>0 (max. 0.02)</td>
<td>0.008</td>
<td>0.56</td>
</tr>
<tr>
<td>PZHT0805</td>
<td>0805</td>
<td>0 (max. 0.02)</td>
<td>0.015</td>
<td>0.77</td>
</tr>
<tr>
<td>PZHT1206</td>
<td>1206</td>
<td>0 (max. 0.025)</td>
<td>0.025</td>
<td>1</td>
</tr>
<tr>
<td>PZHT2010</td>
<td>2010</td>
<td>0 (max. 0.025)</td>
<td>0.08</td>
<td>1.78</td>
</tr>
<tr>
<td>PZHT2512</td>
<td>2512</td>
<td>0 (max. 0.025)</td>
<td>0.1</td>
<td>2</td>
</tr>
</tbody>
</table>

CLIMATIC SPECIFICATIONS

| Operating temperature range | -55 °C; +215 °C |
| Storage temperature range   | -55 °C; +230 °C |

MECHANICAL SPECIFICATIONS

<table>
<thead>
<tr>
<th>Substrate</th>
<th>Alumina</th>
</tr>
</thead>
<tbody>
<tr>
<td>Technology</td>
<td>Thin film</td>
</tr>
<tr>
<td>Film</td>
<td>Au or SnAg over nickel barrier</td>
</tr>
</tbody>
</table>

Terminations (1)
- **G type**: Au for HMP (high melting point) reflow process over nickel barrier
- **P type**: Au one face
- **N type**: SnAg for solder reflow over nickel barrier
- **F type**: SnAg one face

Note
(1) SnAg for temperatures up to 200 °C - Au for temperatures up to 230 °C

More and more, customers use SMD straps (0 Ω) to able or disable a function on their PCB. Vishay Sfernice offers straps in a wide range of standard dimensions: From 02016 to 2512. For applications requiring high temperature withstanding (up to 230 °C).
**DIMENSIONS in millimeters**

<table>
<thead>
<tr>
<th>TERMINATIONS</th>
<th>One face Wraparound</th>
<th>One face Wraparound</th>
<th>One face Wraparound</th>
<th>One face Wraparound</th>
</tr>
</thead>
<tbody>
<tr>
<td>MODEL</td>
<td>L</td>
<td>W</td>
<td>H</td>
<td>D</td>
</tr>
<tr>
<td>PZHT02016</td>
<td>0.48 (± 0.1)</td>
<td>0.39 (± 0.1)</td>
<td>0.42 (± 0.07)</td>
<td>n/a</td>
</tr>
<tr>
<td>PZHT0402</td>
<td>1 ± 0.152</td>
<td>0.6 ± 0.127</td>
<td>0.5 ± 0.127</td>
<td>n/a</td>
</tr>
<tr>
<td>PZHT0605</td>
<td>1.52 ± 0.152</td>
<td>0.85 ± 0.127</td>
<td>0.5 ± 0.127</td>
<td>n/a</td>
</tr>
<tr>
<td>PZHT0805</td>
<td>1.91 ± 0.152</td>
<td>1.27 ± 0.127</td>
<td>0.5 ± 0.127</td>
<td>n/a</td>
</tr>
<tr>
<td>PZHT1206</td>
<td>3.09 ± 0.152</td>
<td>1.6 ± 0.127</td>
<td>0.5 ± 0.127</td>
<td>n/a</td>
</tr>
<tr>
<td>PZHT2010</td>
<td>5.08 ± 0.152</td>
<td>2.54 ± 0.127</td>
<td>0.5 ± 0.127</td>
<td>n/a</td>
</tr>
<tr>
<td>PZHT2512</td>
<td>6.35 ± 0.152</td>
<td>3.3 ± 0.127</td>
<td>0.5 ± 0.127</td>
<td>n/a</td>
</tr>
</tbody>
</table>

**SUGGESTED LAND PATTERN in millimeters**

<table>
<thead>
<tr>
<th>TERMINATIONS</th>
<th>One face Wraparound</th>
<th>One face Wraparound</th>
<th>One face Wraparound</th>
<th>One face Wraparound</th>
</tr>
</thead>
<tbody>
<tr>
<td>MODEL</td>
<td>Z_{max.}</td>
<td>G_{min.}</td>
<td>X_{max.}</td>
<td></td>
</tr>
<tr>
<td>PZHT02016</td>
<td>0.55</td>
<td>n/a</td>
<td>0.15</td>
<td></td>
</tr>
<tr>
<td>PZHT0402</td>
<td>1.15</td>
<td>1.55</td>
<td>0.15</td>
<td></td>
</tr>
<tr>
<td>PZHT0603</td>
<td>1.67</td>
<td>2.37</td>
<td>0.35</td>
<td></td>
</tr>
<tr>
<td>PZHT0805</td>
<td>2.06</td>
<td>2.76</td>
<td>0.74</td>
<td></td>
</tr>
<tr>
<td>PZHT1206</td>
<td>3.21</td>
<td>3.91</td>
<td>1.85</td>
<td></td>
</tr>
<tr>
<td>PZHT2010</td>
<td>5.23</td>
<td>5.93</td>
<td>3.71</td>
<td></td>
</tr>
<tr>
<td>PZHT2512</td>
<td>6.5</td>
<td>7.20</td>
<td>4.91</td>
<td></td>
</tr>
</tbody>
</table>

**POWER DERATING CURVE**

![Power Derating Curve](chart.png)

**CURRENT DERATED CURVE**

![Current Derated Curve](chart.png)
PACKAGING

Antistatic packaging: waffle pack or paper tape or low conductivity plastic tape. PZHT02016 only available in plastic tape.

### PACKAGING RULES

**Waffle Pack**

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered exceeds maximum quantity of a single waffle pack, the waffle packs are stacked up on the top of each other and closed by one single cover.

To get “not stacked up” waffle pack in case of ordered quantity > maximum number of pieces per package: Please consult Vishay Sfernice for specific ordering code.

**Tape and Reel**

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered is between the MOQ and the maximum reel capacity, only one reel is provided. When several reels are needed for ordered quantity within MOQ and maximum reel capacity: Please consult Vishay Sfernice for specific ordering code.

Parts are packed: active face down.

### GLOBAL PART NUMBER INFORMATION

Global Part Numbering: PZHT1206-0R00NT

<table>
<thead>
<tr>
<th>GLOBAL MODEL</th>
<th>SIZE</th>
<th>OHMIC VALUE</th>
<th>TERMINATION (1)(2)</th>
<th>PACKAGING</th>
</tr>
</thead>
<tbody>
<tr>
<td>PZHT</td>
<td>02016</td>
<td>0 Ω</td>
<td>P = Au (one face)</td>
<td>For more information see “Codification of packaging” table</td>
</tr>
<tr>
<td></td>
<td>0402</td>
<td></td>
<td>F = tin / silver (one face)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0603</td>
<td></td>
<td>N = tin / silver (wraparound)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0805</td>
<td></td>
<td>G = Au (wraparound)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1206</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2010</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2512</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Notes**

(1) PZHT02016: one face termination only
(2) P and G: for temperatures up to 230°C
F and N: for temperatures up to 200°C

### CODIFICATION OF PACKAGING

<table>
<thead>
<tr>
<th>CODE 18</th>
<th>PACKAGING</th>
</tr>
</thead>
<tbody>
<tr>
<td>WAFFLE PACK (not available for 02016)</td>
<td></td>
</tr>
<tr>
<td>W</td>
<td>100 min., 1 mult</td>
</tr>
<tr>
<td>WA</td>
<td>100 min., 100 mult (available only in size 1206)</td>
</tr>
<tr>
<td>PLASTIC TAPE (standard for all sizes)</td>
<td></td>
</tr>
<tr>
<td>T</td>
<td>100 min., 1 mult</td>
</tr>
<tr>
<td>TA</td>
<td>100 min., 100 mult</td>
</tr>
<tr>
<td>TB</td>
<td>250 min., 250 mult</td>
</tr>
<tr>
<td>TC</td>
<td>500 min., 500 mult</td>
</tr>
<tr>
<td>TD</td>
<td>1000 min., 1000 mult</td>
</tr>
<tr>
<td>TE</td>
<td>2500 min., 2500 mult</td>
</tr>
<tr>
<td>TF</td>
<td>Full tape (quantity depending on size of chips)</td>
</tr>
<tr>
<td>PAPER TAPE (Available for 0402, 0603, 0805 and 1206. Please consult Vishay Sfernice for other sizes.)</td>
<td></td>
</tr>
<tr>
<td>PT</td>
<td>100 min., 1 mult</td>
</tr>
<tr>
<td>PA</td>
<td>100 min., 100 mult</td>
</tr>
<tr>
<td>PB</td>
<td>250 min., 250 mult</td>
</tr>
<tr>
<td>PC</td>
<td>500 min., 500 mult</td>
</tr>
<tr>
<td>PD (not available for size 0402)</td>
<td>1000 min., 1000 mult</td>
</tr>
<tr>
<td>PE (not available for size 0402)</td>
<td>2500 min., 2500 mult</td>
</tr>
<tr>
<td>PF (not available for size 0402)</td>
<td>Full tape (quantity depending on size of chips)</td>
</tr>
</tbody>
</table>
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