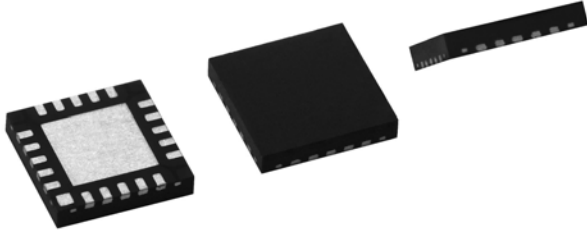


Quad Flat No Lead Molded Precision Thin Film Resistor, Surface Mount Network



The QFN- series features a standard 20 pins quad flat no lead 5 mm x 5 mm 0.65 mm pitch package. The quad flat no lead package saves board space over traditional SOIC packages. Additional pin counts available, consult factory.

FEATURES

- 0.65 mm lead pitch
- MSL level 1 per J-STD-020
- Low profile 1 mm seated height
- Small size 5 mm x 5 mm
- Low TCR ± 25 ppm, TCR tracking to ± 5 ppm
- Compliant to RoHS Directive 2002/95/EC

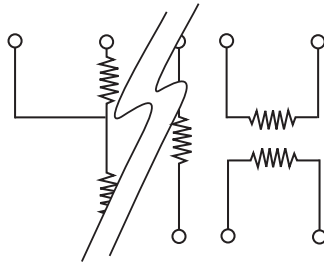


RoHS
COMPLIANT

TYPICAL PERFORMANCE

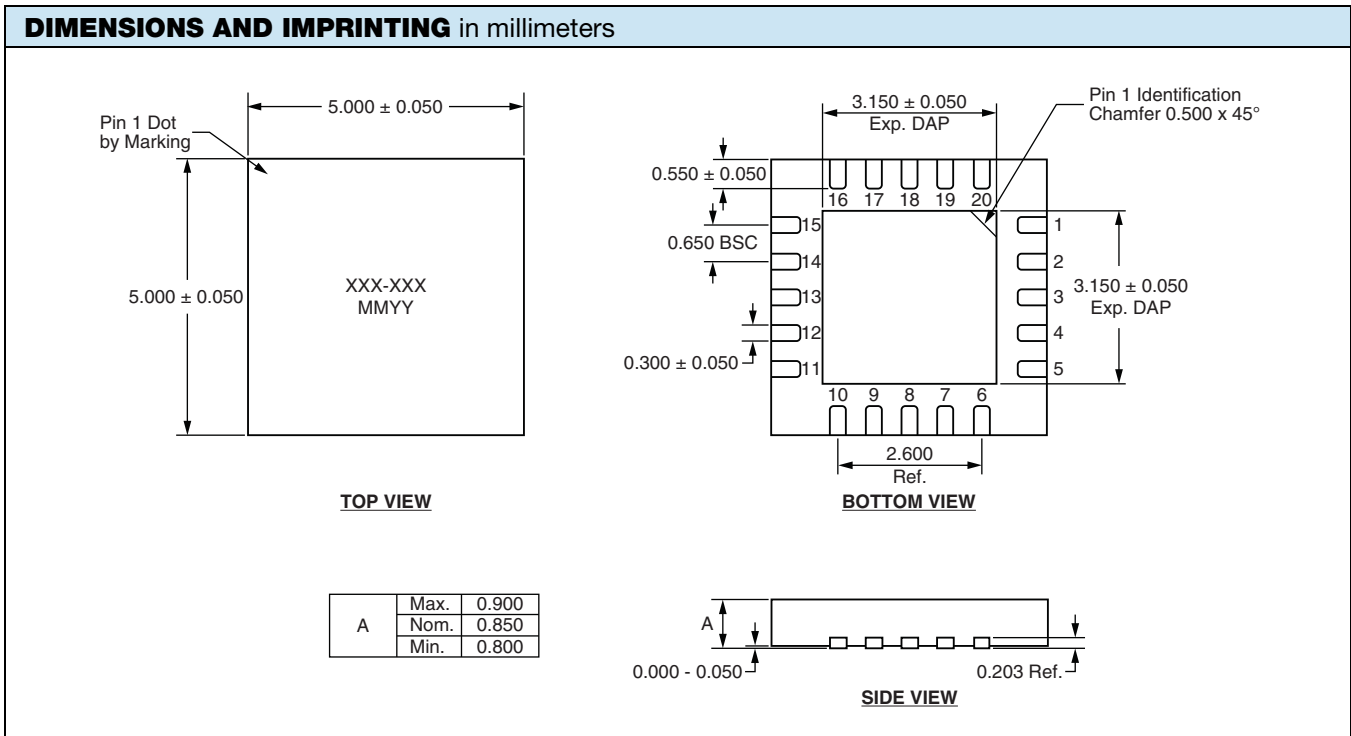
	ABSOLUTE	TRACKING
TCR	25	5
	ABSOLUTE	RATIO
TOL.	0.1	0.05

SCHEMATIC



Custom schematics available
Please consult factory

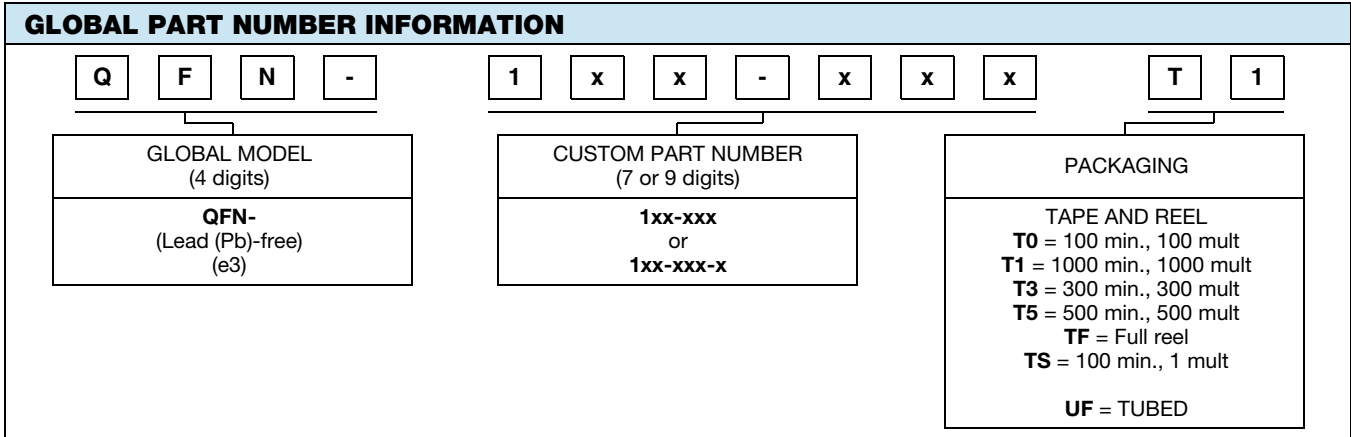
STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	-
Pin/Lead Number	20	-
Resistance Range	100 Ω (resistor) to 500 k Ω (total)	-
TCR: Absolute	± 25 ppm/ $^{\circ}\text{C}$ to ± 100 ppm/ $^{\circ}\text{C}$	- 55 $^{\circ}\text{C}$ to + 125 $^{\circ}\text{C}$
TCR: Tracking	± 5 ppm/ $^{\circ}\text{C}$ (typical)	- 55 $^{\circ}\text{C}$ to + 125 $^{\circ}\text{C}$
Tolerance: Absolute	± 0.1 % to ± 1.0 %	+ 25 $^{\circ}\text{C}$
Tolerance: Ratio	± 0.05 % to ± 0.1 %	+ 25 $^{\circ}\text{C}$
Power Rating: Resistor	100 mW (per element)	Maximum at + 70 $^{\circ}\text{C}$
Power Rating: Package	500 mW	Maximum at + 70 $^{\circ}\text{C}$
Stability: Absolute	$\Delta R \pm 0.05$ %	2000 h at + 70 $^{\circ}\text{C}$
Stability: Ratio	$\Delta R \pm 0.015$ %	2000 h at + 70 $^{\circ}\text{C}$
Voltage Coefficient	0.1 ppm/V	-
Working Voltage	100 V max. not to exceed $\sqrt{P \times R}$	-
Operating Temperature Range	- 55 $^{\circ}\text{C}$ to + 125 $^{\circ}\text{C}$	-
Storage Temperature Range	- 55 $^{\circ}\text{C}$ to + 150 $^{\circ}\text{C}$	-
Noise	< - 30 dB	-
Thermal EMF	0.08 $\mu\text{V}/^{\circ}\text{C}$	-
Shelf Life Stability: Absolute	$\Delta R \pm 0.01$ %	1 year at + 25 $^{\circ}\text{C}$
Shelf Life Stability: Ratio	$\Delta R \pm 0.002$ %	1 year at + 25 $^{\circ}\text{C}$


Note

- Contact factory for package outlines for higher pin count or custom configurations

MECHANICAL SPECIFICATIONS	
Resistive Element	Passivated nichrome
Substrate Material	Silicon
Body	Molded epoxy
Terminals	Copper alloy
Plating	100 % matte tin
Marking Resistance to Solvents	Per MIL-PRF-914

ORDERING INFORMATION CHECK LIST (Customs)	
Special requirements should be identified in advance, but as a minimum, you should have the following information ready.	
ELECTRICAL	MECHANICAL
<ol style="list-style-type: none"> 1. Resistors, by value and tolerance 2. Reference resistor(s) and matching of which resistors to which reference resistors 3. Reference by ratio 4. Absolute temperature coefficient of resistivity 5. Temperature tracking of subordinate resistors to reference resistor(s) 6. Maximum operating voltage 7. Resistor power ratings 8. Operating temperature range 	<ol style="list-style-type: none"> 1. Maximum allowable seated height (from PC board to top of network) 2. Special marking concerns 3. Schematic pin out of package





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