STANDARD ELECTRICAL SPECIFICATIONS			
TEST	SPECIFICATIONS	CONDITIONS	
Material	Passivated nichrome	-	
Pin/Lead Number	20	-	
Resistance Range	100 Ω (resistor) to 500 k Ω (total)	-	
TCR: Absolute	± 25 ppm/°C to ± 100 ppm/°C	- 55 °C to + 125 °C	
TCR: Tracking	± 5 ppm/°C (typical)	- 55 °C to + 125 °C	
Tolerance: Absolute	± 0.1 % to ± 1.0 %	+ 25 °C	
Tolerance: Ratio	\pm 0.05 % to \pm 0.1 %	+ 25 °C	
Power Rating: Resistor	100 mW (per element)	Maximum at + 70 °C	
Power Rating: Package	500 mW	Maximum at + 70 °C	
Stability: Absolute	$\Delta R \pm 0.05 \%$	2000 h at + 70 °C	
Stability: Ratio	$\Delta R \pm 0.015$ %	2000 h at + 70 °C	
Voltage Coefficient	0.1 ppm/V	-	
Working Voltage	100 V max. not to exceed $\sqrt{P \times R}$	-	
Operating Temperature Range	- 55 °C to + 125 °C	-	
Storage Temperature Range	- 55 °C to + 150 °C	-	

SCHEMATIC

Noise

www.vishay.com

The QFN- series features a standard 20 pins guad flat no lead 5 mm x 5 mm 0.65 mm pitch package. The quad flat no lead package saves board space over traditional SOIC packages. Additional pin counts available, consult factory.

FEATURES

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- 0.65 mm lead pitch
- MSL level 1 per J-STD-020
- · Low profile 1 mm seated height
- Small size 5 mm x 5 mm
- Low TCR ± 25 ppm, TCR tracking to ± 5 ppm

Custom schematics available Please consult factory

Compliant to RoHS Directive 2002/95/EC

TYPICAL PERFORMANCE

\bullet	ABSOLUTE	TRACKING
TCR	25	5
	ABSOLUTE	RATIO
TOL.	0.1	0.05

Shelf Life Stability: Ratio

Shelf Life Stability: Absolute

Thermal EMF

ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

< - 30 dB

0.08 µV/°C

 $\Delta R \pm 0.01 \%$

 $\Delta R \pm 0.002 \%$

Document Number: 60015

-

1 year at + 25 °C

1 year at + 25 °C



COMPLIANT

Quad Flat No Lead Molded Precision Thin Film Resistor, Surface Mount Network

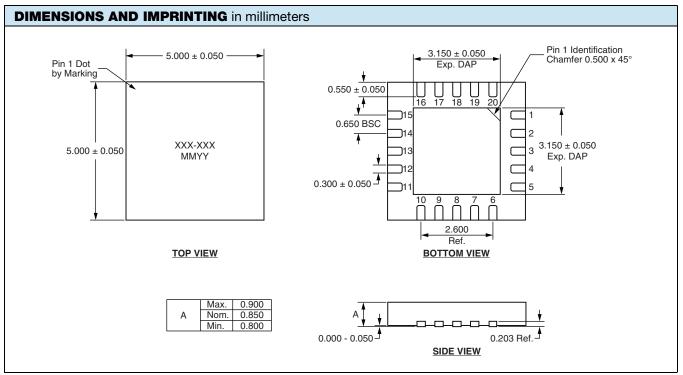
QFN-

Vishay Dale Thin Film





Vishay Dale Thin Film

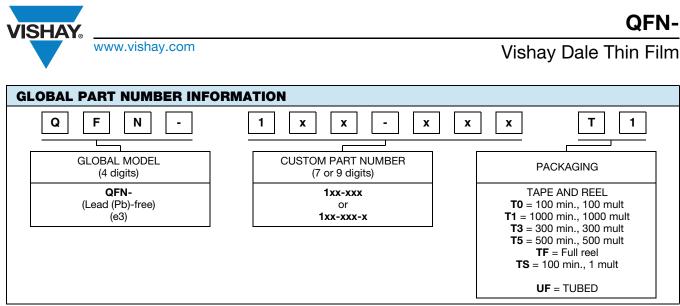


Note

• Contact factory for package outlines for higher pin count or custom configurations

MECHANICAL SPECIFICATIONS		
Resistive Element	Passivated nichrome	
Substrate Material	Silicon	
Body	Molded epoxy	
Terminals	Copper alloy	
Plating	100 % matte tin	
Marking Resistance to Solvents	Per MIL-PRF-914	

ORDERING INFORMATION CHECK LIST (Customs)			
Special requirements should be identified in advance, but as a minimum, you should have the following information ready.			
ELECTRICAL	MECHANICAL		
 Resistors, by value and tolerance Reference resistor(s) and matching of which resistors to which reference resistors Reference by ratio Absolute temperature coefficient of resistivity Temperature tracking of subordinate resistors to reference resistor(s) Maximum operating voltage Resistor power ratings Operating temperature range 	 Maximum allowable seated height (from PC board to top of network) Special marking concerns Schematic pin out of package 		





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