RoHS

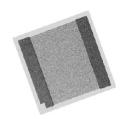
HALOGEN FREE

GREEN

<u>(5-2008)</u>



Thin Film 0505 Size Resistor on Alumina



Product may not be to scale

The CC3- series single-value resistor chips offer a relatively small size, low shunt capacitance and solder pad option. The CC3- nichrome resistors material offers excellent stability.

The CC3- resistors are manufactured using Vishay Electro-Films (EFI) sophisticated thin film equipment and manufacturing technology. The CC3- resistors are 100 % electrically tested and visually inspected to MIL-STD-883, method 2032 class H or K.

FEATURES

- Chip size: 0.050 inches square
- Wire bondable
- Case: 0505
- Resistance range: 30 Ω to 125 k Ω
- Alumina substrate
- Low stray capacitance: < 0.2 pF
- Resistor material: Nichrome
- Resistor passivation coat optional
- Tolerances to 0.05 %
- Solder pad optional
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

ADDI ICATIONS

Vishay EFI CC3- chip resistors provide excellent high-frequency response and are ideally suited for prototyping.

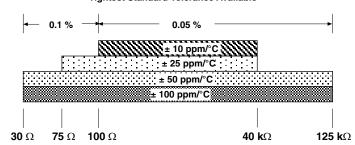
Typical application areas are:

- Amplifiers
- Oscillators
- Attenuators
- Couplers
- Filters

Recommended for hermetic environments where die is not exposed to moisture.

TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES, AND TOLERANCES		
PARAMETER	VALUE	UNIT
Total Resistance Range	30 to 125K	Ω
Standard Tolerances	± 0.05, ± 0.1	%
TCR	± 10, ± 25, ± 50, ± 100	ppm/°C

Tightest Standard Tolerance Available

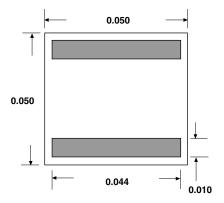


STANDARD ELECTRICAL SPECIFICATIONS		
PARAMETER	VALUE	UNIT
Noise, MIL-STD-202, Method 308	-20 typ.	dB
Moisture Resistance, MIL-STD-202, Method 106 - Hermetic Applications	\pm 0.2 max. $\Delta R/R$	%
Stability, 1000 h, +125 °C, 100 mW	± 0.1 max. Δ <i>R/R</i>	%
Operating Temperature Range	-55 to +125	°C
Thermal Shock, MIL-STD-202, Method 107, Test Condition F	± 0.25 max. ∆R/R	%
High Temperature Exposure, +150 °C, 100 h	\pm 0.1 max. $\Delta R/R$	%
Dielectric Voltage Breakdown	400	V
Insulation Resistance	10 ¹² min.	Ω
Operating Voltage	100	V
DC Power Rating at +125 °C (Derated to Zero at +150 °C)	0.100 max.	W
5x Rated Power Short-Time Overload, +25 °C, 5 s	± 0.25 max. ΔR/R	%

Revision: 09-Jun-17 **1** Document Number: 61002 For technical questions, contact: <u>efi@vishay.com</u>



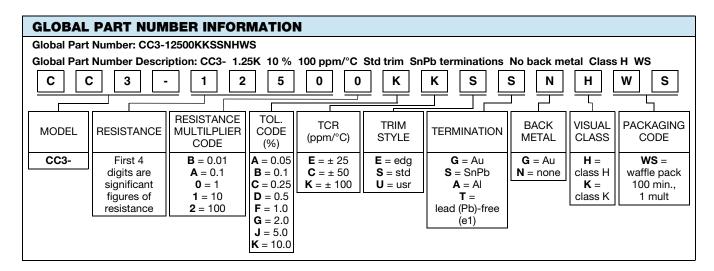
DIMENSIONS in inches



SCHEMATIC

$$\bigcirc$$
— \bigcirc — \bigcirc

MECHANICAL SPECIFICATIONS		
PARAMETER	VALUE	
Chip Size	0.050" x 0.050" ± 0.003" (1.27 mm x 1.27 mm ± 0.076 mm)	
Chip Thickness	0.010" ± 0.002" (0.25 mm ± 0.05 mm)	
Chip Substrate Material	99.6 % alumina, 2 μ" to 4 μ" finish	
Resistor Material	Nichrome	
Bonding Pad Size	0.010" x 0.044" (0.254 mmx 0.117 mm) minimum	
Number of Pads	2	
Pad Material	25 kÅ minimum gold standard	
Backing	None	





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