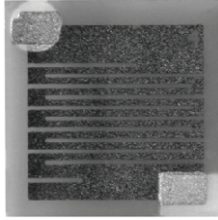


## Thin Film 0303 Size Resistor on Alumina



Product may not be to scale

The CC2 series single-value resistor chips offer a relatively small size, low shunt capacitance and solder pad option. The CC2s nichrome resistor material offers excellent stability.

The CC2s are manufactured using Vishay Electro Films (EFI) sophisticated thin film equipment and manufacturing technology. The CC2s are 100 % electrically tested and visually inspected to MIL-STD-883.

### FEATURES

- Wire bondable
- Chip size: 0.030 inches square
- Case: 0303
- Resistance range: 25  $\Omega$  to 38 k $\Omega$
- Alumina substrate
- Low stray capacitance: < 0.2 pF
- Resistor material: Nichrome
- Resistor passivation coat optional
- Tolerances to 0.05 %
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
(5-2008)

### APPLICATIONS

Vishay EFI CC2 chip resistors provide excellent high-frequency response and are ideally suited for prototyping.

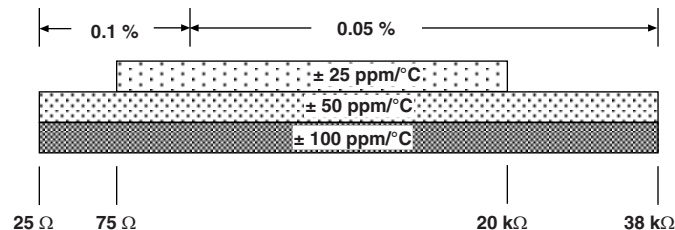
Typical application areas are:

- Amplifiers
- Oscillators
- Attenuators
- Couplers
- Filters

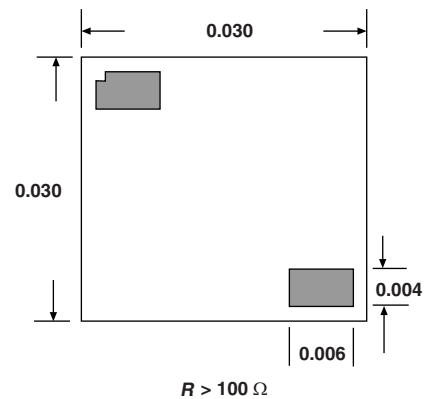
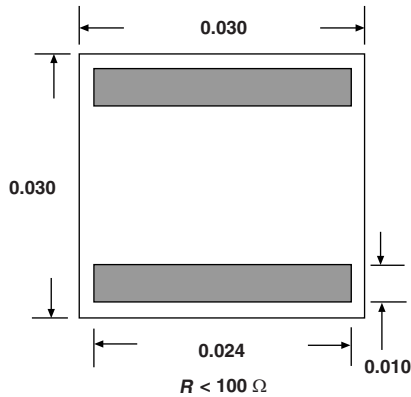
Recommended for hermetic environments where die is not exposed to moisture.

TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES, AND TOLERANCES		
PARAMETER	VALUE	UNIT
Total Resistance Range	25 to 38K	$\Omega$
Standard Tolerances	$\pm 0.05$ , $\pm 0.1$	%
TCR	$\pm 10$ , $\pm 25$ , $\pm 50$ , $\pm 100$	ppm/ $^{\circ}$ C

Tightest Standard Tolerance Available



STANDARD ELECTRICAL SPECIFICATIONS		
PARAMETER	VALUE	UNIT
Noise, MIL-STD-202, Method 308	-20 typ.	dB
Moisture Resistance, MIL-STD-202, Method 106 - Hermetic Applications	$\pm 0.2$ max. $\Delta R/R$	%
Stability, 1000 h, +125 $^{\circ}$ C, 100 mW	$\pm 0.1$ max. $\Delta R/R$	%
Operating Temperature Range	-55 to +125	$^{\circ}$ C
Thermal Shock, MIL-STD-202, Method 107, Test Condition F	$\pm 0.25$ max. $\Delta R/R$	%
High Temperature Exposure, +150 $^{\circ}$ C, 100 h	$\pm 0.1$ max. $\Delta R/R$	%
Dielectric Voltage Breakdown	400	V
Insulation Resistance	$10^{12}$ min.	$\Omega$
Operating Voltage	100	V
DC Power Rating at +125 $^{\circ}$ C (Derated to Zero at +150 $^{\circ}$ C)	0.100 max.	W
5x Rated Power Short-Time Overload, +25 $^{\circ}$ C, 5 s	$\pm 0.25$ % max. $\Delta R/R$	%

**DIMENSIONS** in inches

**SCHEMATIC**


MECHANICAL SPECIFICATIONS	
PARAMETER	VALUE
Chip Size	0.030" x 0.030" ± 0.003" (1.27 mm x 1.27 mm ± 0.076 mm)
Chip Thickness	0.010" ± 0.002" (0.25 mm ± 0.05 mm)
Chip Substrate Material	99.6 % alumina, 2 μ" to 4 μ" finish
Resistor Material	Nichrome
Bonding Pad Size	0.004" x 0.006" (0.100 mm x 0.15 mm) minimum
Number of Pads	2
Pad Material	25 kÅ minimum gold standard
Backing	None

GLOBAL PART NUMBER INFORMATION																
Global Part Number: <b>CC2-12500KSSNHWS</b>																
Global Part Number Description: <b>CC2- 1.25K 10 %, 100 ppm/°C, std trim, SnPb terminations, no back metal, class H, WS</b>																
C	C	2	-	1	2	5	0	0	K	K	S	S	N	H	W	S
MODEL	RESISTANCE	RES. MULTIPLIER CODE	TOL. CODE (%)	TCR (ppm/°C)	TRIM STYLE	TERMINATION	BACK METAL	VISUAL CLASS	PACKAGING CODE							
<b>CC2-</b>	First 4 digits are significant figures of resistance	<b>B</b> = 0.01 <b>A</b> = 0.1 <b>0</b> = 1 <b>1</b> = 10	<b>A</b> = 0.05 <b>B</b> = 0.1 <b>C</b> = 0.25 <b>D</b> = 0.5 <b>F</b> = 1.0 <b>G</b> = 2.0 <b>J</b> = 5.0 <b>K</b> = 10	<b>E</b> = ± 25 <b>C</b> = ± 50 <b>K</b> = ± 100	<b>E</b> = edg <b>S</b> = std <b>U</b> = usr	<b>G</b> = Au <b>S</b> = SnPb <b>A</b> = Al <b>T</b> = Lead (Pb)-free (e1)	<b>G</b> = Au <b>N</b> = none	<b>H</b> = class H <b>K</b> = class K	<b>WS</b> = waffle pack 100 min, 1 mult							



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