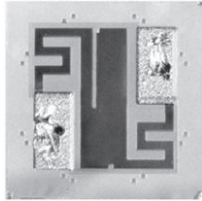


## NiCr Thin Film, Top-Contact Resistor



Product may not be to scale

The QFN series nichrome on quartz resistor chips offer a combination of nichrome stability, excellent frequency response and small size.

The QFNs are manufactured using Vishay Electro-Films (EFI) sophisticated thin film equipment and manufacturing technology. The QFNs are 100 % electrically tested and visually inspected to MIL-STD-883, method 2032 class H or K.

### FEATURES

- Wire bondable
- Chip size: 0.020 inches square
- Case: 0202
- Resistance range: 1  $\Omega$  to 510 k $\Omega$
- Resistor material: Nichrome
- Quartz substrate: < 0.1 pF shunt capacitance
- Power: 25 mW
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

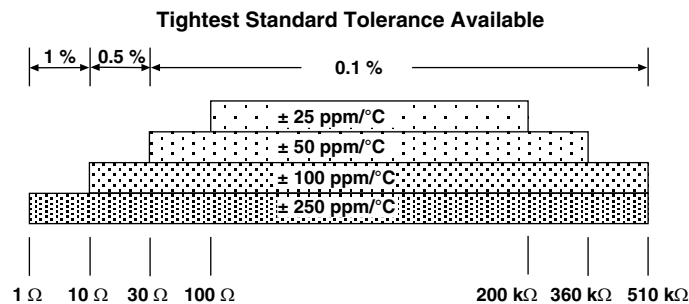


### APPLICATIONS

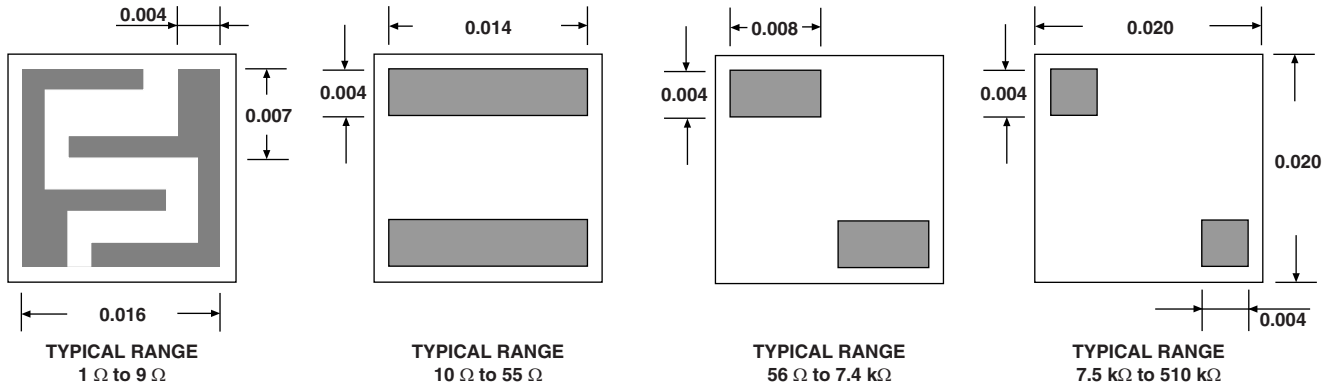
Vishay EFI QFN top-contact resistor chips are widely used in hybrid packages where space is limited. Designed with capacity to handle substantial power loads, they also have the benefit of nichrome stability.

Recommended for hermetic environments where die is not exposed to moisture.

TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES, AND TOLERANCES		
PARAMETER	VALUE	UNIT
Total Resistance Range	1 to 510K	$\Omega$
Standard Tolerances	$\pm 0.1$ , $\pm 0.5$ , $\pm 1$	%
TCR	$\pm 25$ , $\pm 50$ , $\pm 100$ , $\pm 250$	ppm/ $^{\circ}$ C



STANDARD ELECTRICAL SPECIFICATIONS		
PARAMETER	VALUE	UNIT
Noise, MIL-STD-202, Method 308 100 $\Omega$ to 250 k $\Omega$ < 100 $\Omega$ or > 251 k $\Omega$	-35 typ. -20 typ.	dB
Stability, 1000 h, +125 $^{\circ}$ C, 12.5 mW	$\pm 0.1$ % max. $\Delta R/R$	%
Operating Temperature Range	-55 to +125	$^{\circ}$ C
Thermal Shock, MIL-STD-202, Method 107, Test Condition F	$\pm 0.25$ max. $\Delta R/R$	%
High Temperature Exposure, +150 $^{\circ}$ C, 100 h	$\pm 0.5$ max. $\Delta R/R$	%
Dielectric Voltage Breakdown	200	V
Insulation Resistance	$10^{12}$ min.	$\Omega$
Operating Voltage	100 max.	V
DC Power Rating at +70 $^{\circ}$ C (Derated to zero at +175 $^{\circ}$ C)	0.025	W
5x Rated Power Short-Time Overload, +25 $^{\circ}$ C, 5 s	$\pm 0.25$ max. $\Delta R/R$	%

**CONFIGURATIONS** in inches

**SCHEMATIC**


MECHANICAL SPECIFICATIONS	
PARAMETER	VALUE
Chip Size	0.020" x 0.020" ± 0.003" (0.51 mm x 0.51 mm ± 0.076 mm)
Chip Thickness	0.010" ± 0.002" (0.254 mm ± 0.05 mm)
Chip Substrate Material	Quartz
Resistor Material	Nichrome (passivation optional)
Bonding Pad size	0.004" x 0.004" (0.10 mm x 0.10 mm)
Number of Pads	2
Pad Material	15 kÅ minimum gold (Al optional)
Backing	None, lapped quartz (Au optional)

GLOBAL PART NUMBER INFORMATION														
Global Part Number: QFN5000FKANHWS														
Global Part Number Description: QFN 5K 1% 100 ppm/°C Al None H WS														
Q	F	N	5	0	0	0	0	F	K	A	N	H	W	S
MODEL	RESISTANCE	RESISTANCE MULTIPLIER CODE	TOLERANCE CODE (%)	TCR (ppm/°C)	TERMINATION	BACK METAL	VISUAL CLASS	PACKAGING CODE						
<b>QFN</b> 20 x 20 size NiCr on quartz	First 4 digits are significant figures of resistance	<b>C</b> = 0.001 <b>B</b> = 0.01 <b>A</b> = 0.1 <b>0</b> = 1 <b>1</b> = 10 <b>2</b> = 100 <b>3</b> = 1000	<b>B</b> = 0.1 <b>C</b> = 0.25 <b>D</b> = 0.5 <b>F</b> = 1.0 <b>G</b> = 2.0 <b>J</b> = 5.0 <b>K</b> = 10.0	<b>E</b> = ± 25 <b>C</b> = ± 50 <b>K</b> = ± 100 <b>M</b> = ± 250	<b>G</b> = gold <b>A</b> = aluminum	<b>G</b> = gold <b>N</b> = none	<b>H</b> = class H <b>K</b> = class K	<b>WS</b> = waffle pack 100 min., 1 mult						



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