Vishay Siliconix

P-Channel 20 V (D-S) MOSFET

SOT-23 (TO-236)



Marking Code: C3

PRODUCT SUMMARY					
V _{DS} (V)	-20				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5 \text{ V}$	0.112				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 2.5 \text{ V}$	0.142				
Q _g typ. (nC)	3.3				
I _D (A) ^a	-3.1				
Configuration	Single				

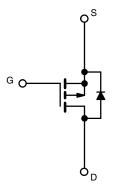
FEATURES

- TrenchFET® power MOSFET
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

· Load switch



P-Channel MOSFET

ORDERING INFORMATION	
Package	SOT-23 (TO-236)
Lead (Pb)-free and halogen-free	Si2301HDS-T1-GE3

ABSOLUTE MAXIMUM RATINGS (TA	= 25 °C, unless other	rwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V _{DS}	-20	.,	
Gate-source voltage		V_{GS}	±8	V	
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		-3.1		
	T _C = 70 °C		-2.5		
	T _A = 25 °C	I _D	-2.3 b, c		
	T _A = 70 °C		-1.8 ^{b, c}	Α	
Pulsed drain current		I _{DM}	-10		
Continuous source-drain diode current	T _C = 25 °C		-1.3		
	T _A = 25 °C	I _S	-0.72 ^{b, c}		
Maximum power dissipation	T _C = 25 °C		1.6		
	T _C = 70 °C	5	1	10/	
	T _A = 25 °C	P _D	0.86 b, c	W	
	T _A = 70 °C		0.55 b, c		
Operating junction and storage temperature range	T _J , T _{stq}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	≤ 5 s	R _{thJA}	120	145	°C/W	
Maximum junction-to-foot (drain)	Steady state	R _{th IE}	62	78	C/VV	

Notes

- a. Based on T_C = 25 °C
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 175 °C/W

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						•
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$		-	-18	-	mV/°C
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	2.2	-	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4	-	-1	V
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±8 V	-	-	±100	nA
Zero gate voltage drain current		V _{DS} = -20 V, V _{GS} = 0 V	-	-	-1	μА
	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-10	
On-state drain current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-6	-	-	Α
Drain-source on-state resistance ^a	Б	$V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$	-	0.090	0.112	Ω
	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -2 \text{ A}$	-	0.110	0.142	
Forward transconductance a	9fs	$V_{DS} = -5 \text{ V}, I_D = -2.8 \text{ A}$	-	9.5	-	S
Dynamic ^b						
Input capacitance	C _{iss}		-	405	-	pF
Output capacitance	Coss	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	75	-	
Reverse transfer capacitance	C _{rss}		-	55	-	
Total pate above	0	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -3 \text{ A}$	-	5.5	10	nC
Total gate charge	Q _g		-	3.3	6	
Gate-source charge	Q_{gs}	V_{DS} = -10 V, V_{GS} = -2.5 V, I_D = -3 A	-	0.7	-	
Gate-drain charge	Q_{gd}		-	1.3	-	
Gate resistance	R_g	f = 1 MHz	-	6	-	Ω
Turn-on delay time	t _{d(on)}		-	11	20	
Rise time	t _r	V_{DD} = -10 V, R_L = 10 Ω	-	35	60	
Turn-off delay time	t _{d(off)}	I_D = -1 A, V_{GEN} = -4.5 V, R_g = 1 Ω	-	30	50	- ns
Fall time	t _f		-	10	20	
Drain-Source Body Diode Characteristi	cs					•
Continuous source-drain diode current	IS	T _C = 25 °C	-	-	-1.3	А
Pulse diode forward current ^a	I _{SM}		-	-	-10	
Body diode voltage	V _{SD}	I _S = -0.7 A	-	-0.8	-1.2	V
Body diode reverse recovery time	t _{rr}		-	30	50	ns
Body diode reverse recovery charge	Q _{rr}	1 0 A 31/41 400 A/ - T 05 00	-	25	50	nC
Reverse recovery fall time	ta	$I_F = -3 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 °\text{C}$	-	15	-	
Reverse recovery rise time	t _b		-	15	-	ns

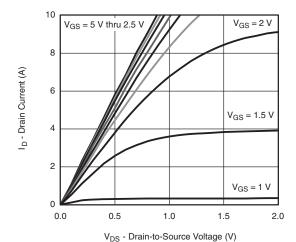
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing

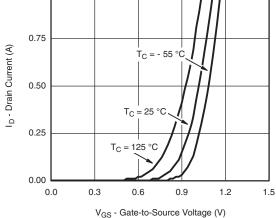
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

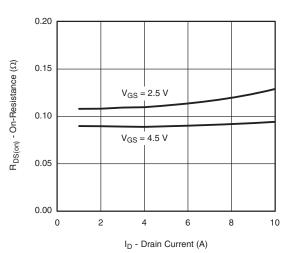


Output Characteristics

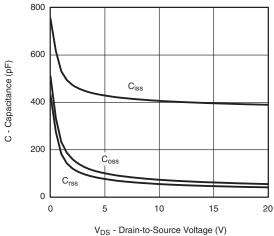


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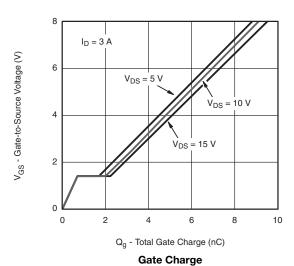
Transfer Characteristics

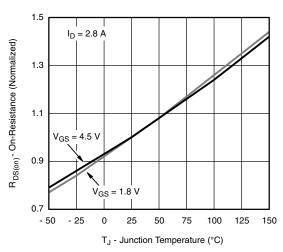


On-Resistance vs. Drain Current and Gate Voltage



Capacitance

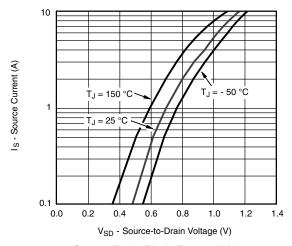




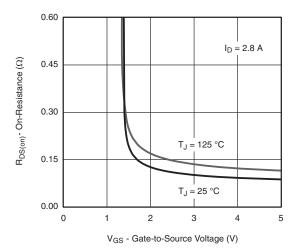
On-Resistance vs. Junction Temperature



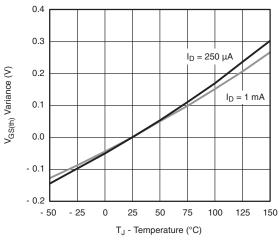
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



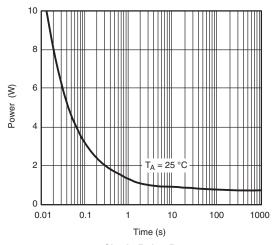
Source-Drain Diode Forward Voltage



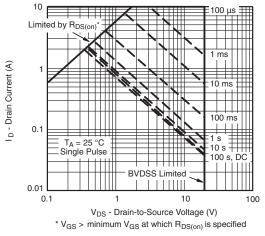
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

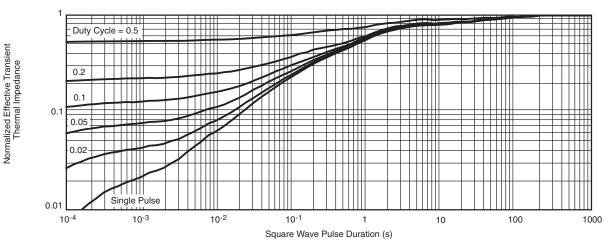


Single Pulse Power

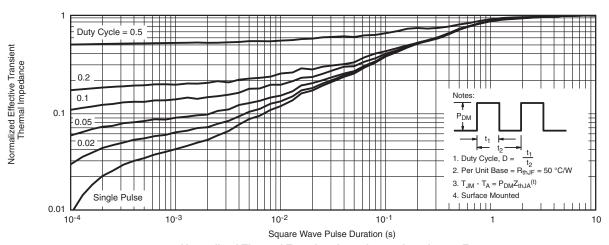




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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