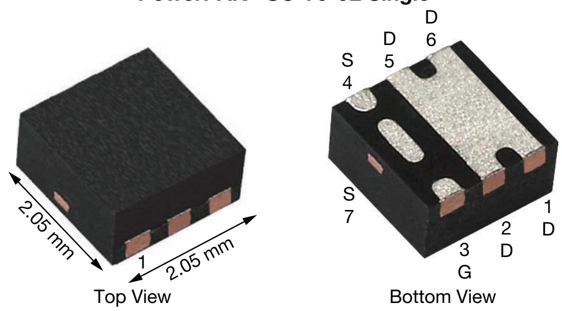
# N-Channel 40 V (D-S) MOSFET

**PowerPAK® SC-70-6L Single**




**Marking code**: A1

| PRODUCT SUMMARY | |
|---|---|
| $V_{DS}$ (V) | 40 |
| $R_{DS(on)}$ max. (Ω) at $V_{GS}$ = 10 V | 0.0110 |
| $R_{DS(on)}$ max. (Ω) at $V_{GS}$ = 4.5 V | 0.0150 |
| $Q_g$ typ. (nC) | 5.3 |
| $I_D$ (A) | 31 [a] |
| Configuration | Single |

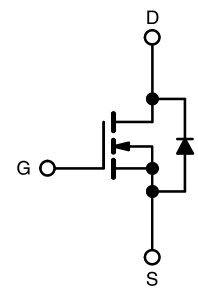
## FEATURES

- TrenchFET® Gen IV power MOSFET
- Tuned for the lowest $R_{DS}$ - $Q_{oss}$ FOM
- Thermally enhanced PowerPAK® SC-70 package - Small footprint area
- 100 % $R_g$ and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

## APPLICATIONS

- DC/DC converters
- Synchronous rectification
- Motor drive control
- Battery management and protection
- Load switch




N-Channel MOSFET

| ORDERING INFORMATION | |
|---|---|
| Package | PowerPAK SC-70 |
| Lead (Pb)-free and halogen-free | SiA4446DJ-T1-GE3 |

**ABSOLUTE MAXIMUM RATINGS** ($T_A$ = 25 °C, unless otherwise noted)

| PARAMETER | | SYMBOL | LIMIT | UNIT |
|---|---|---|---|---|
| Drain-source voltage | | $V_{DS}$ | 40 | V |
| Gate-source voltage | | $V_{GS}$ | +20 / -16 | |
| Continuous drain current ($T_J$ = 150 °C) | $T_C$ = 25 °C | $I_D$ | 31 | A |
| | $T_C$ = 70 °C | | 25 | |
| | $T_A$ = 25 °C | | 13 [b, c] | |
| | $T_A$ = 70 °C | | 10.6 [b, c] | |
| Pulsed drain current (t = 100 μs) | | $I_{DM}$ | 80 | |
| Continuous source-drain diode current | $T_C$ = 25 °C | $I_S$ | 17.5 | |
| | $T_A$ = 25 °C | | 3.2 [b, c] | |
| Single-pulse avalanche current | L = 0.1 mH | $I_{AS}$ | 13 | |
| Single-pulse avalanche energy | | $E_{AS}$ | 8.5 | mJ |
| Maximum power dissipation | $T_C$ = 25 °C | $P_D$ | 19.2 | W |
| | $T_C$ = 70 °C | | 12.3 | |
| | $T_A$ = 25 °C | | 3.5 [b, c] | |
| | $T_A$ = 70 °C | | 2.2 [b, c] | |
| Operating junction and storage temperature range | | $T_J$, $T_{stg}$ | -55 to +150 | °C |
| Soldering recommendations (peak temperature) [d, e] | | | 260 | |

| THERMAL RESISTANCE RATINGS | | | | | |
|---|---|---|---|---|---|
| PARAMETER | | SYMBOL | TYPICAL | MAXIMUM | UNIT |
| Maximum junction-to-ambient [b, f] | t ≤ 5 s | $R_{thJA}$ | 28 | 36 | °C/W |
| Maximum junction-to-case (drain) | Steady state | $R_{thJC}$ | 5.3 | 6.5 | |

**Notes**

a. $T_C$ = 25 °C.
b. Surface mounted on 1" x 1" FR4 board
c. t = 5 s
d. See solder profile (www.vishay.com/doc?73257). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
e. Rework conditions: Manual soldering with a soldering iron is not recommended for leadless components
f. Maximum under steady state conditions is 80 °C/W

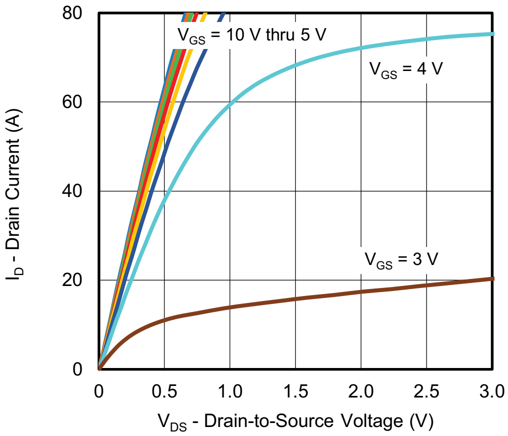
| **SPECIFICATIONS** ($T_J$ = 25 °C, unless otherwise noted) | | | | | | |
|---|---|---|---|---|---|---|
| **PARAMETER** | **SYMBOL** | **TEST CONDITIONS** | **MIN.** | **TYP.** | **MAX.** | **UNIT** |
| **Static** | | | | | | |
| Drain-source breakdown voltage | $V_{DS}$ | $V_{GS}$ = 0 V, $I_D$ = 250 μA | 40 | - | - | V |
| $V_{DS}$ temperature coefficient | $\Delta V_{DS}/T_J$ | $I_D$ = 10 mA | - | 24 | - | mV/°C |
| $V_{GS(th)}$ temperature coefficient | $\Delta V_{GS(th)}/T_J$ | $I_D$ = 250 μA | - | -5 | - | |
| Gate-source threshold voltage | $V_{GS(th)}$ | $V_{DS}$ = $V_{GS}$ , $I_D$ = 250 μA | 1.1 | - | 2.4 | V |
| Gate-source leakage | $I_{GSS}$ | $V_{DS}$ = 0 V, $V_{GS}$ = +20 V / -16 V | - | - | ± 100 | nA |
| Zero gate voltage drain current | $I_{DSS}$ | $V_{DS}$ = 40 V, $V_{GS}$ = 0 V | - | - | 1 | μA |
| | | $V_{DS}$ = 40 V, $V_{GS}$ = 0 V, $T_J$ = 55 °C | - | - | 10 | |
| Drain-source on-state resistance [a] | $R_{DS(on)}$ | $V_{GS}$ = 10 V, $I_D$ = 10 A | - | 0.0086 | 0.0110 | Ω |
| | | $V_{GS}$ = 4.5 V, $I_D$ = 5 A | - | 0.0110 | 0.0150 | |
| Forward transconductance [a] | $g_{fs}$ | $V_{DS}$ = 10 V, $I_D$ = 20 A | - | 51 | - | S |
| **Dynamic** [b] | | | | | | |
| Input capacitance | $C_{iss}$ | $V_{DS}$ = 20 V, $V_{GS}$ = 0 V, f = 1 MHz | - | 915 | - | pF |
| Output capacitance | $C_{oss}$ | | - | 180 | - | |
| Reverse transfer capacitance | $C_{rss}$ | | - | 28 | - | |
| $C_{rss}/C_{iss}$ ratio | | | - | 0.031 | 0.064 | |
| Total gate charge | $Q_g$ | $V_{DS}$ = 20 V, $V_{GS}$ = 10 V, $I_D$ = 10 A | - | 12.6 | 19 | nC |
| | | $V_{DS}$ = 20 V, $V_{GS}$ = 4.5 V, $I_D$ = 10 A | - | 5.9 | 8 | |
| Gate-source charge | $Q_{gs}$ | | - | 3 | - | |
| Gate-drain charge | $Q_{gd}$ | | - | 1.2 | - | |
| Output charge | $Q_{oss}$ | $V_{DS}$ = 20 V, $V_{GS}$ = 0 V | - | 6 | - | |
| Gate resistance | $R_g$ | f = 1 MHz | 0.5 | 2.5 | 5.0 | Ω |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD}$ = 20 V, $R_L$ = 2 Ω<br>$I_D$ ≅ 10 A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 Ω | - | 14 | 30 | ns |
| Rise time | $t_r$ | | - | 54 | 110 | |
| Turn-off delay time | $t_{d(off)}$ | | - | 14 | 30 | |
| Fall time | $t_f$ | | - | 6 | 15 | |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD}$ = 20 V, $R_L$ = 2 Ω<br>$I_D$ ≅ 10 A, $V_{GEN}$ = 10 V, $R_g$ = 1 Ω | - | 9 | 20 | |
| Rise time | $t_r$ | | - | 5 | 10 | |
| Turn-off delay time | $t_{d(off)}$ | | - | 16 | 35 | |
| Fall time | $t_f$ | | - | 5 | 10 | |
| **Drain-Source Body Diode Characteristics** | | | | | | |
| Continuous source-drain diode current | $I_S$ | $T_C$ = 25 °C | - | - | 17.5 | A |
| Pulse diode forward current (t = 100 μs) | $I_{SM}$ | | - | - | 60 | |
| Body diode voltage | $V_{SD}$ | $I_S$ = 5 A, $V_{GS}$ = 0 V | - | 0.85 | 1.1 | V |
| Body diode reverse recovery time | $t_{rr}$ | $I_F$ = 10 A, dI/dt = 100 A/μs, $T_J$ = 25 °C | - | 15 | 30 | ns |
| Body diode reverse recovery charge | $Q_{rr}$ | | - | 3.1 | 10 | nC |
| Reverse recovery fall time | $t_a$ | | - | 8 | - | ns |
| Reverse recovery rise time | $t_b$ | | - | 7 | - | |

**Notes**

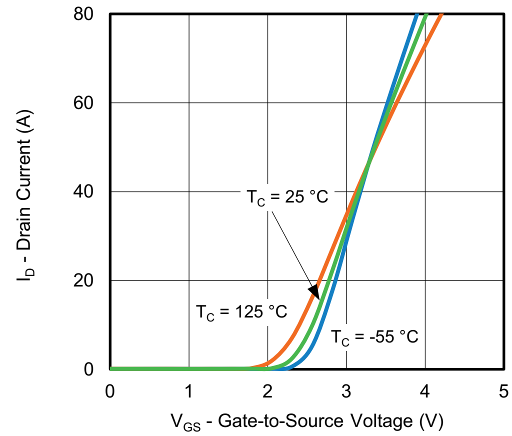
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
b. Guaranteed by design, not subject to production testing

*Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.*

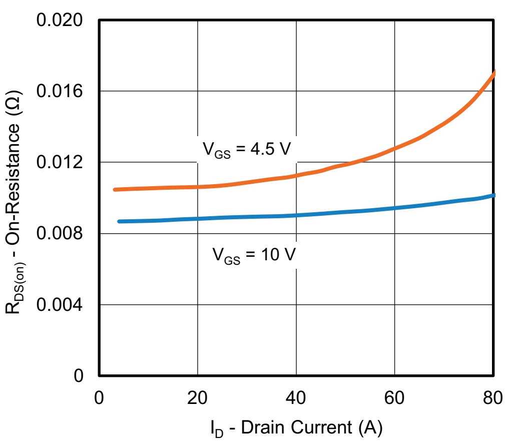
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



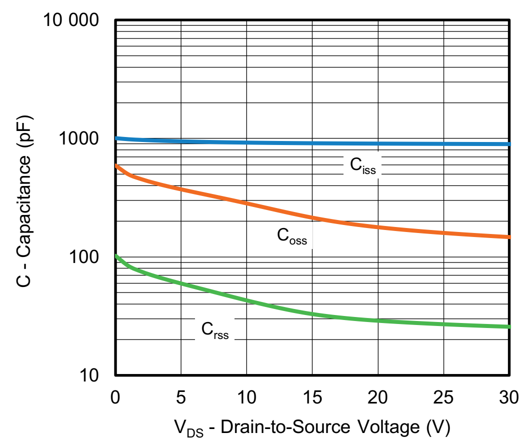

**Output Characteristics**



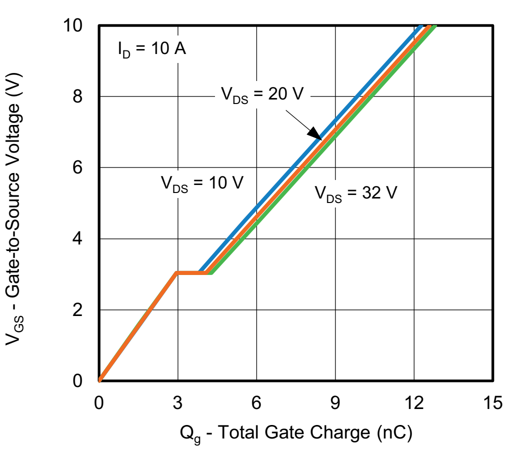

**Transfer Characteristics**



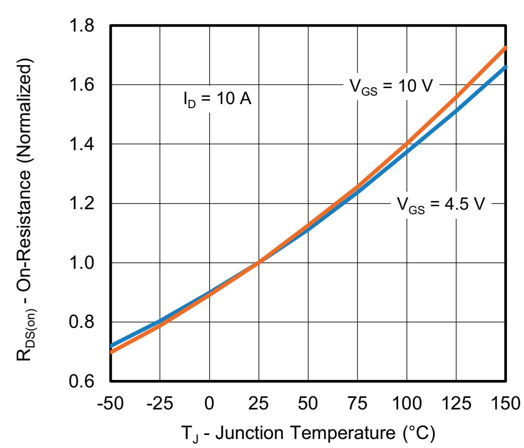

**On-Resistance vs. Drain Current**




**Capacitance**

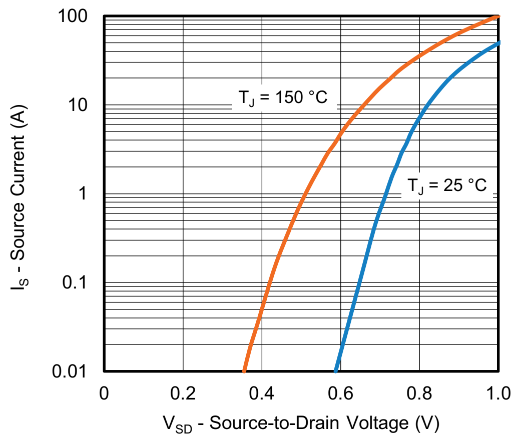



**Gate Charge**

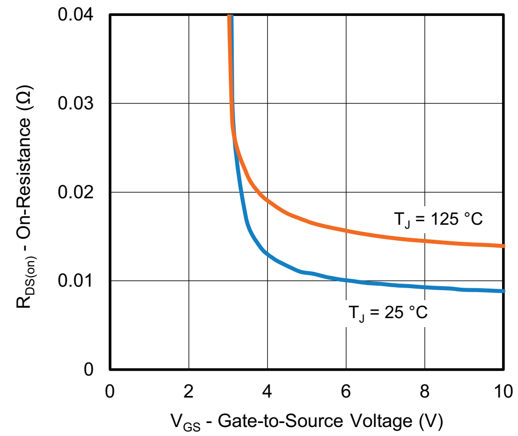



**On-Resistance vs. Junction Temperature**

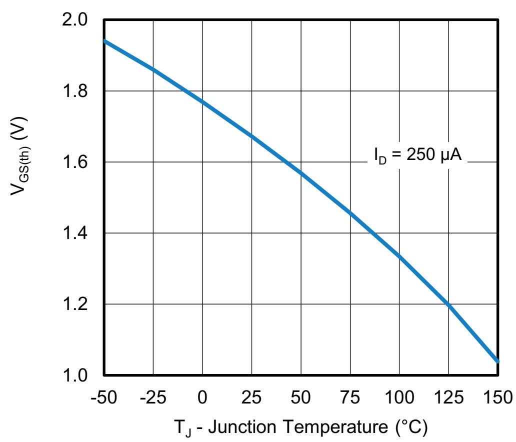
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



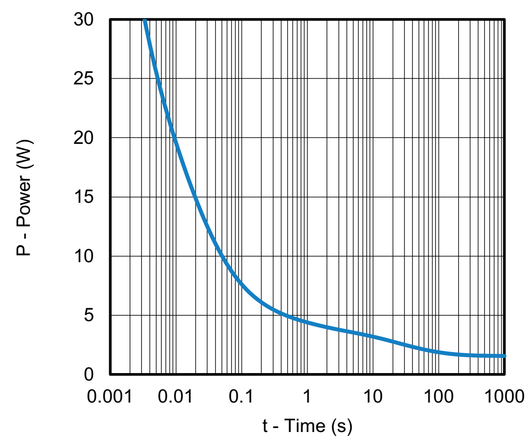

**Source-Drain Diode Forward Voltage**



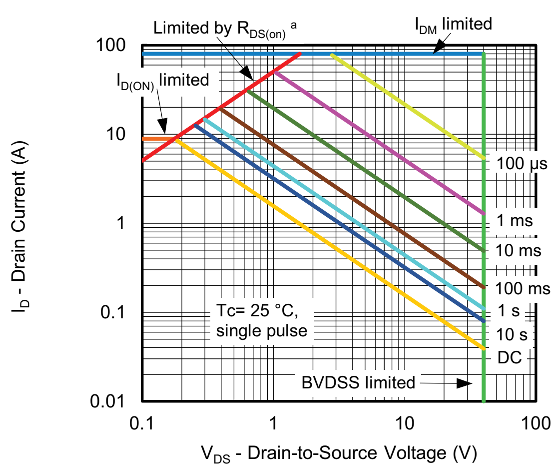

**On-Resistance vs. Gate-to-Source Voltage**




**Threshold Voltage**

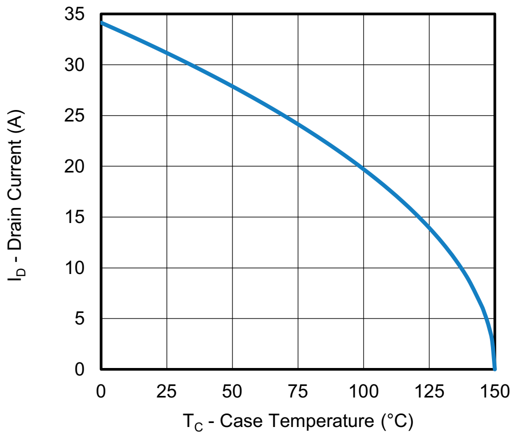



**Single Pulse Power, Junction-to-Ambient**

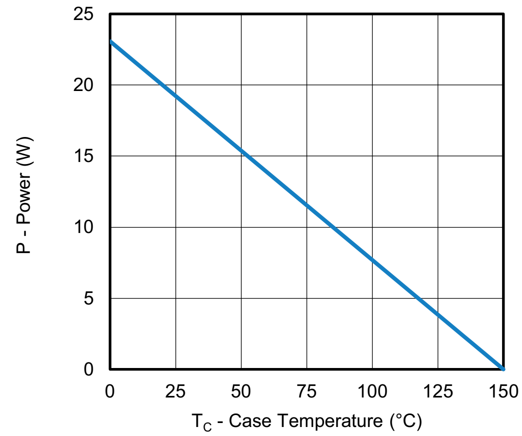



**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)




**Current Derating** [a]

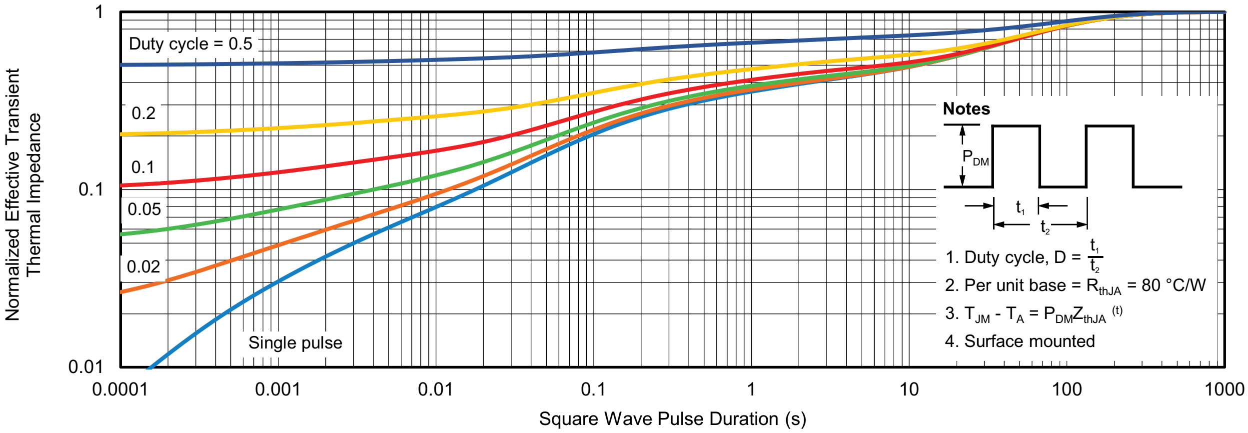



**Power Derating**

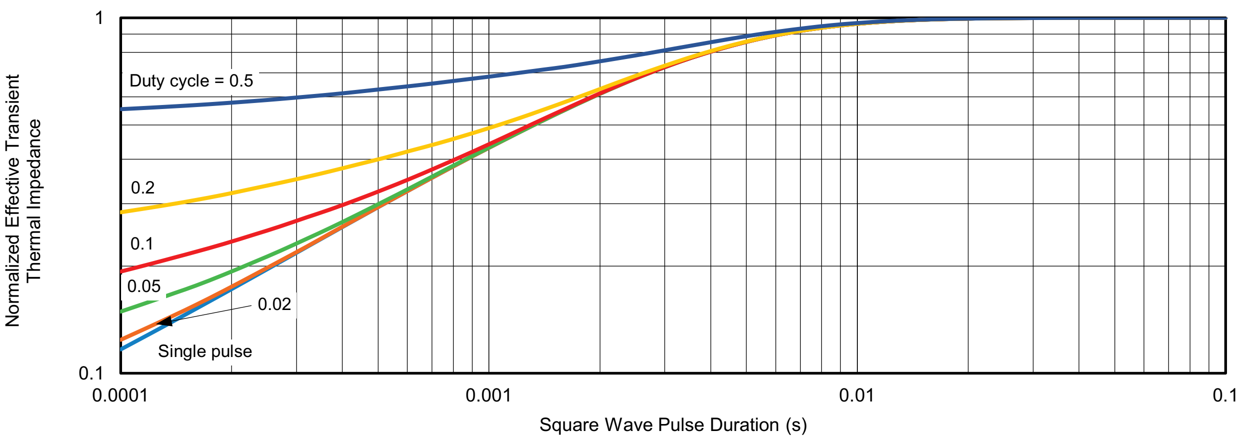
**Note**

a. The power dissipation $P_D$ is based on $T_J$ max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)




**Normalized Thermal Transient Impedance, Junction-to-Ambient**




**Normalized Thermal Transient Impedance, Junction-to-Case**

*Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62235.*

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, “Vishay”), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.