

# P-Channel 60 V (D-S) MOSFET



PRODUCT SUMMARY					
V <sub>DS</sub> (V)	-60				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = -10 \text{ V}$	0.007				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = -4.5 \text{ V}$	0.012				
Q <sub>g</sub> typ. (nC)	31.7				
I <sub>D</sub> (A) <sup>a</sup>	-90.9				
Configuration	Single				

### **FEATURES**

- TrenchFET® Gen V p-channel power MOSFET
- Very low R<sub>DS(on)</sub> minimizes voltage drop and reduces conduction loss

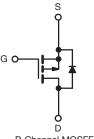


100 % R<sub>a</sub> and UIS tested

· Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

### **APPLICATIONS**

- · Adapter and charger switch
- · Battery and circuit protection
- OR-ing
- · Load switch
- · Motor drive control



P-Channel MOSFET

ORDERING INFORMATION	
Package	PowerPAK SO-8
Lead (Pb)-free and halogen-free	SiR5607DP-T1-RE3
Lead (Pb)-free, halogen-free, BLR and IOL	SiR5607DP-T1-UE3

ABSOLUTE MAXIMUM RATING	<b>S</b> (T <sub>A</sub> = 25 °C, u	nless other	wise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		$V_{DS}$	-60	V	
Gate-source voltage		$V_{GS}$	+20 / -20	V	
	T <sub>C</sub> = 25 °C		-90.9		
Continuous drain current (T, = 150 °C)	T <sub>C</sub> = 70 °C	1	-72.7		
Continuous drain current (1) = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	-22.2 b, c		
	T <sub>A</sub> = 70 °C		17.8 <sup>b, c</sup>	^	
Pulsed drain current (t = 100 μs)		I <sub>DM</sub>	-250	A	
Continuous source-drain diode current	T <sub>C</sub> = 25 °C	- I <sub>S</sub>	-94.6		
Continuous source-drain diode current	T <sub>A</sub> = 25 °C		-5.7 <sup>b, c</sup>		
Single pulse avalanche current	L = 0.1 mH	I <sub>AS</sub>	-50		
Single pulse avalanche energy		E <sub>AS</sub>	125	mJ	
	T <sub>C</sub> = 25 °C		104		
Maximum nauvar disaination	power dissipation $ \begin{array}{c c} \hline T_C = 70 \ ^{\circ}C \\ \hline T_A = 25 \ ^{\circ}C \\ \end{array} \qquad \begin{array}{c c} P_D \\ \hline \end{array} \qquad \begin{array}{c c} 66.6 \\ \hline 6.25 \ ^{b, \ c} \\ \end{array} $	66.6	W		
Maximum power dissipation		VV			
	T <sub>A</sub> = 70 °C	Ī	4 b, c		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering recommendations (peak temperature) d, e			260		

THERMAL RESISTANCE RAT	INGS				
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient b	t ≤ 10 s	R <sub>thJA</sub>	15	20	°C/W
Maximum junction-to-case (drain)	Steady state	Rthuc	0.9	1.2	C/VV

### **Notes**

- a. T<sub>C</sub> = 25 °C b. Surface mounted on 1" x 1" FR4 board
- See solder profile (www.vishav.com/doc?73257). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components Maximum under steady state conditions is 54 °C/W

# Vishay Siliconix

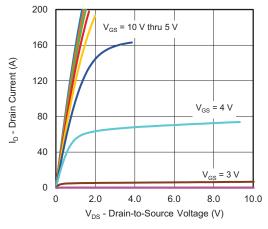
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-60	-	-	٧	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = -10 mA	-	-34	-	\//00	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = -250 μA	-	5.1	-	mV/°C	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.4	-	-2.6	٧	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = +20 \text{ / } -20 \text{ V}$	-	-	100	nA	
Zero gate voltage drain current		V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V	-	-	-10		
	I <sub>DSS</sub>	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C	-	-	-50	μA	
Data a succession and a succession and a	-	$V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$	-	0.0056	0.007		
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$	-	0.0092	0.012	Ω	
Forward transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -15 \text{ V}, I_{D} = -20 \text{ A}$	-	52	-	S	
Dynamic <sup>b</sup>	<u> </u>						
Input capacitance	C <sub>iss</sub>		-	5020	-		
Output capacitance	C <sub>oss</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	2050	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>		-	85	-	[	
<del></del>		$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$	-	74	112		
Total gate charge	$Q_g$		-	34.8	53		
Gate-source charge	Q <sub>qs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -20 \text{ A}$	-	18	-	nC	
Gate-drain charge	Q <sub>gd</sub>		-	10.1	-	1	
Gate resistance	R <sub>q</sub>	f = 1 MHz	0.5	1.4	2.4	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	18	36		
Rise time	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, \text{ R}_L = 1.5 \Omega, \text{ I}_D \cong -20 \text{ A},$	-	9	18		
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	40	80		
Fall time	t <sub>f</sub>		-	9	18		
Turn-on delay time	t <sub>d(on)</sub>		-	45	90	ns	
Rise time	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_1 = 1.5 \Omega, I_D \cong -20 \text{ A},$	-	320	640		
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	30	60		
Fall time	t <sub>f</sub>		_	20	40		
<b>Drain-Source Body Diode Characteristi</b>	cs					ı	
Continuous source-drain diode current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	-94.6		
Pulse diode forward current	I <sub>SM</sub>	-	-	-	-250	A	
Body diode voltage	V <sub>SD</sub>	$I_S = -5 \text{ A}, V_{GS} = 0 \text{ V}$	-	-0.76	-1.1	V	
Body diode reverse recovery time	t <sub>rr</sub>	-	-	70	140	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	$I_F = -20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s},$	-	85	170	nC	
Reverse recovery fall time	t <sub>a</sub>	$T_{\text{J}} = 25 ^{\circ}\text{C}$	-	36	-		
Reverse recovery rise time	t <sub>b</sub>		_	34	_	ns	

### Notes

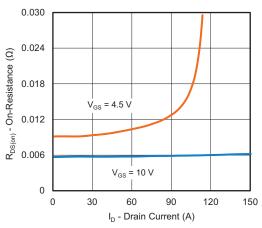
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

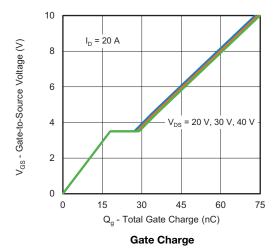


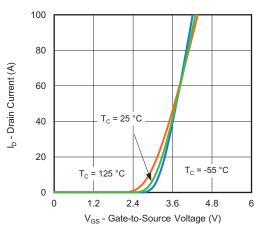


### **Output Characteristics**

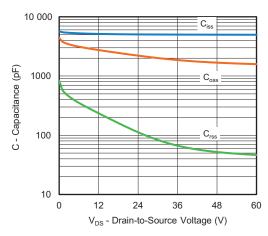


On-Resistance vs. Drain Current and Gate Voltage

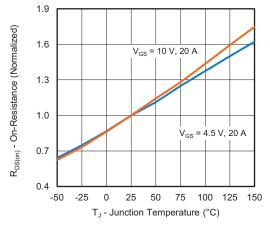




**Transfer Characteristics** 

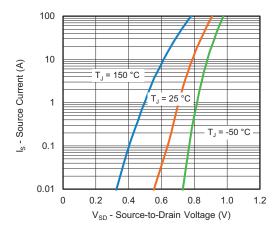


Capacitance

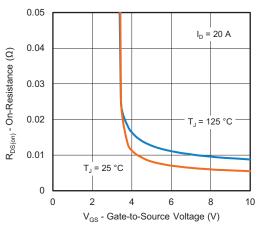


On-Resistance vs. Junction Temperature

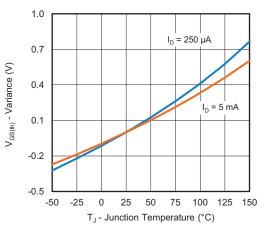




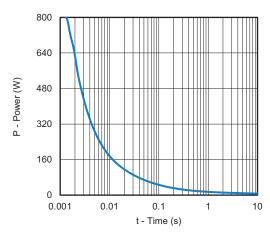
Source-Drain Diode Forward Voltage



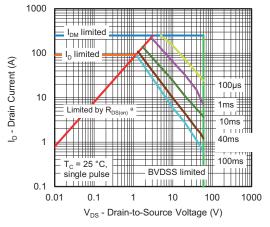
On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 



Single Pulse Power, Junction-to-Ambient

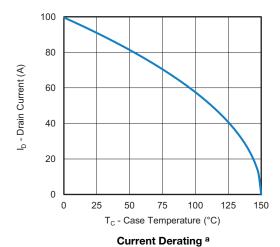


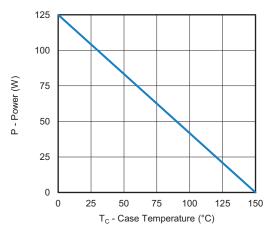
Safe Operating Area, Junction-to-Case

# Note

a.  $V_{GS} > minimum V_{GS}$  at which  $R_{DS(on)}$  is specified





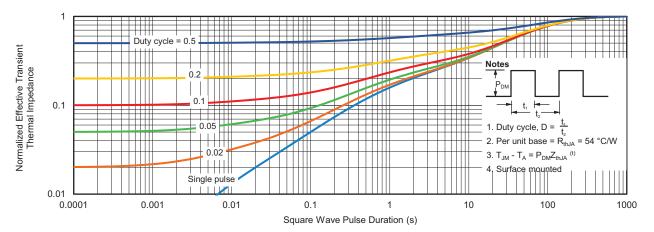


Power, Junction-to-Case

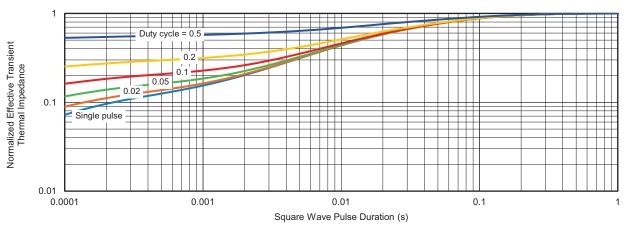
### Note

a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?62247">www.vishay.com/ppg?62247</a>.



DWG: 5881

PowerPAK® SO-8, (Single/Dual)

# Notes 1. Inch will govern. 2 Dimensions exclusive of mold gate burrs.

3. Dimensions exclusive of mold flash and cutting burrs.

Backside View of Dual Pad

DIM.		MILLIMETERS			INCHES			
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX		
Α	0.97	1.04	1.12	0.038	0.041	0.044		
A1		-	0.05	0	-	0.002		
b	0.33	0.41	0.51	0.013	0.016	0.020		
С	0.23	0.28	0.33	0.009	0.011	0.013		
D	5.05	5.15	5.26	0.199	0.203	0.20		
D1	4.80	4.90	5.00	0.189	0.193	0.197		
D2	3.56	3.76	3.91	0.140	0.148	0.15		
D3	1.32	1.50	1.68	0.052	0.059	0.06		
D4		0.57 typ.		0.0225 typ.				
D5		3.98 typ.		0.157 typ.				
E	6.05	6.15	6.25	0.238	0.242	0.24		
E1	5.79	5.89	5.99	0.228	0.232	0.23		
E2	3.48	3.66	3.84	0.137	0.144	0.15		
E3	3.68	3.78	3.91	0.145	0.149	0.15		
E4		0.75 typ.			0.030 typ.			
е		1.27 BSC		0.050 BSC				
K		1.27 typ.		0.050 typ.				
K1	0.56	-	-	0.022	-	-		
Н	0.51	0.61	0.71	0.020	0.024	0.02		
L	0.51	0.61	0.71	0.020	0.024	0.02		
L1	0.06	0.13	0.20	0.002	0.005	0.00		
θ	0°	-	12°	0°	-	12°		
W	0.15	0.25	0.36	0.006	0.010	0.01		
М		0.125 typ.			0.005 typ.			

Revison: 13-Feb-17 1 Document Number: 71655



# RECOMMENDED MINIMUM PADS FOR PowerPAK® SO-8 Single



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



# **Legal Disclaimer Notice**

Vishay

# **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.