Vishay Siliconix

P-Channel 30 V (D-S) MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	-30			
$R_{DS(on)}$ max. (Ω) at V_{GS} = -10 V	0.0075			
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5 \text{ V}$	0.0130			
Q _g typ. (nC)	28			
I _D (A) ^a	-20.5			
Configuration	Single			

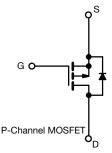
FEATURES

- TrenchFET® Gen IV p-channel power MOSFET
- 100% R_g tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



APPLICATIONS

- Adapter switch
- Battery management
- Circuit protection
- · Load switch
- Motor drive control



ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SI4151DY-T1-GE3

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	-30	V
Gate-source voltage		V _{GS}	±25	
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		-20.5	
	T _C = 70 °C	1 . [-16.4	
	T _A =25 °C	I _D	-15.2 ^{b, c}	
	T _A = 70 °C	1 [-12.1 ^{b, c}	
Pulsed drain current (t = 100 μs)		I _{DM}	-150	A
Continuous source-drain diode current	T _C = 25 °C		-5.1	
	T _A = 25 °C	ls l	-2.8 ^{b, c}	
Single pulse avalanche current	. 0.1!!	I _{AS}	-20	
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	20	mJ
Maximum power dissipation	T _C = 25 °C		5.6	
	T _C = 70 °C	1 5 [3.6	10/
	T _A = 25 °C	P _D	3.1 b, c	W
	T _A = 70 °C	1 [2.0 b, c	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	00
Soldering recommendations (peak temperature) d, e			260	°C

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	t ≤ 10 s	R_{thJA}	34	40	°C/W	
Maximum junction-to-case (drain)	Steady state	R_{thJF}	18	22	C/VV	

Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 10
- d. Maximum under steady state conditions is 85 $^{\circ}\text{C/W}$
- e. $T_C = 25$ °C



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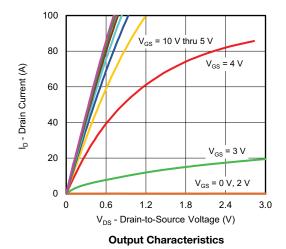
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			•				
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	1 0504	-	-27	-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	4.4	-		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1	-	-2.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$	-	-	± 100	nA	
Zoro goto voltago drain ourrent	_	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1		
Zero gate voltage drain current	I _{DSS}	V_{DS} = -30 V, V_{GS} = 0 V, T_J = 70 °C	-	-	-15	μA	
Drain-source on-state resistance ^a	В	$V_{GS} = -10 \text{ V}, I_D = -10 \text{ A}$	-	0.00625	0.0075	Ω	
	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -10 \text{ A}$	-	0.0102	0.0130		
Forward transconductance ^a	9 _{fs}	$V_{DS} = -10 \text{ V}, I_{D} = -10 \text{ A}$	-	37	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	3250	-	pF	
Output capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	410	-		
Reverse transfer capacitance	C _{rss}		-	375	-		
Total gate charge	0	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -10 \text{ A}$ $V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -10 \text{ A}$ $V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -10 \text{ A}$	-	58	87	nC	
Total gate charge	Q_g		-	28	42		
Gate-source charge	Q_{gs}		-	10.7	-		
Gate-drain charge	Q_{gd}	v _{DS} = -13 v, v _{GS} = -4.5 v, i _D = -10 A	-	9.9	-		
Gate resistance	R_g	f = 1 MHz	1.1	2.2	3.8	Ω	
Turn-on delay time	t _{d(on)}		-	26	54		
Rise time	t _r	V_{DD} = -15 V, R_L = 1.5 Ω , I_D \cong -10 A,	-	67	135		
Turn-off delay time	t _{d(off)}	V_{GEN} = -4.5 V, R_g = 1 Ω	-	30	60		
Fall time	t _f		-	20	40	ns	
Turn-on delay time	t _{d(on)}		-	12	24	115	
Rise time	t _r	V_{DD} = -15 V, R_L = 1.5 $\Omega,~I_D\cong$ -10 A,	-	7	14		
Turn-off delay time	t _{d(off)}	V_{GEN} = -10 V, R_g = 1 Ω	-	40	80		
Fall time	t _f		-	8	16		
Drain-Source Body Diode Characterist	ics						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-5.1	٨	
Pulse diode forward current	I _{SM}		-	-	-150	A	
Body diode voltage	V _{SD}	I _S = -5 A, V _{GS} = 0 V	-	-0.75	-1.1	V	
Body diode reverse recovery time	t _{rr}		-	18	36	ns	
Body diode reverse recovery charge	Q_{rr}	$I_F = -10 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s,}$	-	8	16	nC	
Reverse recovery fall time	t _a	T _J = 25 °C	-	9	-	İ	
Reverse recovery rise time	t _b		-	9	-	ns	

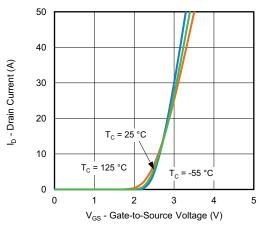
Notes

- f. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- g. Guaranteed by design, not subject to production testing

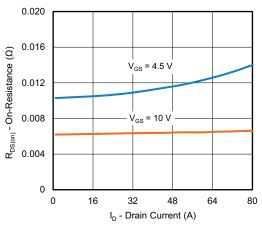
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

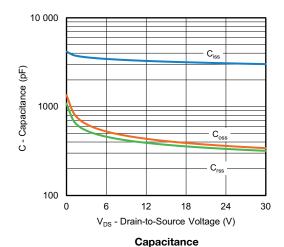




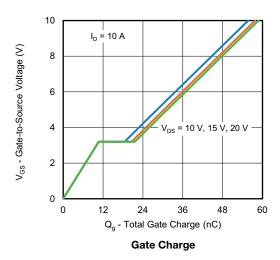


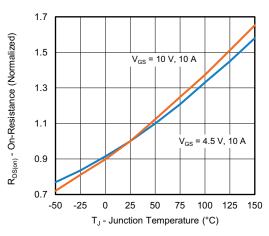






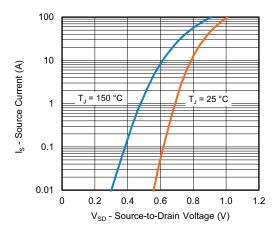
On-Resistance vs. Drain Current and Gate Voltage



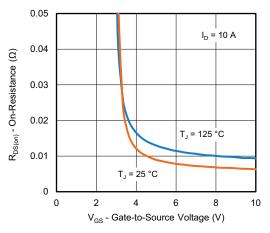


On-Resistance vs. Junction Temperature

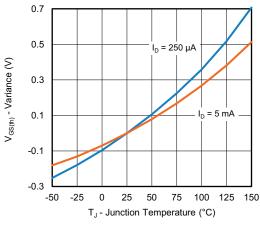




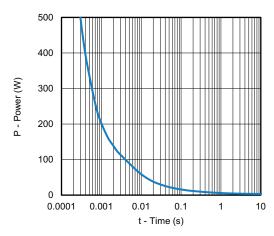
Source-Drain Diode Forward Voltage



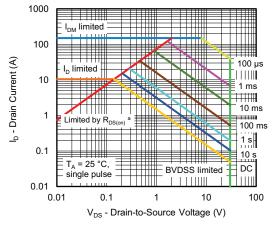
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient

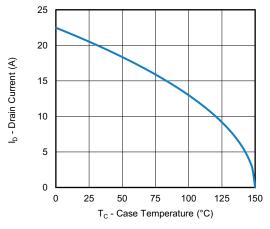


Safe Operating Area, Junction-to-Ambient

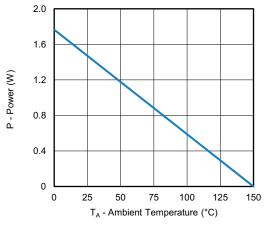
Note

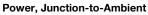
a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

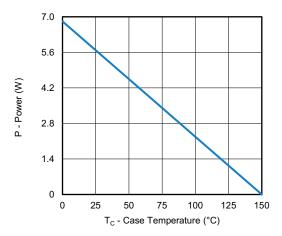




Current Derating a





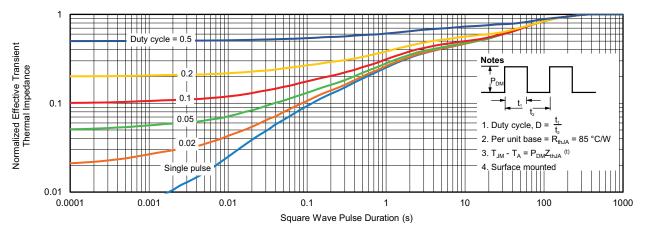


Power, Junction-to-Case

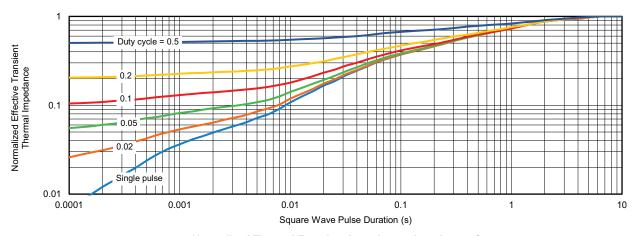
Note

a. The power dissipation P_D is based on T_J max. = 25 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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