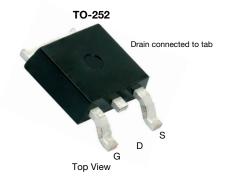


# Automotive N-Channel 40 V (D-S) 175 °C MOSFET

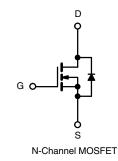
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	40			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.0036			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 4.5 V$	0.0042			
I <sub>D</sub> (A)	100			
Configuration	Single			
Package	TO-252			



#### FEATURES

- TrenchFET<sup>®</sup> power MOSFET
- Package with low thermal resistance
- 100 %  $R_q$  and UIS tested
- AEC-Q101 qualified <sup>d</sup>
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ABSOLUTE MAXIMUM RATING	<b>GS</b> (T <sub>C</sub> = 25 °C, unless	otherwise noted	)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V <sub>DS</sub>	40	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
Continuous Drain Current	T <sub>C</sub> = 25 °C ª	Ι <sub>D</sub>	100	
	T <sub>C</sub> = 125 °C		80	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	100	А
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	400	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	55	
Single Pulse Avalanche Energy		E <sub>AS</sub>	151	mJ
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	Р	136	W
Maximum Fower Dissipation ~	T <sub>C</sub> = 125 °C	P <sub>D</sub>	45	vv
Operating Junction and Storage Temperation	ure Range	T <sub>J</sub> , T <sub>stq</sub>	-55 to +175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	50	°C/W
Junction-to-Case (Drain)		R <sub>thJC</sub>	1.1	0/10

Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- c. When mounted on 1" square PCB (FR4 material).

d. Parametric verification ongoing.

## SQD100N04-3m6L



www.vishay.com

Vishay Siliconix

PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	= 0 V, I <sub>D</sub> = 250 μA	40	-	-	v
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	: V <sub>GS</sub> , I <sub>D</sub> = 250 μΑ	1.5	-	2.5	v
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	0 V, $V_{GS} = \pm 20 V$	-	-	± 100	nA
		$V_{GS} = 0 V$	V <sub>DS</sub> = 40 V	-	-	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 40 V, T <sub>J</sub> = 125 °C	-	-	50	μA
		$V_{GS} = 0 V$	$V_{DS} = 40 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α
		$V_{GS} = 10 V$	I <sub>D</sub> = 20 A	-	0.0030	0.0036	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = 10 V$	I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C	-	-	0.0058	Ω
Drain-Source On-State Resistance "	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C	-	-	0.0070	12
		$V_{GS} = 4.5 V$	I <sub>D</sub> = 20 A	-	0.0035	0.0042	
Forward Transconductance b	<b>g</b> fs	V <sub>DS</sub>	= 15 V, I <sub>D</sub> = 15 A	-	105	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			-	4880	5860	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 25 V, f = 1 MHz	-	560	670	pF
Reverse Transfer Capacitance	C <sub>rss</sub>			-	250	300	
Total Gate Charge <sup>c</sup>	Qg			-	85	130	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{GS} = 10 \text{ V}$	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 50 \text{ A}$	-	14	-	nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	14	-	
Gate Resistance	R <sub>g</sub>		f = 1 MHz	0.6	1.5	3	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	9	11	
Rise Time <sup>c</sup>	t <sub>r</sub>	V <sub>DD</sub> =	= 20 V, R <sub>L</sub> = 0.4 Ω	-	11	14	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 50 \text{ A},$	$V_{GEN} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$	-	39	47	ns
Fall Time <sup>c</sup>	t <sub>f</sub>			-	11	14	
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	400	Α

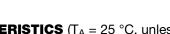
Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



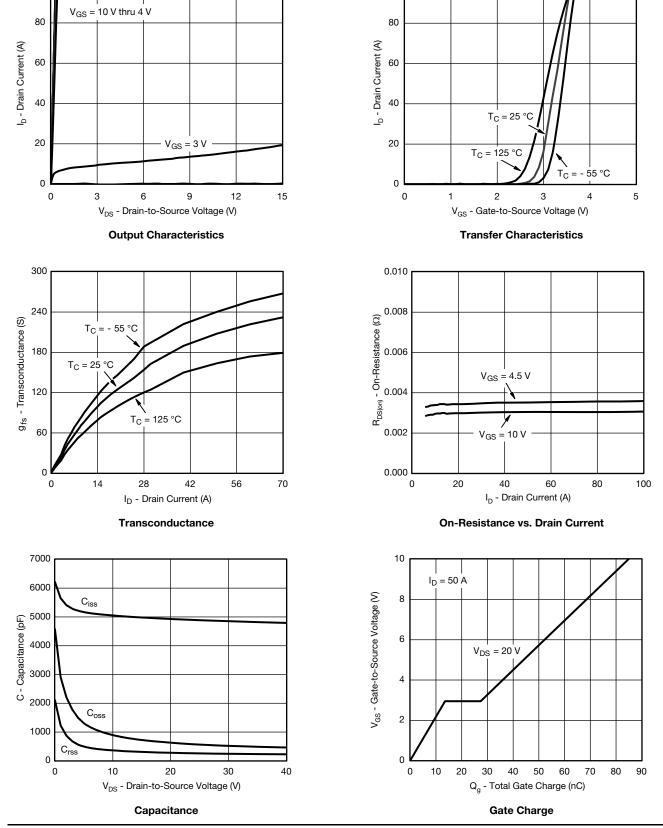
SQD100N04-3m6L

### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

www.vishay.com

**ISHAY** 

100



100

S15-1874-Rev. B, 10-Aug-15

Document Number: 63837

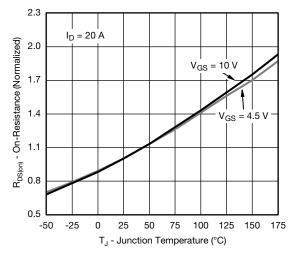
For technical questions, contact: <u>automostechsupport@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u> www.vishay.com

**ISHAY** 

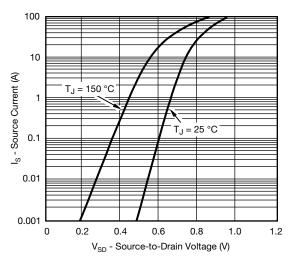
SQD100N04-3m6L

Vishay Siliconix

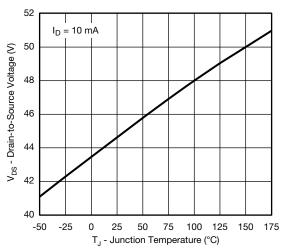
### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



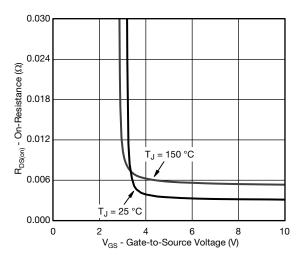
**On-Resistance vs. Junction Temperature** 



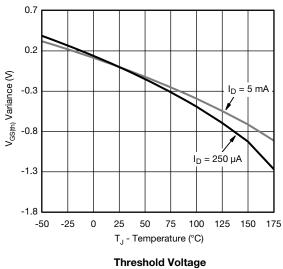
Source Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature



**On-Resistance vs. Gate-to-Source Voltage** 



S15-1874-Rev. B, 10-Aug-15

4

Document Number: 63837

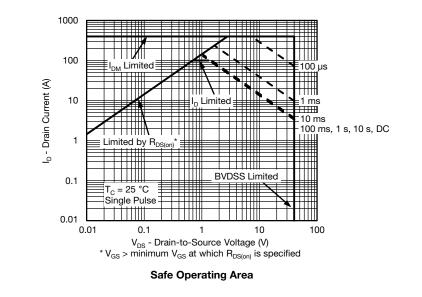
For technical questions, contact: <u>automostechsupport@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

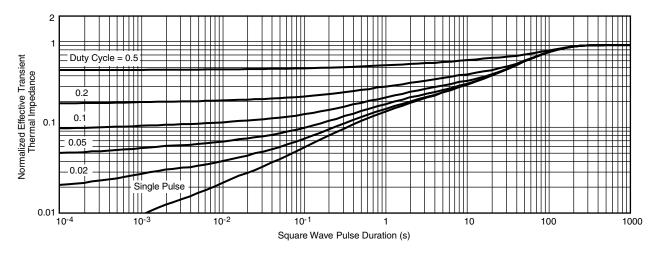


SQD100N04-3m6L

**Vishay Siliconix** 

#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

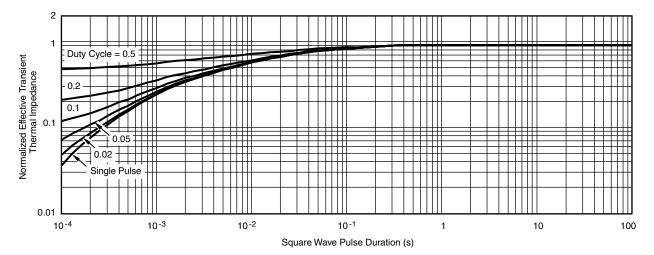




Normalized Thermal Transient Impedance, Junction-to-Ambient



### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?63837">www.vishay.com/ppg?63837</a>.



# SQD100N04-3m6L

Vishay Siliconix

REVISION	HISTORY <sup>a</sup>	
REVISION	DATE	DESCRIPTION OF CHANGE
В	04-Aug-15	Revised R <sub>g</sub> minimum limit

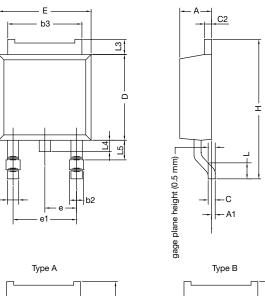
Note

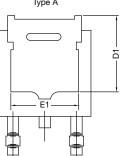
a. As of April 2014



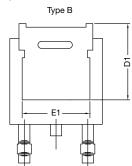


TO-252AA Case Outline





b



MIN.   2.18   -   0.64   0.76   4.95   0.46   0.46   5.97   4.10	MAX. 2.38 0.127 0.88 1.14 5.46 0.61 0.89 6.22	MIN. 0.086 - 0.025 0.030 0.195 0.018 0.018 0.235	MAX. 0.094 0.005 0.035 0.045 0.215 0.024 0.035 0.245
- 0.64 0.76 4.95 0.46 0.46 5.97 0.64 0.65 0.65 0.65 0.65 0.65 0.65 0.65 0.65	0.127 0.88 1.14 5.46 0.61 0.89 6.22	- 0.025 0.030 0.195 0.018 0.018	0.005 0.035 0.045 0.215 0.024 0.035
0.64     0.76       4.95     0.46       0.46     0.46       5.97     0.46	0.88 1.14 5.46 0.61 0.89 6.22	0.025 0.030 0.195 0.018 0.018	0.035 0.045 0.215 0.024 0.035
0.76 4.95 0.46 0.46 5.97	1.14 5.46 0.61 0.89 6.22	0.030 0.195 0.018 0.018	0.045 0.215 0.024 0.035
4.95 0.46 0.46 5.97	5.46 0.61 0.89 6.22	0.195 0.018 0.018	0.215 0.024 0.035
0.46 0.46 5.97	0.61 0.89 6.22	0.018 0.018	0.024 0.035
0.46 5.97	0.89 6.22	0.018	0.035
5.97	6.22		
		0.235	0.245
4.10			0.240
	-	0.161	-
6.35	6.73	0.250	0.265
4.32	-	0.170	-
9.40	10.41	0.370	0.410
2.28 BSC		0.090	BSC
4.56 BSC		0.180	BSC
1.40	1.78	0.055	0.070
0.89	1.27	0.035	0.050
-	1.02	-	0.040
1.01	1.52	0.040	0.060
	4.32 9.40 2.28 B 4.56 B 1.40 0.89 -	4.32     -       9.40     10.41       2.28 BSC     4.56 BSC       1.40     1.78       0.89     1.27       -     1.02	4.32     -     0.170       9.40     10.41     0.370       2.28 BSC     0.090       4.56 BSC     0.180       1.40     1.78     0.055       0.89     1.27     0.035       -     1.02     -

#### Notes

• Dimension L3 is for reference only

• Dimension D1 and E1 on type A and B is the same



### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



Vishay

## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025