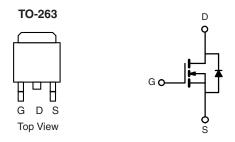


www.vishay.com

Vishay Siliconix

Automotive N-Channel 330 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	330		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.160		
I _D (A)	31		
Configuration	Single		



N-Channel MOSFET

FEATURES

- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- AEC-Q101 Qualified
- 100 % R_g and UIS Tested
- Material categorization:
 For definitions of compliance please see www.vishav.com/doc?99912





ORDERING INFORMATION	
Package	TO-263
Lead (Pb)-free and Halogen-free	SQM18N33-160H-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	330	V	
Gate-Source Voltage		V _{GS}	± 30		
Continuous Drain Current	T _C = 25 °C	- I _D	31		
	T _C = 125 °C		18		
Continuous Source Current (Diode Conduction) ^a		Is	120	Α	
Pulsed Drain Current ^b		I _{DM}	65		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	16		
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	12	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	—— P ₂ ⊢	375	W	
	T _C = 125 °C		125	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	40	°C/W	
nction-to-Case (Drain)		R _{thJC}	0.4	C/VV	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,\,duty~cycle \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static						l .	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = 250 μA		330	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		3.8	5.0	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 30 \text{ V}$		-	-	± 100	nA
Zero Gate Voltage Drain Current		V _{GS} = 0 V	V _{DS} = 330 V	-	-	1	
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 330 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 330 V, T _J = 175 °C	-	-	250	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 30 A	-	0.130	0.160	Ω
	R _{DS(on)}	V _{GS} = 10 V	I _D = 20 A, T _J = 125 °C	-	-	0.348	
		V _{GS} = 10 V	I _D = 20 A, T _J = 175 °C	-	-	0.467	
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		-	41	-	S
Dynamic ^b	·						
Input Capacitance	C _{iss}		V V _{DS} = 75 V, f = 1 MHz	-	3700	4625	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	140	210	
Reverse Transfer Capacitance	C _{rss}	1		-	60	90	
Total Gate Charge ^c	Qg		V _{DS} = 175 V, I _D = 18 A	-	71	105	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V		-	18	=-	nC
Gate-Drain Charge ^c	Q _{gd}	1		-	22	-	
Gate Resistance	R_g	f = 1 MHz		0.40	0.97	1.50	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	18	27	
Rise Time ^c	t _r	$V_{DD} = 175 \text{ V}, \text{ R}_{L} = 9.7 \Omega$ $I_{D} \cong 18 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$		-	35	52	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	27	41	
Fall Time ^c	t _f			-	12	18	
Source-Drain Diode Ratings and Char	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	65	А
Forward Voltage	V _{SD}	I _F = 18 A, V _{GS} = 0		-	1.1	1.5	V

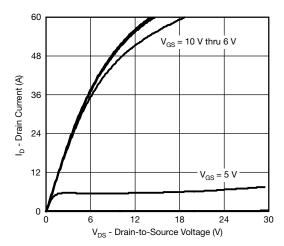
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

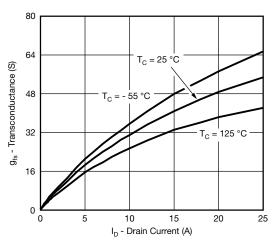
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



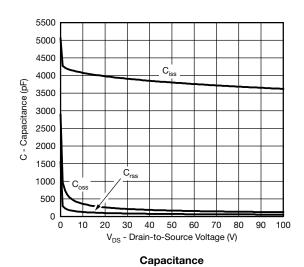
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

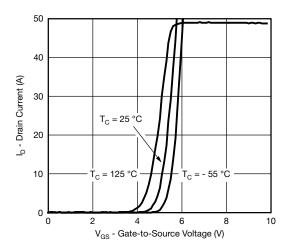


Output Characteristics

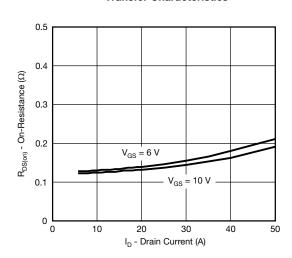


Transconductance

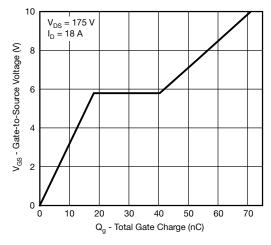




Transfer Characteristics



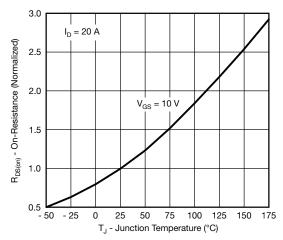
On-Resistance vs. Drain Current



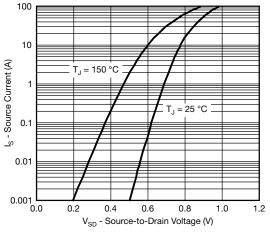
Gate Charge



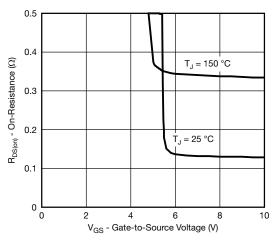
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



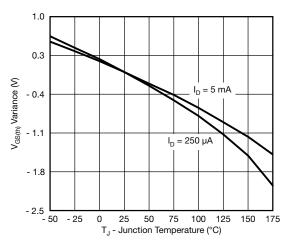
On-Resistance vs. Junction Temperature



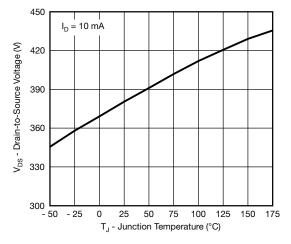
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

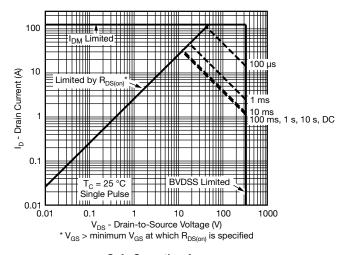


Threshold Voltage

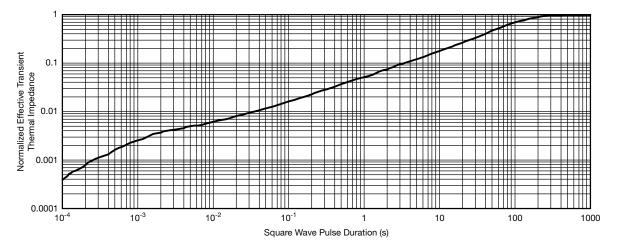


Drain Source Breakdown vs. Junction Temperature

THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



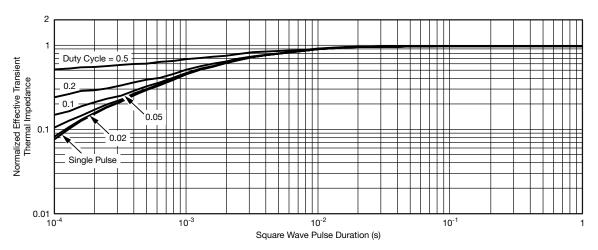
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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