Automotive N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at V_{GS} = 10 V	0.00470
$R_{DS(on)}$ (Ω) at V_{GS} = 4.5 V	0.00600
I_D (A)	120
Configuration	Single
Package	TO-262

FEATURES

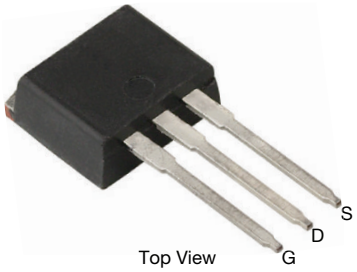
- TrenchFET® power MOSFET
- Package with low thermal resistance
- AEC-Q101 qualified [d]
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE

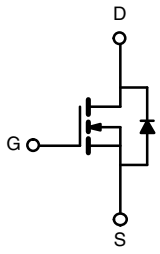
RoHS COMPLIANT

HALOGEN FREE

TO-262



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	T_C = 25 °C [a]	I_D	120	A
	T_C = 125 °C		91	
Continuous Source Current (Diode Conduction) [a]		I_S	120	
Pulsed Drain Current [b]		I_{DM}	300	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	80	
Single Pulse Avalanche Energy		E_{AS}	320	mJ
Maximum Power Dissipation [b]	T_C = 25 °C	P_D	250	W
	T_C = 125 °C		83	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount [c]	R_{thJA}	40	°C/W
Junction-to-Case (Drain)		R_{thJC}	0.6	

Notes

a. Package limited.
b. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
c. When mounted on 1" square PCB (FR4 material).
d. Parametric verification ongoing.

For technical questions, contact: automostechsupport@vishay.com

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SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static							
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0, I_D = 250 µA		60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA		1.5	2.0	2.5	
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS} = 0 V	V_{DS} = 60 V	-	-	1	µA
		V_{GS} = 0 V	V_{DS} = 60 V, T_J = 125 °C	-	-	50	
		V_{GS} = 0 V	V_{DS} = 60 V, T_J = 175 °C	-	-	500	
On-State Drain Current [a]	$I_{D(on)}$	V_{GS} = 10 V	V_{DS} ≥ 5 V	120	-	-	A
Drain-Source On-State Resistance [a]	$R_{DS(on)}$	V_{GS} = 10 V	I_D = 30 A	-	0.00378	0.00470	Ω
		V_{GS} = 10 V	I_D = 30 A, T_J = 125 °C	-	-	0.00801	
		V_{GS} = 10 V	I_D = 30 A, T_J = 175 °C	-	-	0.00992	
		V_{GS} = 4.5 V	I_D = 20 A	-	0.00481	0.00600	
Forward Transconductance [b]	g_{fs}	V_{DS} = 15 V, I_D = 30 A		-	118	-	S
Dynamic [b]							
Input Capacitance	C_{iss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	-	6705	8800	pF
Output Capacitance	C_{oss}			-	904	1200	
Reverse Transfer Capacitance	C_{rss}			-	555	800	
Total Gate Charge [c]	Q_g	V_{GS} = 10 V	V_{DS} = 30 V, I_D = 85 A	-	148	230	nC
Gate-Source Charge [c]	Q_{gs}			-	21.4	-	
Gate-Drain Charge [c]	Q_{gd}			-	33.2	-	
Gate Resistance	R_g	f = 1 MHz		0.45	0.99	1.5	Ω
Turn-On Delay Time [c]	$t_{d(on)}$	V_{DD} = 30 V, R_L = 0.353 Ω I_D ≅ 85 A, V_{GEN} = 10 V, R_g = 1 Ω		-	16	25	ns
Rise Time [c]	t_r			-	9	15	
Turn-Off Delay Time [c]	$t_{d(off)}$			-	46	75	
Fall Time [c]	t_f			-	12	20	
Source-Drain Diode Ratings and Characteristics [b]							
Pulsed Current [a]	I_{SM}			-	-	300	A
Forward Voltage	V_{SD}	I_F = 70 A, V_{GS} = 0		-	0.92	1.5	V

Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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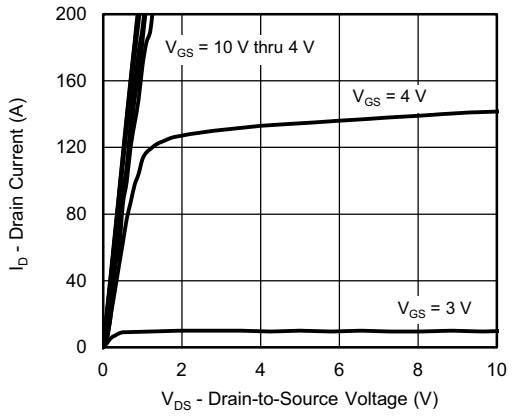
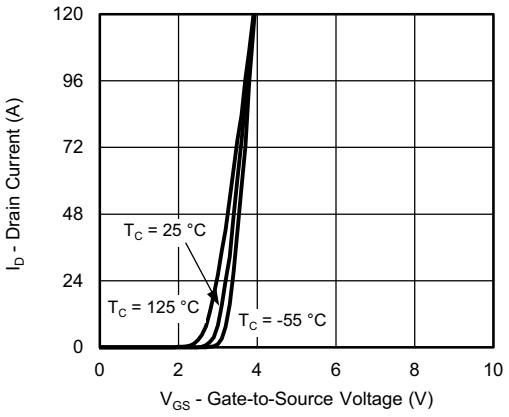
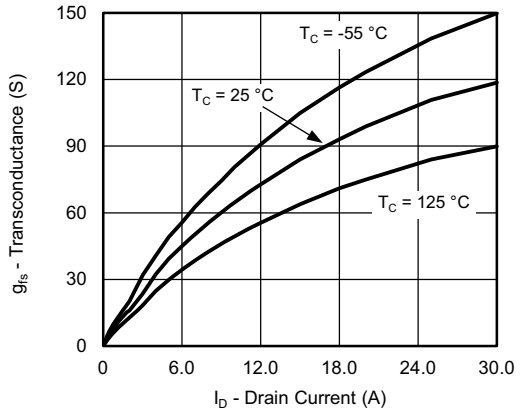
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



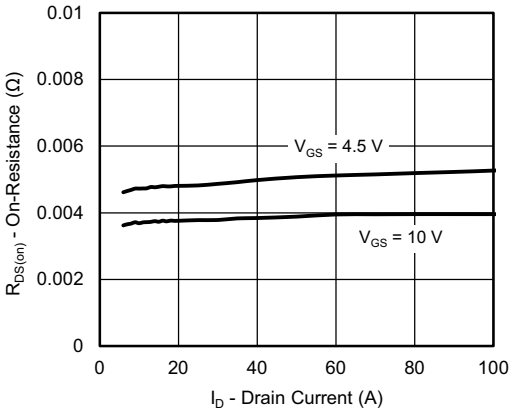
Output Characteristics



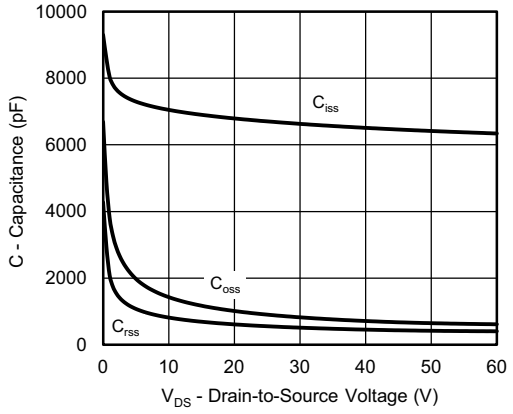
Transfer Characteristics



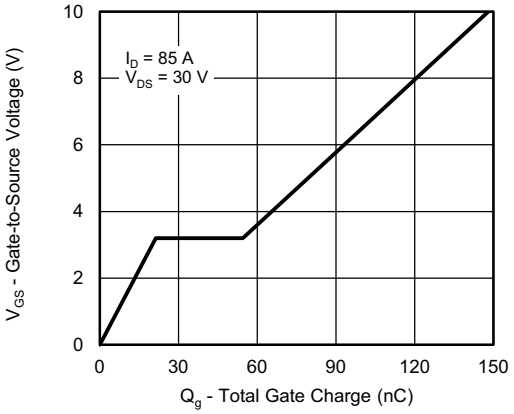
Transconductance



On-Resistance vs. Drain Current



Capacitance



Gate Charge

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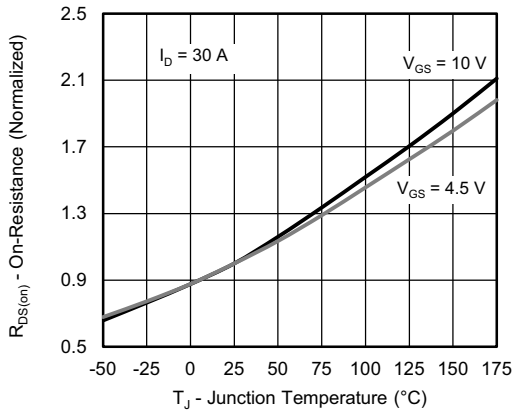
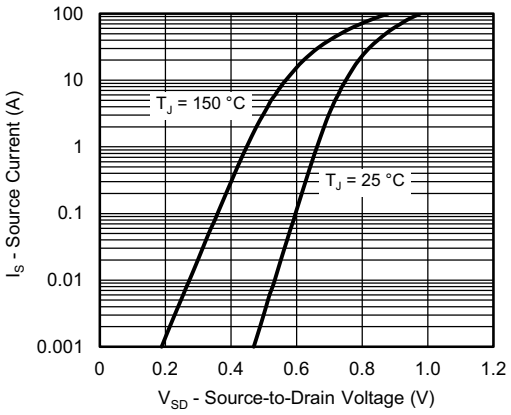
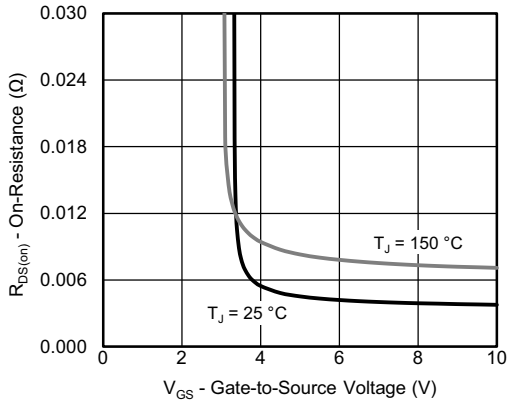


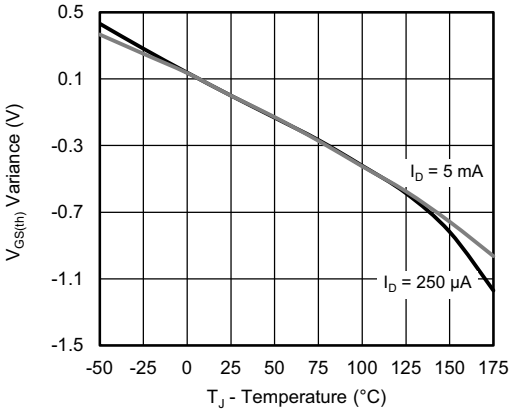
On-Resistance vs. Junction Temperature



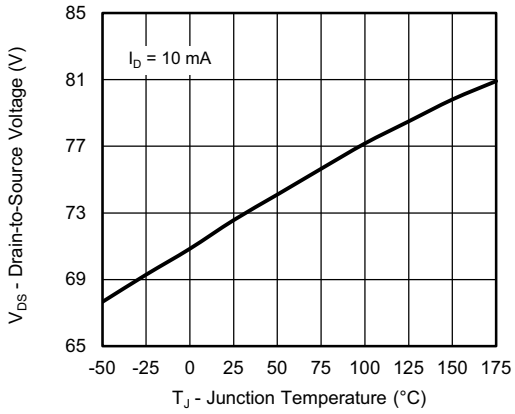
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

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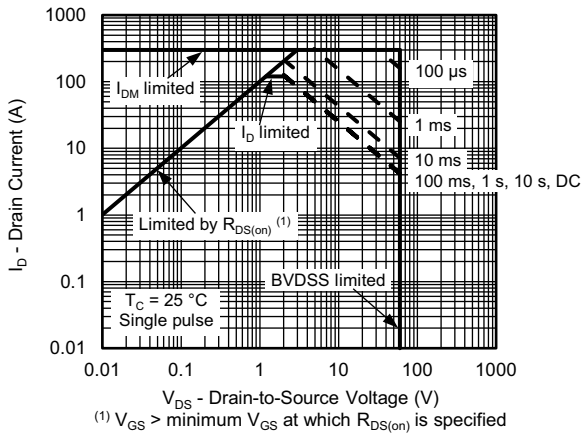
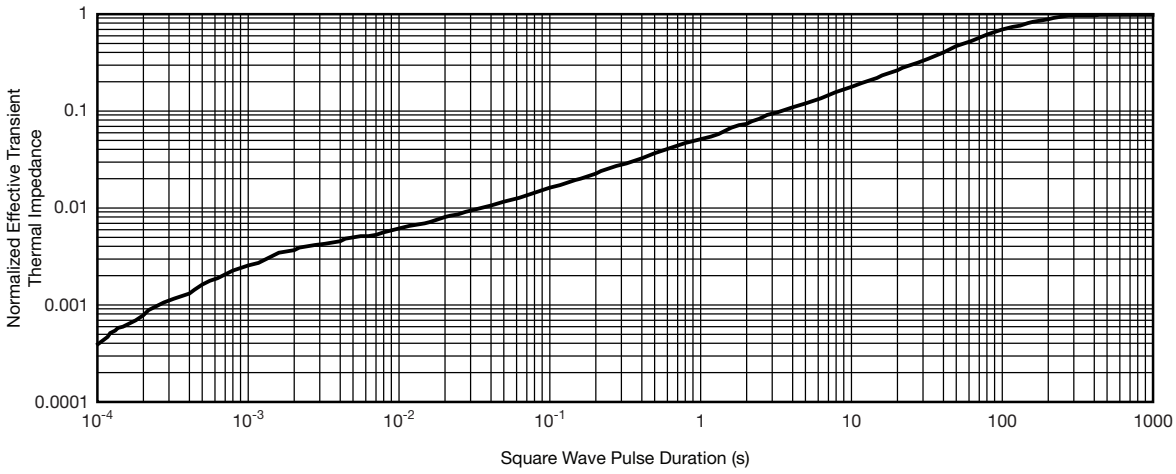


Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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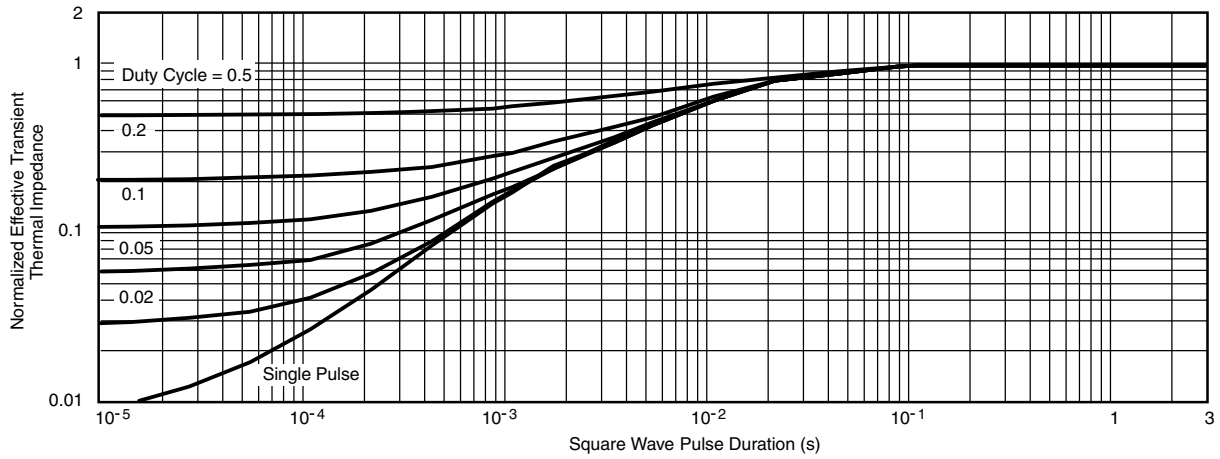

Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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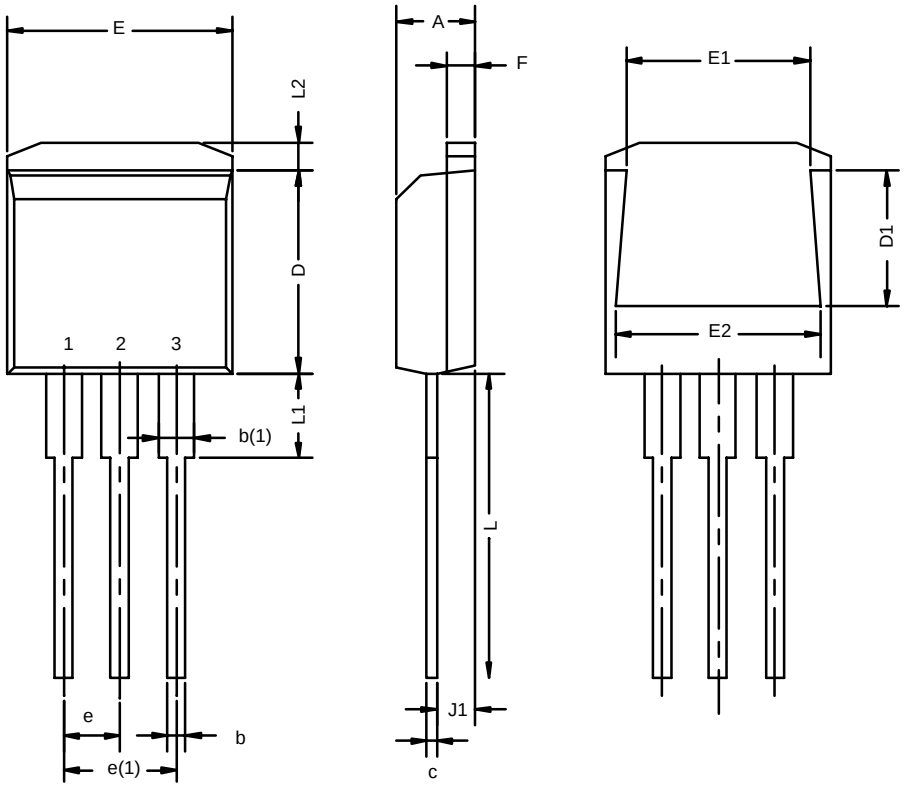
TO-262: 3-LEAD



Dim	MILLIMETERS*		INCHES	
	Min	Max	Min	Max
A	4.32	4.70	0.170	0.185
b	0.64	1.00	0.025	0.039
b(1)	1.14	1.40	0.045	0.055
c	0.36	0.50	0.014	0.020
D	8.64	9.65	0.340	0.380
D1	5.59	6.10	0.220	0.240
e	2.41	2.67	0.095	0.105
e(1)	4.95	5.33	0.195	0.210
E	10.03	10.41	0.395	0.410
E1	7.87	8.64	0.310	0.340
E2	9.02	9.53	0.355	0.375
F	1.14	1.40	0.045	0.055
J1	2.41	2.79	0.095	0.110
L	13.08	14.22	0.515	0.560
L1	-	3.81	-	0.150
L2	1.02	1.40	0.040	0.055
ECN: T-02234—Rev. C, 14-Oct-02 DWG: 5855				

*Use millimeters as the primary measurement

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