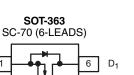


Vishay Siliconix

Dual N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)			
	0.198 at V _{GS} = 4.5 V	1.3 ^a				
20	0.225 at V _{GS} = 2.5 V	1.3 ^a	0.9 nC			
	0.263 at V _{GS} = 1.8 V	1.3 ^a				

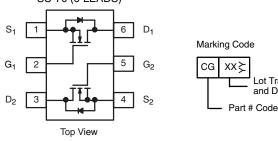


FEATURES •

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Typical ESD Protection 2100 V HBM
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

Load Switch for Portable Applications



D D C 1 kΩ $1 \ k\Omega$ G₁ C G_2 Ċ Ò S₁ S_2

Ordering Information:

Si1922EDH-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	20	V		
Gate-Source Voltage		V _{GS} ±8		V	
	T _C = 25 °C		1.3 ^a		
Continuous Drain Current (T 150 °C)	T _C = 70 °C		1.3 ^a		
Continuous Drain Current ($T_J = 150 \ ^{\circ}C$)	T _A = 25 °C	I _D	1.3 ^{a, b, c}		
	T _A = 70 °C		1.2 ^{b, c}	A	
Pulsed Drain Current	I _{DM}	4			
Continuous Source-Drain Diode Current	T _C = 25 °C	1	1		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	0.61 ^{b, c}	1	
	T _C = 25 °C		1.25		
Maximum Dawar Dissinction	T _C = 70 °C		0.8	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	0.74 ^{b, c}	vv	
	T _A = 70 °C		0.47 ^{b, c}	7	
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		

Lot Traceability

and Date Code

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	130	170	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	80	100	0/10		

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board. c. t = 5 s.

d. Maximum under steady state conditions is 220 °C/W.

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Parameter	Symbol	Min.	Тур.	Max.	Unit	
Static						•
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050		20		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μΑ		- 2.3		- mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	0.4		1	V
Cata Cauraa Laalka sa	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 25	μA
Gate-Source Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			1	
Zaro Cata Valtago Drain Current		$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5$ V, $V_{GS} = 4.5$ V	4			A
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 1 \text{ A}$		0.165	0.198	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 1 \text{ A}$	0.187		0.225	Ω
		$V_{GS} = 1.8 \text{ V}, I_D = 0.2 \text{ A}$		0.210	0.263	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 4 \text{ V}, \text{ I}_{D} = 1.5 \text{ A}$		4		S
Dynamic ^b						•
Total Cata Charge	0	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 8 \text{ V}, \text{ I}_{D} = 1.5 \text{ A}$		1.6	2.5	nC
Total Gate Charge	Qg			0.9	1.8	
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 1.5 A		0.1		
Gate-Drain Charge	Q _{gd}			0.2		
Gate Resistance	R _g	f = 1 MHz	0.4	1.9	3.8	kΩ
Turn-On Delay Time	ay Time t _{d(on)}			43	65	
Rise Time	t _r	V_{DD} = 10 V, R_L = 8.3 Ω		80	120	- ns
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ 1.2 A, ${\rm V}_{\rm GEN}$ = 4.5 V, ${\rm R}_{\rm g}$ = 1 Ω		480	720	
Fall Time	t _f			220	330	
Turn-on Delay Time	t _{d(on)}			22	33	
Rise Time	tr	V_{DD} = 10 V, R_L = 8.3 Ω		46	70	
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong {\rm 1.2~A},~{\rm V}_{\rm GEN} = {\rm 8~V},~{\rm R}_{\rm g} = {\rm 1}~\Omega$		645	968	
Fall Time	tr			215	323	
Drain-Source Body Diode Characteristic	cs					
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			1	
Pulse Diode Forward Current	I _{SM}				4	A
Body Diode Voltage	V _{SD}	$I_{\rm S}$ = 1.2 A, $V_{\rm GS}$ = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			9	18	ns
Body Diode Beverse Becovery Charge Or				2	4	nC
		$I_F = 1.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		5		
Reverse Recovery Rise Time	t _b			4	1	ns

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

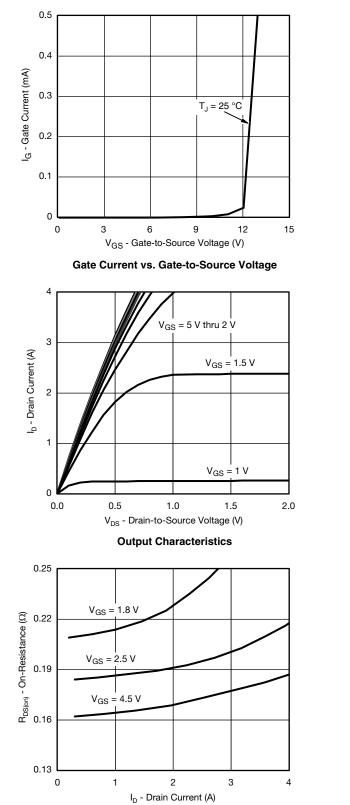
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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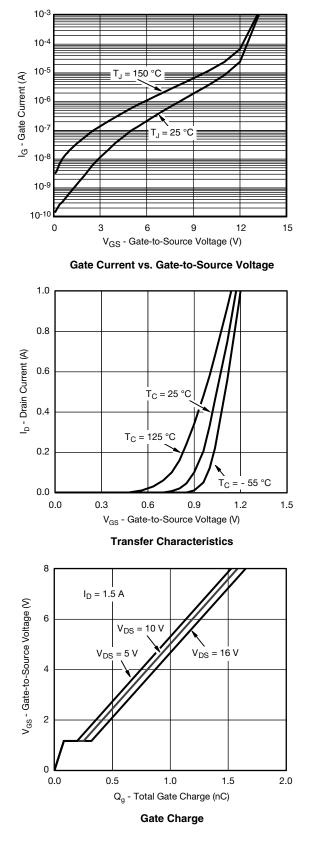


Si1922EDH Vishay Siliconix

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Drain Current



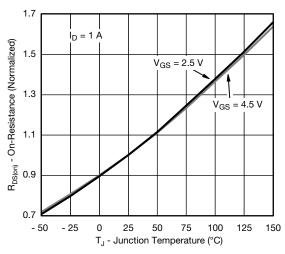
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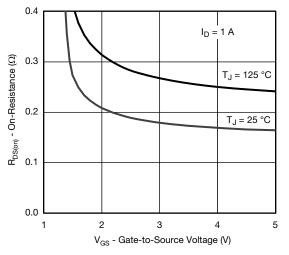
Vishay Siliconix



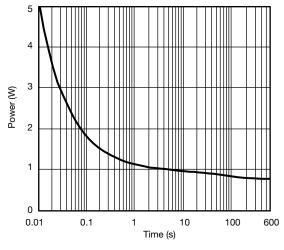
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



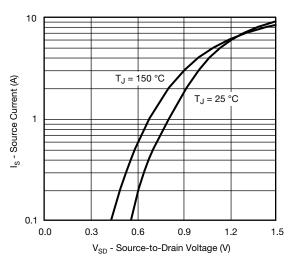
On-Resistance vs. Junction Temperature



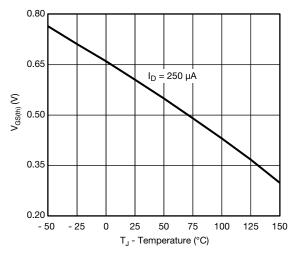
On-Resistance vs. Gate-to-Source Voltage



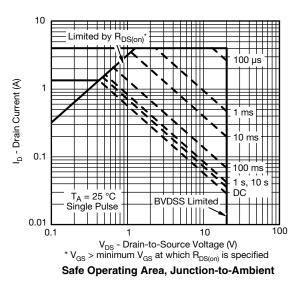
Single Pulse Power, Junction-to-Ambient



Source-Drain Diode Forward Voltage



Threshold Voltage

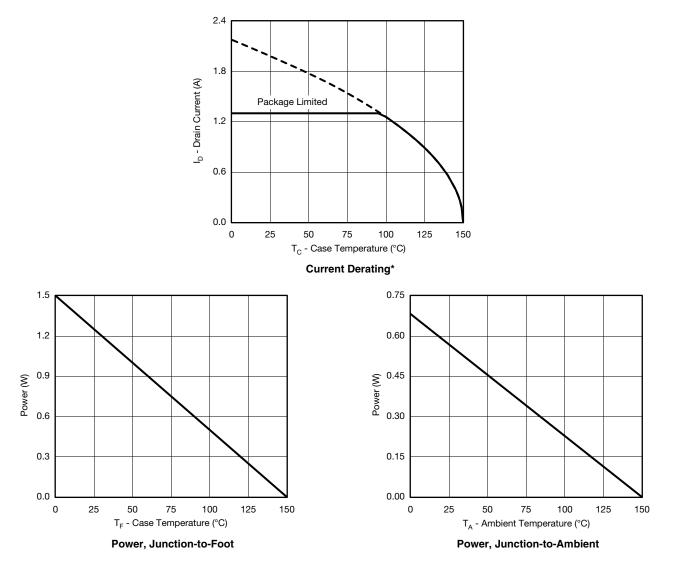


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



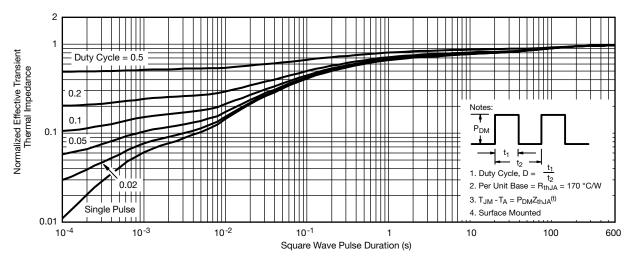
* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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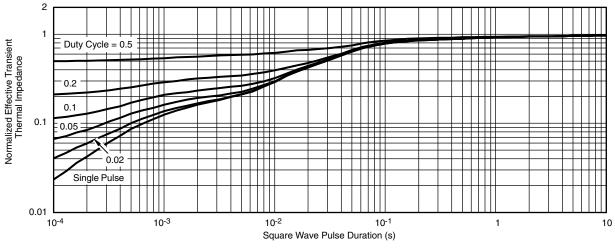
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?67192</u>.

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Package Information Vishay Siliconix

SC-70: 6-LEADS





	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	-	0.043
A ₁	-	-	0.10	-	-	0.004
A ₂	0.80	-	1.00	0.031	-	0.039
b	0.15	-	0.30	0.006	-	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Е	1.80	2.10	2.40	0.071	0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC			0.026BSC		
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
٩	7°Nom			7°Nom		
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550						

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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Vishay

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