



# Automotive N-Channel 30 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY					
V <sub>DS</sub> (V)	30				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0175				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.0213				
I <sub>D</sub> (A)	8				
Configuration	Single				

#### **FEATURES**

- TrenchFET® Power MOSFET
- AEC-Q101 Qualifiedd
- 100 % R<sub>a</sub> and UIS Tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912







(3) G	(1, 2, 5, 6) D
(4) S	

N-Channel MOSFET

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3410EV-T1 (for detailed order number please see <a href="https://www.vishay.com/doc?79771">www.vishay.com/doc?79771</a> )

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	Drain-Source Voltage			V		
Gate-Source Voltage	Gate-Source Voltage			V		
Continuous Drain Current	T <sub>C</sub> = 25 °C <sup>a</sup>		8			
Continuous Drain Current	T <sub>C</sub> = 125 °C	l <sub>D</sub>	6.8			
Continuous Source Current (Diode Conduction)	Continuous Source Current (Diode Conduction)			Α		
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	32				
Single Pulse Avalanche Current		I <sub>AS</sub>	22			
Single Pulse Avalanche Energy	L = 0.1 mH		24	mJ		
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	P <sub>D</sub>	5	W		
Maximum i Ower Dissipation	T <sub>C</sub> = 125 °C		1.6	VV		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C			

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient Po	CB Mount <sup>c</sup>	$R_{thJA}$	110	°C/W
Junction-to-Foot (Drain)		$R_{thJF}$	30	C/VV

#### Notes

- a. Package limited
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %
- c. When mounted on 1" square PCB (FR-4 material)
- d. Parametric verification ongoing



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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		30	-	=-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	= V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.5	2.0	2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	$0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 30 V	-	-	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 30 V, T <sub>J</sub> = 125 °C	-	-	50	μΑ
		$V_{GS} = 0 V$	V <sub>DS</sub> = 30 V, T <sub>J</sub> = 175 °C	-	-	150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> ≥ 5 V	10	-	=-	Α
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A	-	0.014	0.0175	
Drain-Source On-State Resistance <sup>a</sup>	Б	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A, T <sub>J</sub> = 125 °C	=.	-	0.030	Ω
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A, T <sub>J</sub> = 175 °C	-	-	0.036	
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 4 A	-	0.017	0.0213	
Forward Transconductanceb	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5 A		-	25	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			=.	804	1005	pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 15 V, f = 1 MHz	=.	175	219	
Reverse Transfer Capacitance	C <sub>rss</sub>			=.	68	85	
Total Gate Charge <sup>c</sup>	Qg			-	14	21	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$V_{DS} = 15 \text{ V}, I_{D} = 5 \text{ A}$	-	2.4	-	nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>	]		-	2.2	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.5	3.91	7	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	9	14	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3 $\Omega$ $I_D$ $\cong$ 5 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		-	12	18	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	20	30	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	7	11	
Source-Drain Diode Ratings and Chara	icteristics <sup>b</sup>						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	32	Α
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 5 A, V <sub>GS</sub> = 0 V		_	0.8	1.2	V

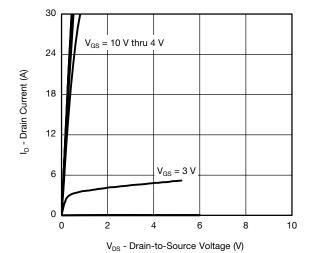
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

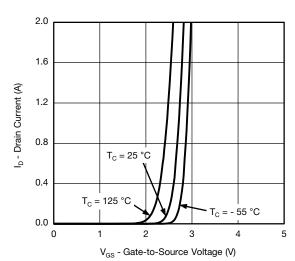
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



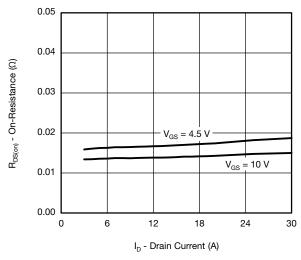
### TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



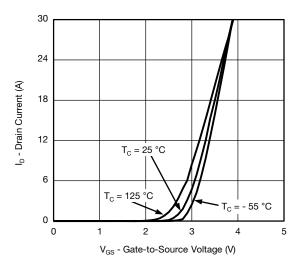
#### **Output Characteristics**



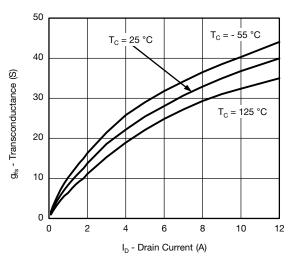
#### **Transfer Characteristics**



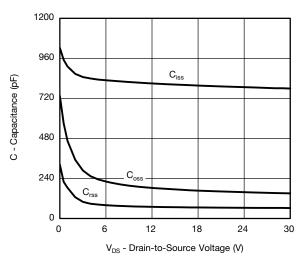
#### On-Resistance vs. Drain Current



#### **Transfer Characteristics**



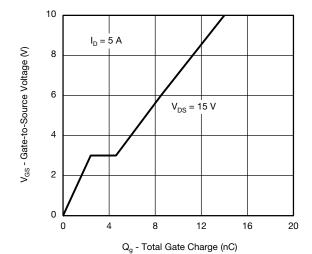
#### Transconductance



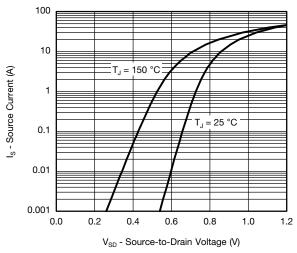
Capacitance



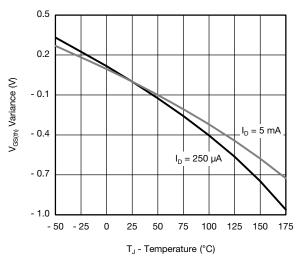
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



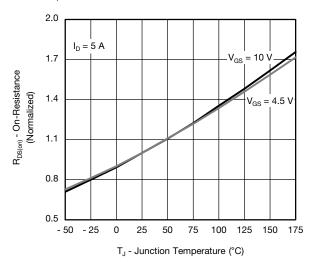
#### **Gate Charge**



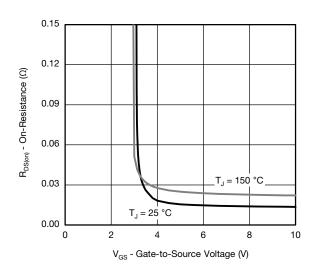
#### Source Drain Diode Forward Voltage



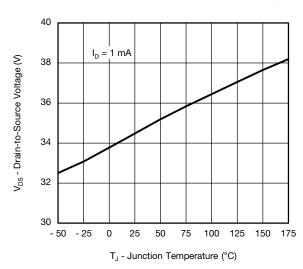
**Threshold Voltage** 



On-Resistance vs. Junction Temperature



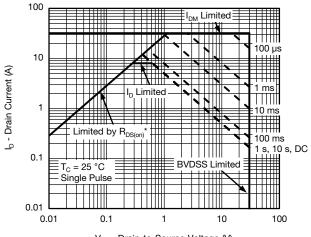
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

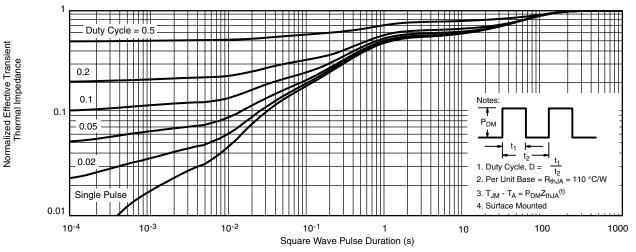


### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



 $V_{DS} \text{ - Drain-to-Source Voltage (V)} \\ ^*V_{GS} \text{ > minimum } V_{GS} \text{ at which } R_{DS(on)} \text{ is specified}$ 

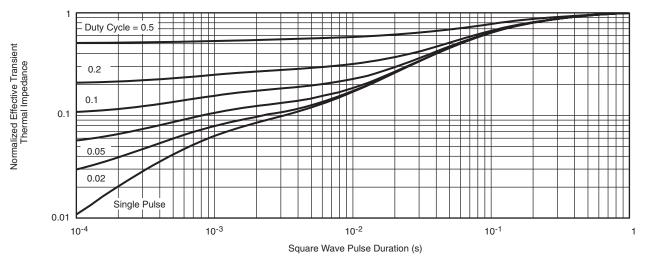
#### Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



## **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

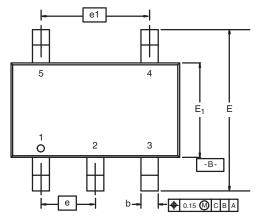
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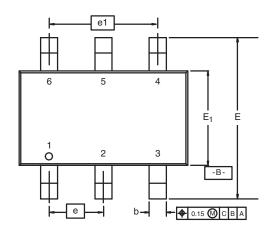




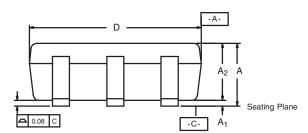
TSOP: 5/6-LEAD

**JEDEC Part Number: MO-193C** 

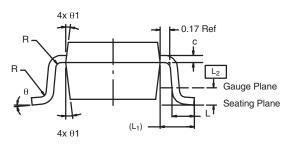




**5-LEAD TSOP** 





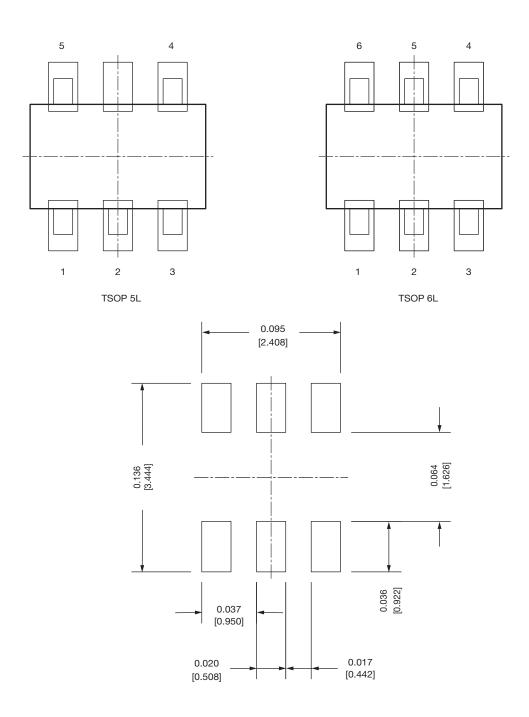


	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min Nom		Max	
Α	0.91	-	1.10	0.036	-	0.043	
A <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004	
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC			0.0374 BSC		
e <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L <sub>1</sub>		0.60 Ref		0.024 Ref			
L <sub>2</sub>		0.25 BSC			0.010 BSC		
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ1		7° Nom		7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200 18-Dec-06



# Recommended Land Pattern For TSOP-5L / TSOP-6L



#### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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