

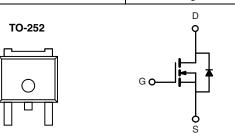
Top View

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Vishay Siliconix

Automotive N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	30			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0032			
I _D (A)	50			
Configuration	Single			



FEATURES

- TrenchFET® Power MOSFET
- AEC-Q101 Qualifiedd
- 100 % R_q and UIS Tested
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912



HALOGEN FREE

ORDERING INFORMATION	
Package	TO-252
Lead (Pb)-free and Halogen-free	SQD50N03-4m3-GE3

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	30	V	
Gate-Source Voltage		V_{GS}	± 20	V	
Continuous Drain Current ^a	T _C = 25 °C	I _D	50		
	T _C = 125 °C		50		
Continuous Source Current (Diode Conduction) ^a		I _S	50	Α	
Pulsed Drain Current ^b		I _{DM}	200		
Single Pulse Avalanche Current	L = 0.1 mH	las	58		
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	168	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	P _D	136	W	
	T _C = 125 °C		45	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	50	°C/W	
Junction-to-Case (Drain)		R _{thJC}	1.1	C/VV	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static					•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		30	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	· V _{GS} , I _D = 250 μA	2.5	3.0	3.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} =	0 V, V _{GS} = ± 20 V	=	-	± 100	nA
		V _{GS} = 0 V	V _{DS} = 30 V	=	-	1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 30 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 175 °C	=.	-	150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α
		V _{GS} = 10 V	I _D = 20 A	=	0.0026	0.0032	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 20 A, T _J = 125 °C	-	-	0.0048	Ω
	$V_{GS} = 10 \text{ V}$ $I_D = 20 \text{ A}, T_J = 175 \text{ °C}$	=.	-	0.0057			
Forward Transconductanceb	9 _{fs}	V _{DS} = 20 V, I _D = 20 A		=.	108	-	S
Dynamic ^b							
Input Capacitance	C _{iss}			=	5879	7349	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = 15 \text{ V}, f = 1 \text{ MHz}$	=	942	1178	pF
Reverse Transfer Capacitance	C _{rss}			=	413	516	
Total Gate Charge ^c	Qg			=.	82.7	124	
Gate-Source Charge ^c	Q_{gs}	V _{GS} = 10 V	$V_{DS} = 15 \text{ V}, I_D = 50 \text{ A}$	=	23.8	-	nC
Gate-Drain Charge ^c	Q_{gd}			=	14.1	-	
Gate Resistance	R _g	f = 1 MHz		0.9	1.9	2.9	Ω
Turn-On Delay Time ^c	t _{d(on)}			=.	13	20	
Rise Time ^c	t _r	$V_{DD} = 15 \text{ V}, \text{ R}_L = 0.3 \Omega$ $I_D \cong 50 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		=.	10	15	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	37	56	
Fall Time ^c	t _f			=	10	15	
Source-Drain Diode Ratings and Char-	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	200	Α
Forward Voltage	V _{SD}	I _F = 30 A, V _{GS} = 0 V		-	0.84	1.2	V

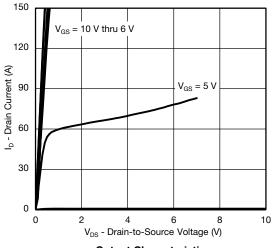
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

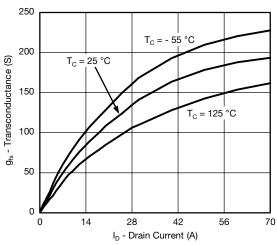
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



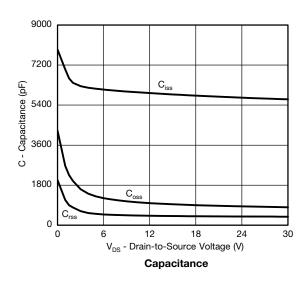
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

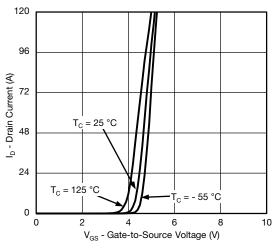


Output Characteristics

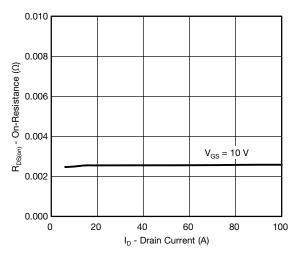


Transconductance

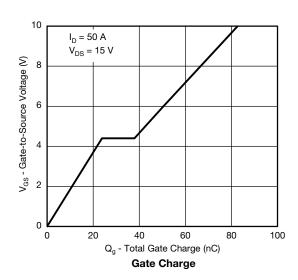




Transfer Characteristics

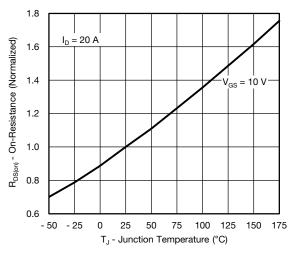


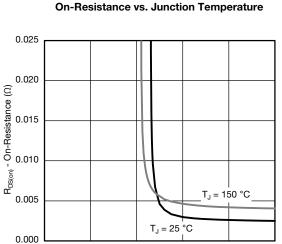
On-Resistance vs. Drain Current



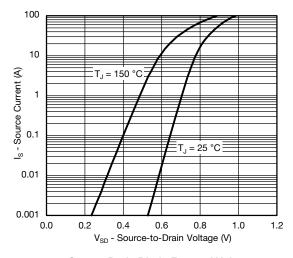


TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

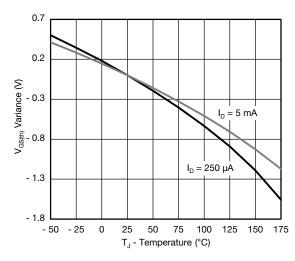




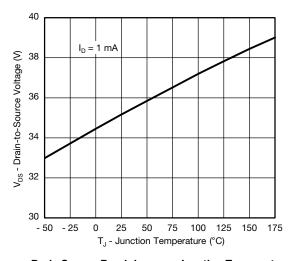
V_{GS} - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Voltage



Source Drain Diode Forward Voltage



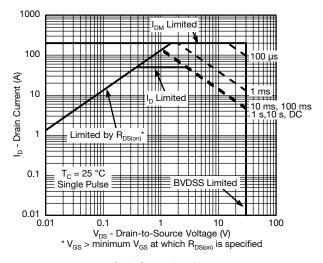
Threshold Voltage



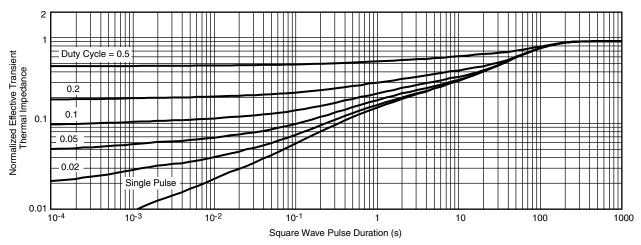
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



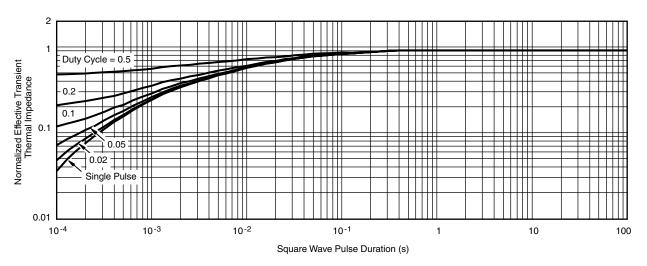
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

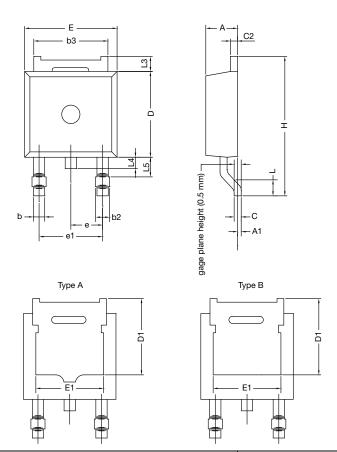
Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg267473.



TO-252AA Case Outline



MILLIN	MILLIMETERS			
MIN.	MAX.	MIN.	MAX.	
2.18	2.38	0.086	0.094	
-	0.127	-	0.005	
0.64	0.88	0.025	0.035	
0.76	1.14	0.030	0.045	
4.95	5.46	0.195	0.215	
0.46	0.61	0.018	0.024	
0.46	0.89	0.018	0.035	
5.97	6.22	0.235	0.245	
4.10	-	0.161	-	
6.35	6.73	0.250	0.265	
4.32	-	0.170	-	
9.40	10.41	0.370	0.410	
2.28	BSC	0.090 BSC		
4.56 BSC		0.180 BSC		
1.40	1.78	0.055	0.070	
0.89	1.27	0.035	0.050	
=	1.02	-	0.040	
1.01	1.52	0.040	0.060	
	MIN. 2.18 - 0.64 0.76 4.95 0.46 0.46 5.97 4.10 6.35 4.32 9.40 2.28 4.56	MIN. MAX. 2.18 2.38 - 0.127 0.64 0.88 0.76 1.14 4.95 5.46 0.46 0.61 0.46 0.89 5.97 6.22 4.10 - 6.35 6.73 4.32 - 9.40 10.41 2.28 BSC 4.56 BSC 1.40 1.78 0.89 1.27 - 1.02 1.01 1.52	MIN. MAX. MIN. 2.18 2.38 0.086 - 0.127 - 0.64 0.88 0.025 0.76 1.14 0.030 4.95 5.46 0.195 0.46 0.61 0.018 0.46 0.89 0.018 5.97 6.22 0.235 4.10 - 0.161 6.35 6.73 0.250 4.32 - 0.170 9.40 10.41 0.370 2.28 BSC 0.090 4.56 BSC 0.180 1.40 1.78 0.055 0.89 1.27 0.035 - 1.02 - 1.01 1.52 0.040	

ECN: T25-0122-Rev. C, 12-May-2025 DWG: 6019

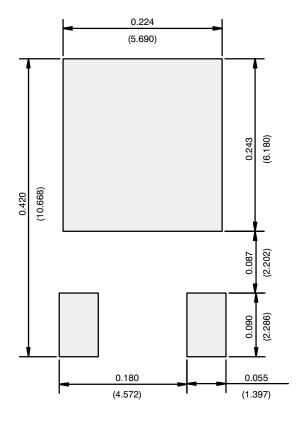
Notes

- Dimension L3 is for reference only
- Dimension D1 and E1 on type A and B is the same

Revision: 12-May-2025 1 Document Number: 64424



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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