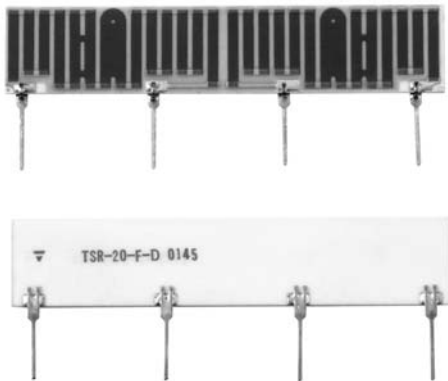


# Thick Film Surge Resistor Networks, Single-In-Line



The TSR surge resistor from Vishay Techno is used to protect sensitive components and circuits from the surges introduced by lightning strikes and power cross conditions.

The proprietary thick film technology used in the TSR can dissipate a large amount of energy during a short transient condition. These networks are designed to meet the applicable requirement of Bellcore GR-1089 and ITU-T K.20. The TSR is available in large quantities with a short lead-time.

## FEATURES

- Secondary protection for telecon line cards
- Lightning protection to Bellcore GR-1089 and ITU-T K.20
- Optional version with thermal fuse
- Custom designs available
- Material categorization:  
For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



## Note

\* This datasheet provides information about parts that are RoHS-compliant and/or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information/tables in this datasheet for details.

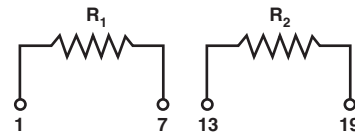
## LIGHTNING SURGE TESTS

**Bellcore Spec. GR-1089:** 10 x 1000  $\mu$ s 1 kV, 2 x 10  $\mu$ s 2.5 kV

**ITU-T K.20:** 10 x 700  $\mu$ s 2 kV

**Power Cross Test:** Per Bellcore spec.

## SCHEMATICS



## STANDARD ELECTRICAL SPECIFICATIONS

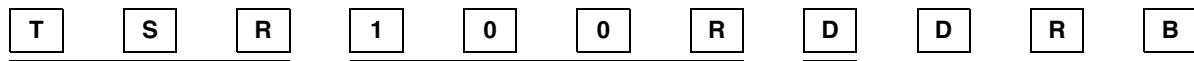
GLOBAL MODEL	POWER RATING ELEMENT $P_{25^{\circ}\text{C}}$ W	STANDARD RESISTANCE VALUES <sup>(1)</sup> $\Omega$ [ $R_1 = R_2$ ]	TOLERANCE <sup>(2)</sup> $\pm$ %	RATIO TOLERANCE <sup>(2)</sup> $\pm$ %	TEMPERATURE COEFFICIENT <sup>(2)</sup> (-55 $^{\circ}$ C to +125 $^{\circ}$ C) $\pm$ ppm/ $^{\circ}$ C	PULSE
TSR	2	24, 50, 100, 200	0.5	0.5	100	31 kW to 312 kW (value dependent)

## Notes

- <sup>(1)</sup> Other values available on special order  
<sup>(2)</sup> Contact factory for tighter specifications

## GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: **TSR100RDDR** (preferred part number format)



GLOBAL MODEL	RESISTANCE VALUE	TOLERANCE	RATIO TOLERANCE	TERMINAL FINISH	PACKAGING
TSR	R = $\Omega$ 20R = 20 $\Omega$ 100R = 100 $\Omega$	C = $\pm$ 0.25 % D = $\pm$ 0.5 % F = $\pm$ 1 %	C = $\pm$ 0.25 % D = $\pm$ 0.5 % F = $\pm$ 1 %	R = Sn60/Pb40 C = Sn95.5/Ag3.9/Cu0.6	B = Bulk S = Strip

Historical Part Numbering: **TSR100DD** (will continue to be accepted)

HISTORICAL MODEL	RESISTANCE VALUE	TOLERANCE	RATIO TOLERANCE	TERMINAL FINISH
TSR	100	D	D	

**ENVIRONMENTAL SPECIFICATIONS** (typical)

Tests per MIL-STD-202

**Resistance to Solvents:** No marking deterioration

**Resistance to Solder Heat:**  $\pm 0.5\%$  or  $0.5\ \Omega$  whichever is greater

**Solderability:**  $> 95\%$  coverage

**Insulation Resistance:** 10 M $\Omega$  minimum (isolated pins)

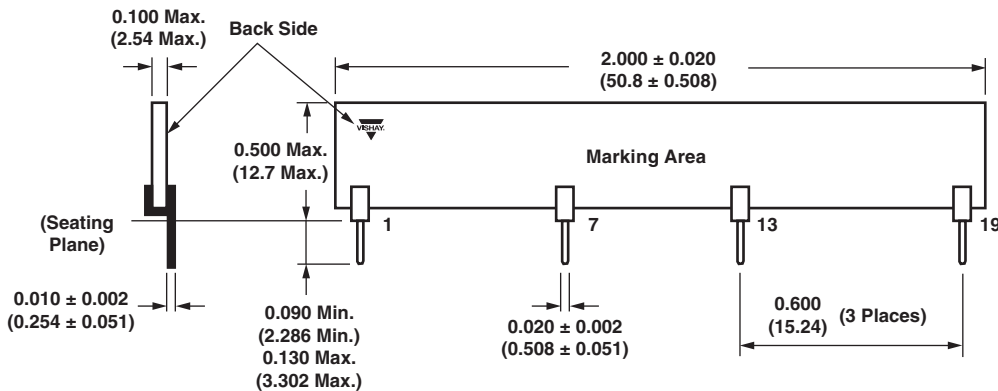
**Bias Humidity Test:** 50 V, 85 % relative humidity, 85 °C

**MECHANICAL SPECIFICATIONS**

<b>Type</b>	Ceramic SIP
<b>Thick Film Element</b>	96 % alumina
<b>Terminals</b>	Tinned copper alloy

**MARKING**

- Complete part number
- Manufacturer's name/code
- Date code
- Pin #1 identifier

**DIMENSIONS** in inches (millimeters)




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