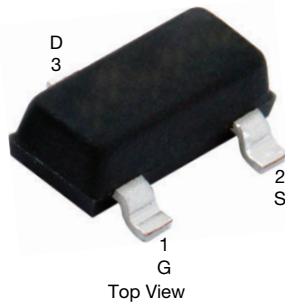


P-Channel 12 V (D-S) MOSFET

SOT-23 (TO-236)



Marking code: O3

| PRODUCT SUMMARY | |
|---|--------|
| V_{DS} (V) | -12 |
| $R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5$ V | 0.035 |
| $R_{DS(on)}$ max. (Ω) at $V_{GS} = -2.5$ V | 0.045 |
| $R_{DS(on)}$ max. (Ω) at $V_{GS} = -1.8$ V | 0.059 |
| Q_g typ. (nC) | 9 |
| I_D (A) ^a | -5.1 |
| Configuration | Single |

| ORDERING INFORMATION | |
|---------------------------------|------------------|
| Package | SOT-23 (TO-236) |
| Lead (Pb)-free | Si2333CDS-T1-E3 |
| Lead (Pb)-free and halogen-free | Si2333CDS-T1-GE3 |

| ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted) | | | | |
|---|---------------|----------------|-----------------------|------|
| PARAMETER | | SYMBOL | LIMIT | UNIT |
| Drain-source voltage | | V_{DS} | -12 | V |
| Gate-source voltage | | V_{GS} | ± 8 | |
| Continuous drain current ($T_J = 150$ °C) | $T_C = 25$ °C | I_D | -7.1 | A |
| | $T_C = 70$ °C | | -5.7 | |
| | $T_A = 25$ °C | | -5.1 ^{b, c} | |
| | $T_A = 70$ °C | | -4.0 ^{b, c} | |
| Pulsed drain current | | I_{DM} | -20 | |
| Continuous source-drain diode current | $T_C = 25$ °C | I_S | -1.0 | |
| | $T_A = 25$ °C | | -0.63 ^{b, c} | |
| Maximum power dissipation | $T_C = 25$ °C | P_D | 2.5 | W |
| | $T_C = 70$ °C | | 1.6 | |
| | $T_A = 25$ °C | | 1.25 ^{b, c} | |
| | $T_A = 70$ °C | | 0.8 ^{b, c} | |
| Operating junction and storage temperature range | | T_J, T_{stg} | -55 to +150 | °C |

| THERMAL RESISTANCE RATINGS | | | | | |
|---|--------------|------------|---------|---------|------|
| PARAMETER | | SYMBOL | TYPICAL | MAXIMUM | UNIT |
| Maximum junction-to-ambient ^{b, d} | $t \leq 5$ s | R_{thJA} | 75 | 100 | °C/W |
| Maximum junction-to-case (drain) | Steady state | R_{thJC} | 40 | 50 | |

Notes

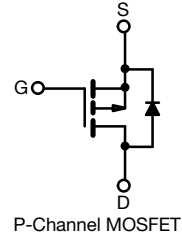
- $T_C = 25$ °C
- Surface mounted on 1" x 1" FR4 board
- $t = 5$ s
- Maximum under steady state conditions is 166 °C/W

FEATURES

- TrenchFET® power MOSFET
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Load switch
- PA switch


RoHS
 COMPLIANT
 HALOGEN
FREE
 Available


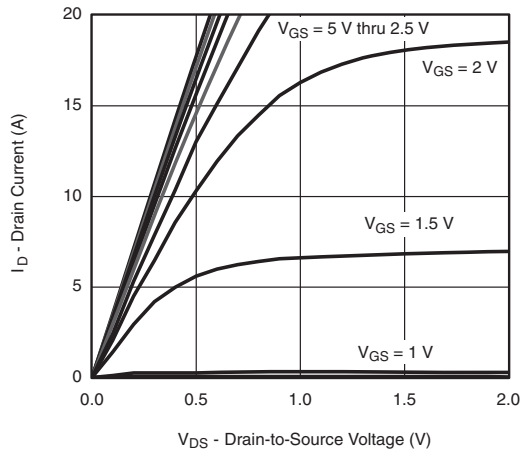
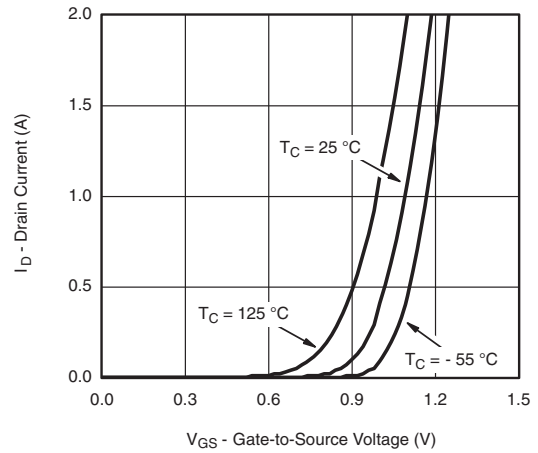
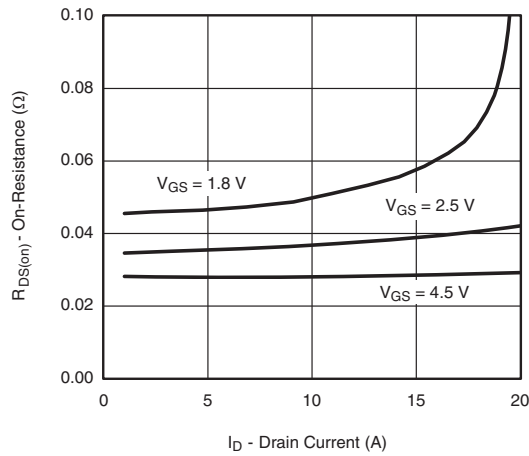
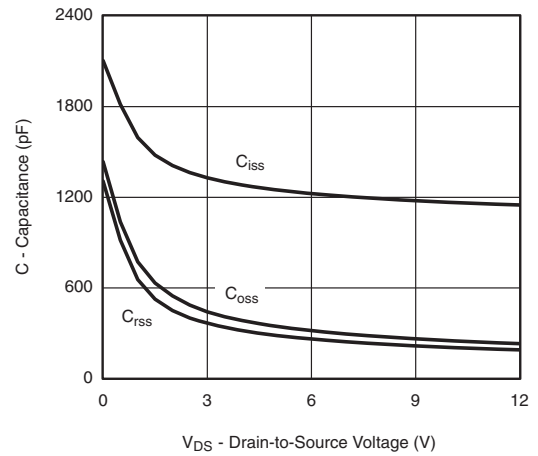
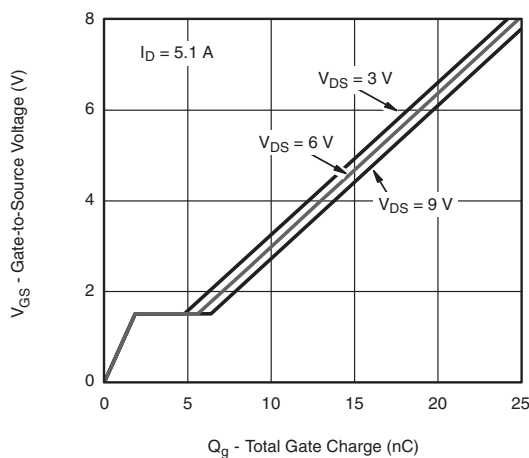
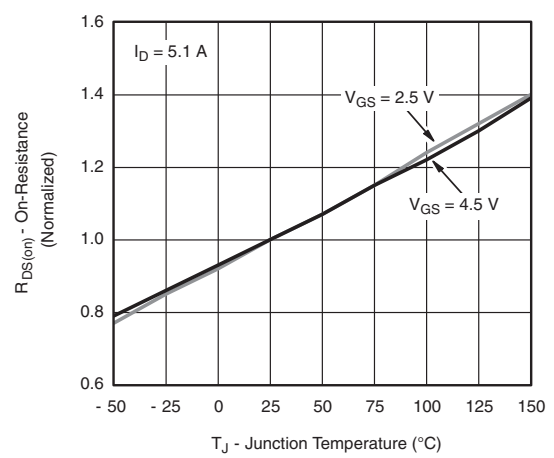


| SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted) | | | | | | |
|---|-------------------------|---|------|--------|-----------|----------------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
| Static | | | | | | |
| Drain-source breakdown voltage | V_{DS} | $V_{GS} = 0\text{ V}$, $I_D = -250\text{ }\mu\text{A}$ | -12 | - | - | V |
| V_{DS} temperature coefficient | $\Delta V_{DS}/T_J$ | $I_D = -250\text{ }\mu\text{A}$ | - | -13 | - | mV/ $^\circ\text{C}$ |
| $V_{GS(th)}$ temperature coefficient | $\Delta V_{GS(th)}/T_J$ | | - | 2.6 | - | |
| Gate-source threshold voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}$, $I_D = -250\text{ }\mu\text{A}$ | -0.4 | - | -1 | V |
| Gate-source leakage | I_{GSS} | $V_{DS} = 0\text{ V}$, $V_{GS} = \pm 8\text{ V}$ | - | - | ± 100 | nA |
| Zero gate voltage drain current | I_{DSS} | $V_{DS} = -12\text{ V}$, $V_{GS} = 0\text{ V}$ | - | - | -1 | μA |
| | | $V_{DS} = -12\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 55\text{ }^\circ\text{C}$ | - | - | -10 | |
| On-state drain current ^a | $I_{D(on)}$ | $V_{DS} \leq -5\text{ V}$, $V_{GS} = -4.5\text{ V}$ | -20 | - | - | A |
| Drain-source on-state resistance ^a | $R_{DS(on)}$ | $V_{GS} = -4.5\text{ V}$, $I_D = -5.1\text{ A}$ | - | 0.0285 | 0.035 | Ω |
| | | $V_{GS} = -2.5\text{ V}$, $I_D = -4.5\text{ A}$ | - | 0.036 | 0.045 | |
| | | $V_{GS} = -1.8\text{ V}$, $I_D = -2.0\text{ A}$ | - | 0.046 | 0.059 | |
| Forward transconductance ^a | g_{fs} | $V_{DS} = -5\text{ V}$, $I_D = -5.3\text{ A}$ | - | 18.5 | - | S |
| Dynamic ^b | | | | | | |
| Input capacitance | C_{ISS} | $V_{DS} = -6\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$ | - | 1225 | - | pF |
| Output capacitance | C_{OSS} | | - | 315 | - | |
| Reverse transfer capacitance | C_{RSS} | | - | 260 | - | |
| Total gate charge | Q_g | $V_{DS} = -6\text{ V}$, $V_{GS} = -4.5\text{ V}$, $I_D = -5.1\text{ A}$ | - | 15 | 25 | nC |
| | | $V_{DS} = -6\text{ V}$, $V_{GS} = -2.5\text{ V}$, $I_D = -5.1\text{ A}$ | - | 9 | 15 | |
| Gate-source charge | Q_{gs} | $V_{DS} = -6\text{ V}$, $V_{GS} = -2.5\text{ V}$, $I_D = -5.1\text{ A}$ | - | 1.9 | - | |
| Gate-drain charge | Q_{gd} | $V_{DS} = -6\text{ V}$, $V_{GS} = -2.5\text{ V}$, $I_D = -5.1\text{ A}$ | - | 3.8 | - | |
| Gate resistance | R_g | $f = 1\text{ MHz}$ | - | 4 | - | Ω |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD} = -6\text{ V}$, $R_L = 6\text{ }\Omega$, $I_D = -1\text{ A}$, $V_{GEN} = -4.5\text{ V}$, $R_g = 1\text{ }\Omega$ | - | 13 | 20 | ns |
| Rise time | t_r | | - | 35 | 60 | |
| Turn-off delay time | $t_{d(off)}$ | | - | 45 | 70 | |
| Fall time | t_f | | - | 12 | 20 | |
| Drain-Source Body Diode Characteristics | | | | | | |
| Continuous source-drain diode current | I_S | $T_C = 25\text{ }^\circ\text{C}$ | - | - | -1 | A |
| Pulse diode forward current ^a | I_{SM} | | - | - | -20 | |
| Body diode voltage | V_{SD} | $I_S = -1.0\text{ A}$ | - | -0.7 | -1.2 | V |
| Body diode reverse recovery time | t_{rr} | $I_F = -1.0\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, $T_J = 25\text{ }^\circ\text{C}$ | - | 32 | 50 | ns |
| Body diode reverse recovery charge | Q_{rr} | | - | 20 | 40 | nC |
| Reverse recovery fall time | t_a | | - | 16 | - | ns |
| Reverse recovery rise time | t_b | | - | 16 | - | |

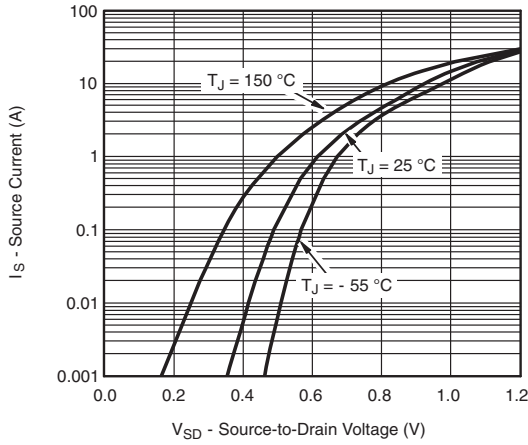
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing

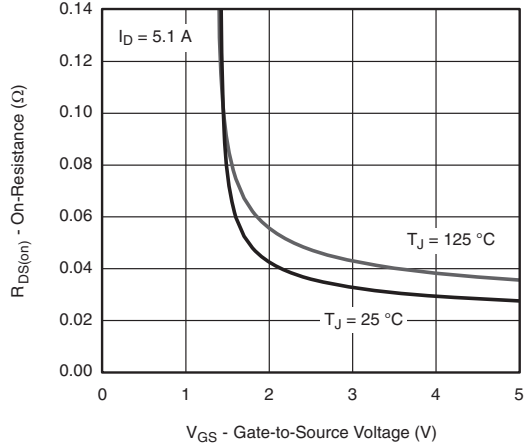
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current and Gate Voltage

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

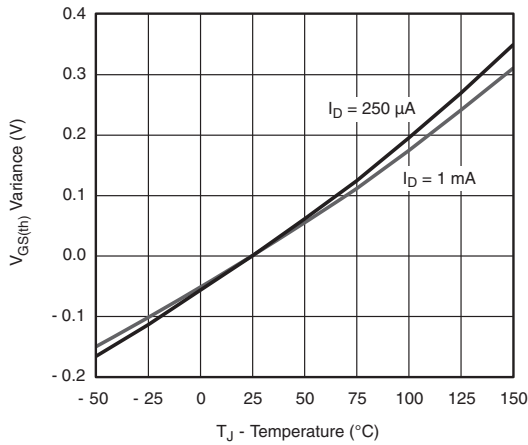
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



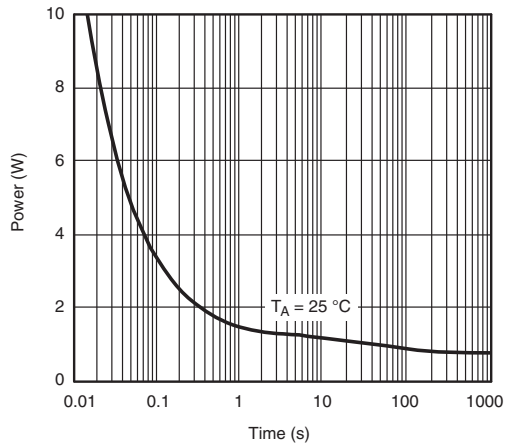
Source-Drain Diode Forward Voltage



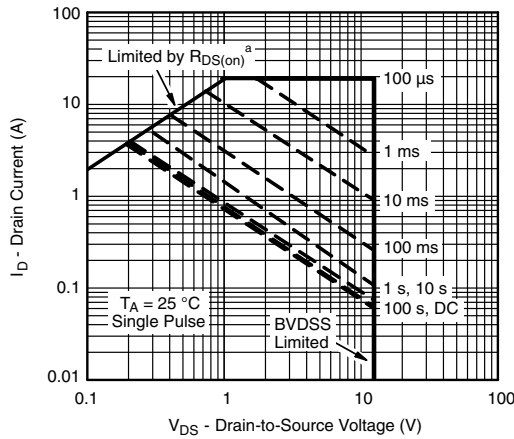
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power



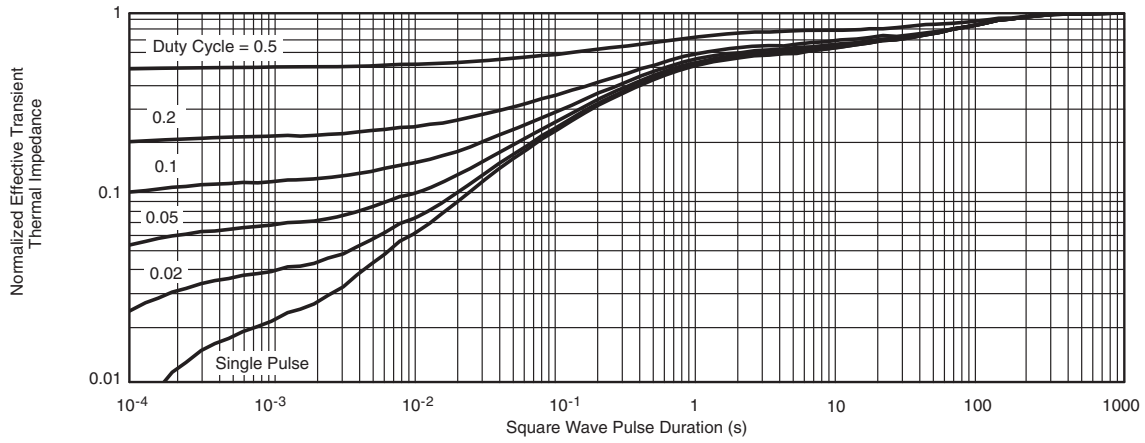
Safe Operating Area

Note

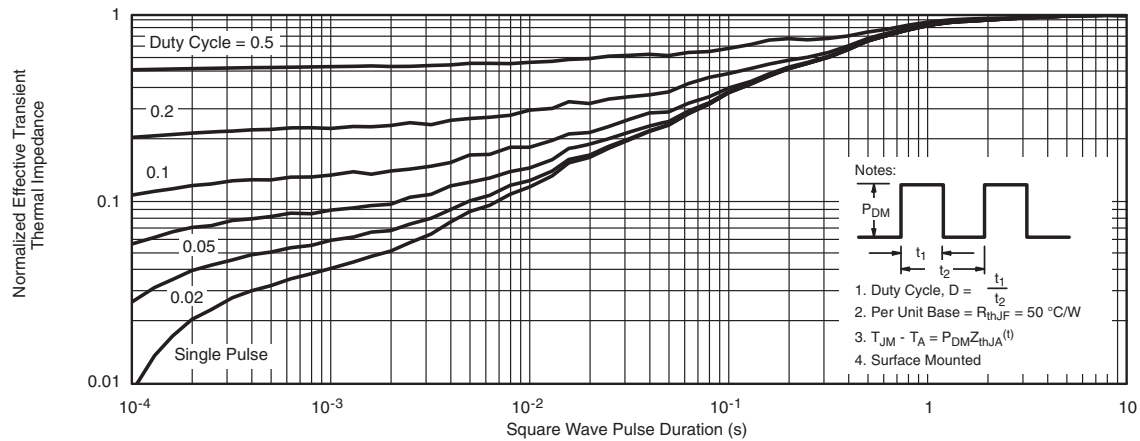
a. $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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SOT-23 (TO-236): 3-LEAD



| Dim | MILLIMETERS | | INCHES | |
|----------------|-------------|------|------------|-------|
| | Min | Max | Min | Max |
| A | 0.89 | 1.12 | 0.035 | 0.044 |
| A ₁ | 0.01 | 0.10 | 0.0004 | 0.004 |
| A ₂ | 0.88 | 1.02 | 0.0346 | 0.040 |
| b | 0.35 | 0.50 | 0.014 | 0.020 |
| c | 0.085 | 0.18 | 0.003 | 0.007 |
| D | 2.80 | 3.04 | 0.110 | 0.120 |
| E | 2.10 | 2.64 | 0.083 | 0.104 |
| E ₁ | 1.20 | 1.40 | 0.047 | 0.055 |
| e | 0.95 BSC | | 0.0374 Ref | |
| e ₁ | 1.90 BSC | | 0.0748 Ref | |
| L | 0.40 | 0.60 | 0.016 | 0.024 |
| L ₁ | 0.64 Ref | | 0.025 Ref | |
| S | 0.50 Ref | | 0.020 Ref | |
| q | 3° | 8° | 3° | 8° |

ECN: S-03946-Rev. K, 09-Jul-01
 DWG: 5479

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads
Dimensions in Inches/(mm)

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