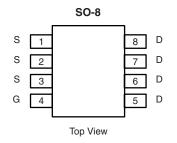


N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A)	Q _g (Typ.)		
30	0.0079 at V _{GS} = 10 V	19.3 ^a	8.8 nC		
	0.010 at V _{GS} = 4.5 V	17.1 ^a	0.0 110		



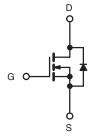
Halogen-free

FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested

APPLICATIONS

- DC/DC
 - High Side
 - VRM
 - POL
 - Server



Ordering Information: Si4162DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATIN	I GS T _A = 25 °C,	unless other	wise noted		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V_{DS}	30	V		
Gate-Source Voltage	V_{GS}	± 20	v		
	T _C = 25 °C		19.3 ^a		
Continuous Drain Current (T = 150 °C)	T _C = 70 °C	1 , [15.4		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	l _D	13.6 ^{b, c}	^	
	T _A = 70 °C	1	10.9 ^{b, c}	A	
Pulsed Drain Current	I _{DM}	70			
Avalanche Current	L = 0.1 mH	I _{AS}	31		
valanche Energy L = 0.1		E _{AS}	48	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C		4.2 ^a	A	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	2.1 ^{b, c}	^	
	T _C = 25 °C		5		
Maximum Rower Dissinction	T _C = 70 °C		3.2	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	2.5 ^{b, c}	VV	
	T _A = 70 °C	1	1.6 ^{b, c}		
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	38	50	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	20	25	J/VV		

- a. Based on T_C = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under Steady State conditions is 85 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	-						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			٧	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		32		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = 30 V, V _{GS} = 0 V			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
D : 0	Б	V _{GS} = 10 V, I _D = 20 A		0.0065	0.0079		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 14 A		0.0082	0.010	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		70		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1155			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		260		pF	
Reverse Transfer Capacitance	C _{rss}			95			
		$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		20	30		
Total Gate Charge	Q_g			8.8	14		
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		3.5		nC	
Gate-Drain Charge	Q_{gd}			2.2			
Gate Resistance	R_{g}	f = 1 MHz		1.0	2.0	Ω	
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		15	25		
Turn-Off Delay Time	t _{d(off)}	$I_{D} \cong 1.0 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_{g} = 17 \Omega$		25	40		
Fall Time	t _f			10	15		
Turn-On Delay Time	t _{d(on)}			14	20	ns	
Rise Time	t _r	$V_{DD} = 15 \text{ V, R}_{L} = 15 \Omega$		9	15	-	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1.0 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		25	40		
Fall Time	t _f			9	15		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			30	۸	
Pulse Diode Forward Current	I _{SM}				70	Α	
Body Diode Voltage	V _{SD}	I _S = 4.0 A, V _{GS} = 0 V		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			21	42	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 4.0 A, dl/dt = 100 A/μs, T _J = 25 °C		15	30	nC	
Reverse Recovery Fall Time	t _a			12.6			
Reverse Recovery Rise Time	t _b			8.4		ns	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

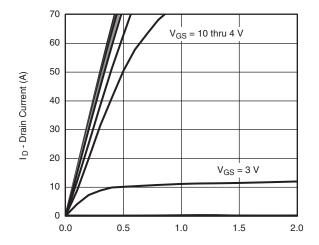
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.



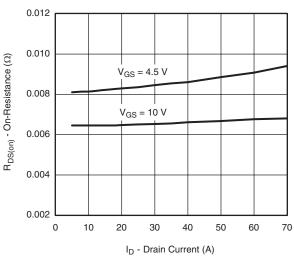


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

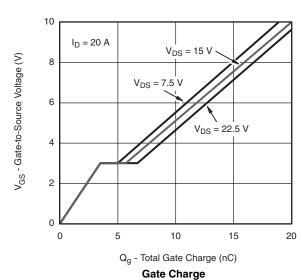


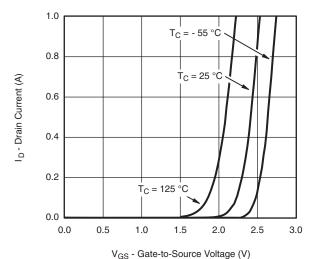
 V_{DS} - Drain-to-Source Voltage (V)

Output Characteristics



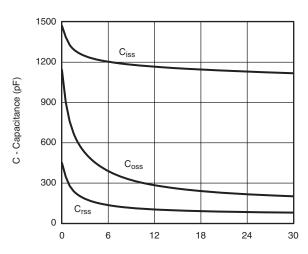
On-Resistance vs. Drain Current and Gate Voltage





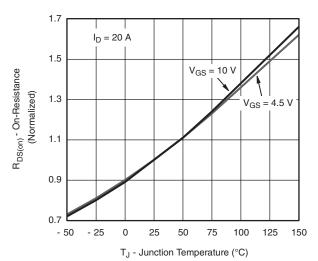
Tag came to country to the graph (1)





 V_{DS} - Drain-to-Source Voltage (V)

Capacitance

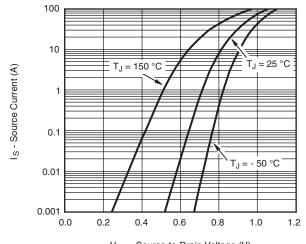


On-Resistance vs. Junction Temperature

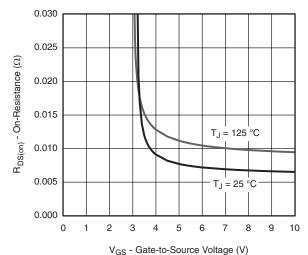
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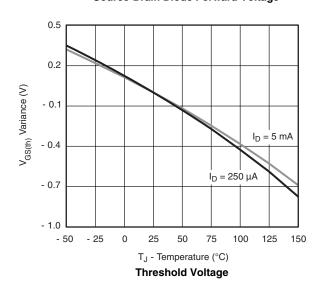
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

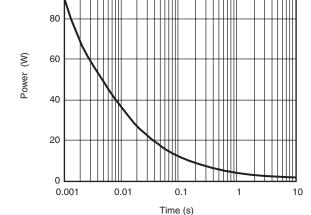


 V_{SD} - Source-to-Drain Voltage (V) **Source-Drain Diode Forward Voltage**



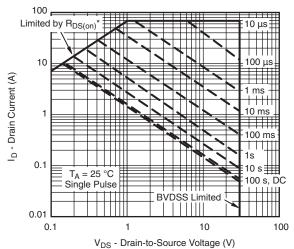
On-Resistance vs. Gate-to-Source Voltage





100

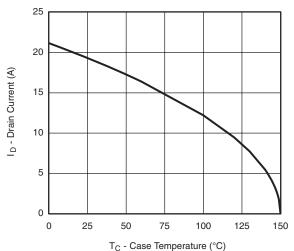
Single Pulse Power (Junction-to-Ambient)



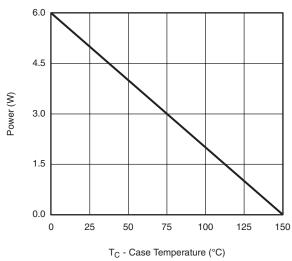
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified Safe Operating Area, Junction-to-Ambient

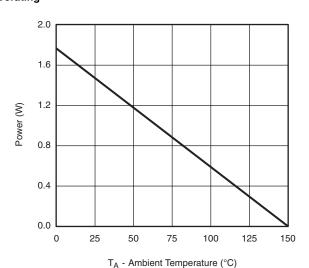


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*





Power, Junction-to-Case

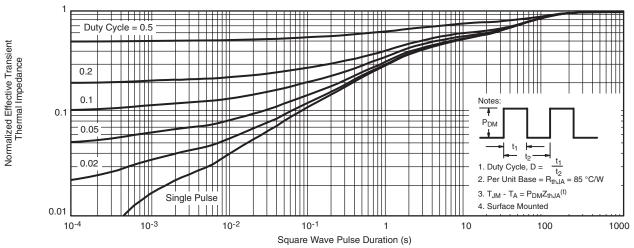
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

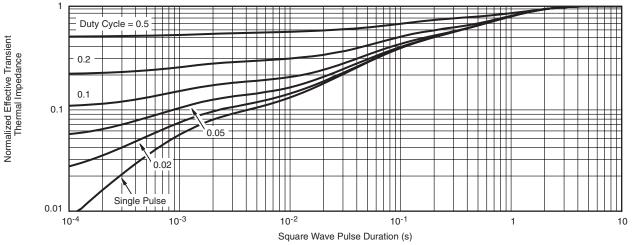
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INC	INCHES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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