

RoHS

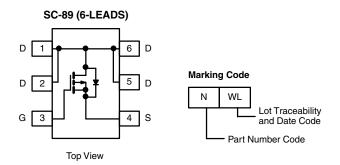
COMPLIANT HALOGEN

FREE

Vishay Siliconix

P-Channel 1.8 V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 20	0.195 at V _{GS} = - 4.5 V	- 0.84		
	0.260 at V _{GS} = - 2.5 V	- 0.73		
	0.350 at V _{GS} = - 1.8 V	- 0.64		



Ordering Information: Si1037X-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- Low Threshold
- Smallest LITTLE FOOT[®] Package: 1.6 mm x 1.6 mm
- Low 0.6 mm Profile
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Cell Phones and Pagers
 Load Switch
- Battery Operated Systems

Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		V
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	- 0.84	- 0.77	
	T _A = 70 °C		- 0.68	- 0.62	
Pulsed Drain Current		I _{DM}	- 4		A
Continuous Diode Current (Diode Conduction) ^a		l _S - 0.18		- 0.18 - 0.14	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	0.21	0.17	w
	T _A = 70 °C		0.13	0.10	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 5 s	- R _{thJA}	500	600	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		600	720	0/10	

Notes:

a. Surface mounted on 1" x 1" FR4 board with minimum copper.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.45			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	- 1				
		V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 70 °C			- 5	μA	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 4			Α	
Drain-Source On-State Resistance ^a		V _{GS} = - 4.5 V, I _D = - 0.77 A		0.160	0.195	Ω	
	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 0.67 A		0.212	0.260		
		V _{GS} = - 1.8 V, I _D = - 0.2 A		0.290	0.350		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 0.77 A		3.1		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 0.14 A, V _{GS} = 0 V		- 0.78	- 1.2	V	
Dynamic ^b	-						
Total Gate Charge	Qg			3.5	5.5	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 0.77 A		0.65			
Gate-Drain Charge	Q _{gd}			0.60			
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 20 Ω		15	30	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ - 0.5 A, V_{GEN} = - 4.5 V, R_{G} = 6 Ω		30	60		
Fall Time	t _f			10	20		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 0.14 A, dl/dt = 100 A/μs		20	40		

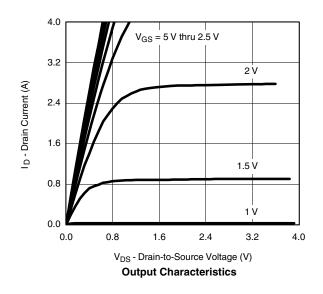
Notes:

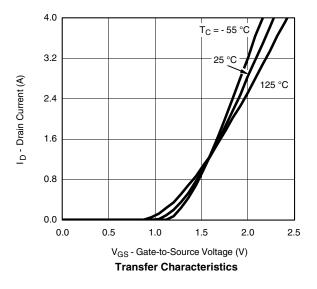
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



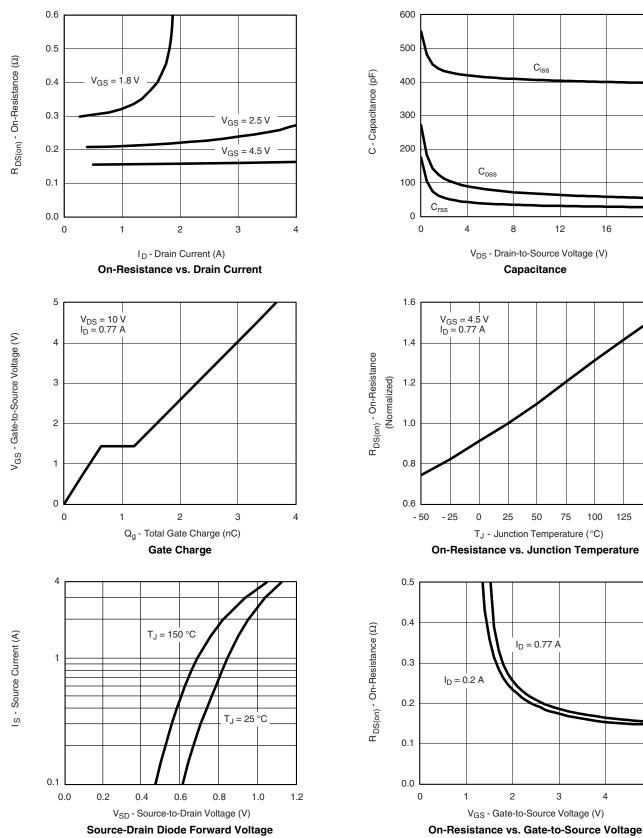




20

150

TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

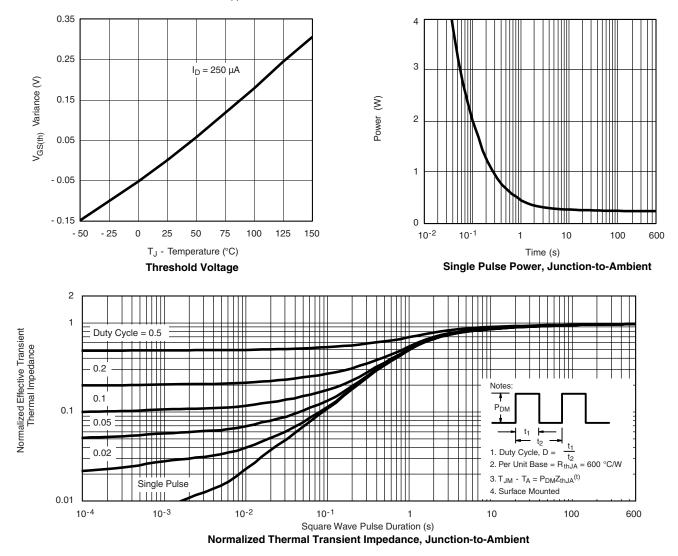


Document Number: 70686 S10-2544-Rev. D, 08-Nov-10 5

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TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



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Revision: 01-Jan-2024