

N-Channel 1.5 V (G-S) MOSFET

PRODUCT SUMMARY		
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (mA)
20	5 at V _{GS} = 4.5 V	200
	7 at V _{GS} = 2.5 V	175
	9 at V _{GS} = 1.8 V	150
	10 at V _{GS} = 1.5 V	50

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Low-Side Switching
- Low On-Resistance: 5 Ω
- Low Threshold: 0.9 V (typ.)
- Fast Switching Speed: 35 ns
- TrenchFET® Power MOSFETs: 1.5 V Rated
- 2000 V ESD Protection
- Compliant to RoHS Directive 2002/95/EC



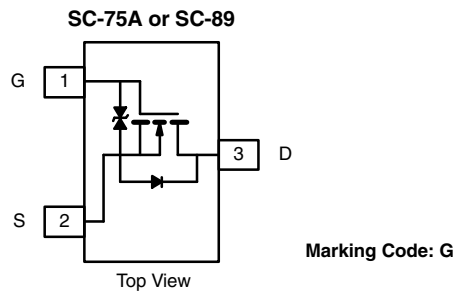
RoHS
COMPLIANT
HALOGEN
FREE

BENEFITS

- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers



Ordering Information:

Si1032R-T1-GE3 (SC-75A, Lead (Pb)-free and Halogen-free)
Si1032X-T1-GE3 (SC-89, Lead (Pb)-free -free Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)							
Parameter	Symbol	Si1032R		Si1032X		Unit	
		5 s	Steady State	5 s	Steady State		
Drain-Source Voltage	V _{DS}	20				V	
Gate-Source Voltage	V _{GS}	± 6					
Continuous Drain Current (T _J = 150 °C) ^a	I _D	T _A = 25 °C	200	140	210	200	mA
		T _A = 85 °C	110	100	150	140	
Pulsed Drain Current ^a	I _{DM}	500		600			
Continuous Source Current (Diode Conduction) ^a	I _S	250	200	300	240		
Maximum Power Dissipation ^a for SC-75	P _D	T _A = 25 °C	280	250	340	300	mW
		T _A = 85 °C	145	130	170	150	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150				°C	
Gate-Source ESD Rating (HBM, Method 3015)	ESD	2000				V	

Notes:

a. Surface mounted on FR4 board.

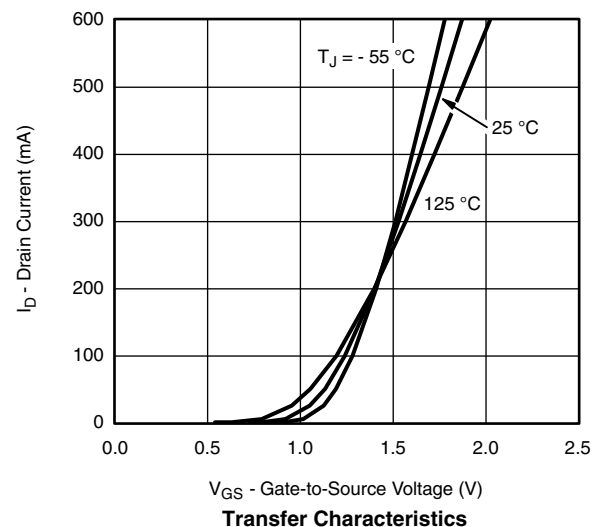
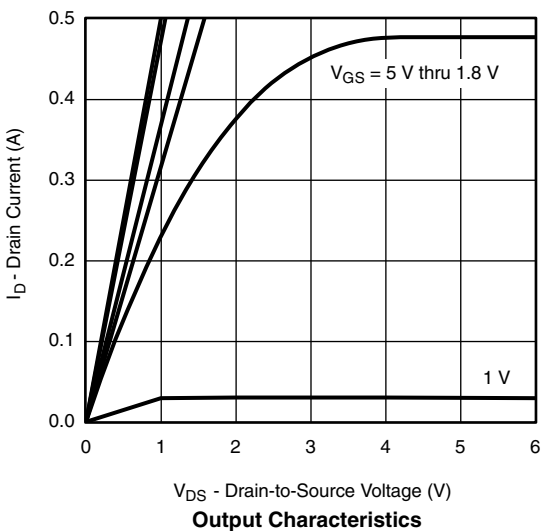
SPECIFICATIONS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$	0.40	0.7	1.2	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 2.8\text{ V}$		± 0.5	± 1.0	μA
		$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 4.5\text{ V}$		± 1.0	± 3.0	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20\text{ V}$, $V_{GS} = 0\text{ V}$			1	
		$V_{DS} = 20\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5\text{ V}$, $V_{GS} = 4.5\text{ V}$	250			mA
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}$, $I_D = 200\text{ mA}$			5	Ω
		$V_{GS} = 2.5\text{ V}$, $I_D = 175\text{ mA}$			7	
		$V_{GS} = 1.8\text{ V}$, $I_D = 150\text{ mA}$			9	
		$V_{GS} = 1.5\text{ V}$, $I_D = 40\text{ mA}$			10	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}$, $I_D = 200\text{ mA}$		0.5		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 150\text{ mA}$, $V_{GS} = 0\text{ V}$			1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = 10\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 250\text{ mA}$		750		pC
Gate-Source Charge	Q_{gs}			75		
Gate-Drain Charge	Q_{gd}			225		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}$, $R_L = 47\text{ }\Omega$ $I_D \cong 200\text{ mA}$, $V_{GEN} = 4.5\text{ V}$, $R_g = 10\text{ }\Omega$			50	ns
Rise Time	t_r				25	
Turn-Off Delay Time	$t_{d(off)}$				50	
Fall Time	t_f				25	

Notes:

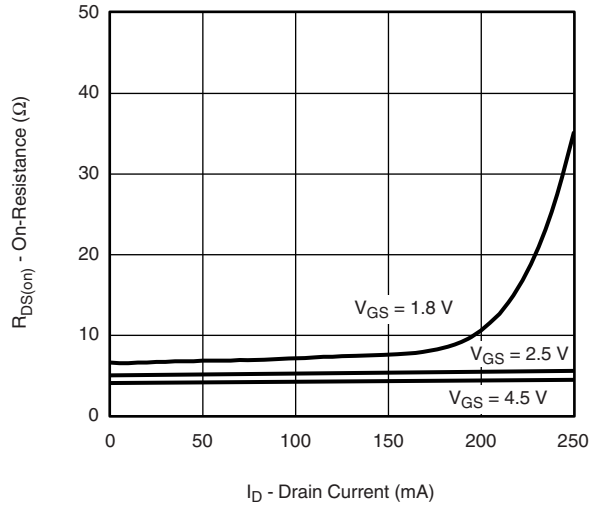
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

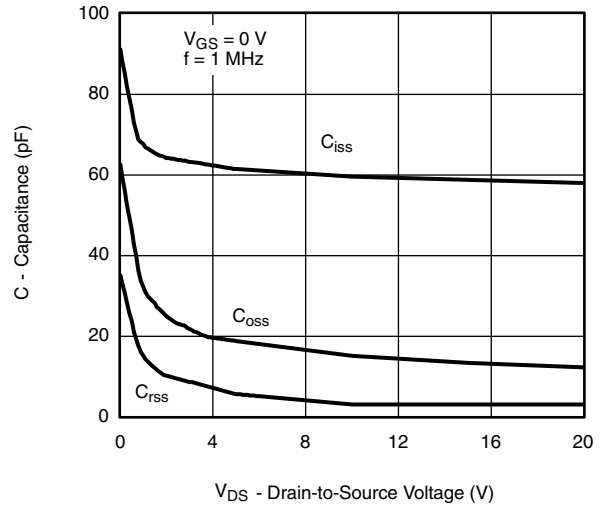
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



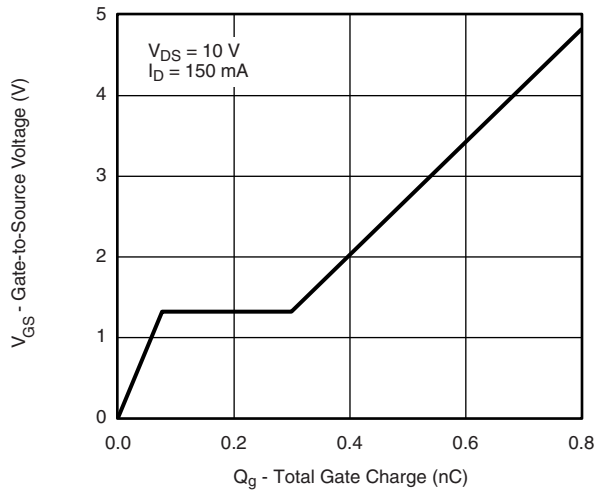
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



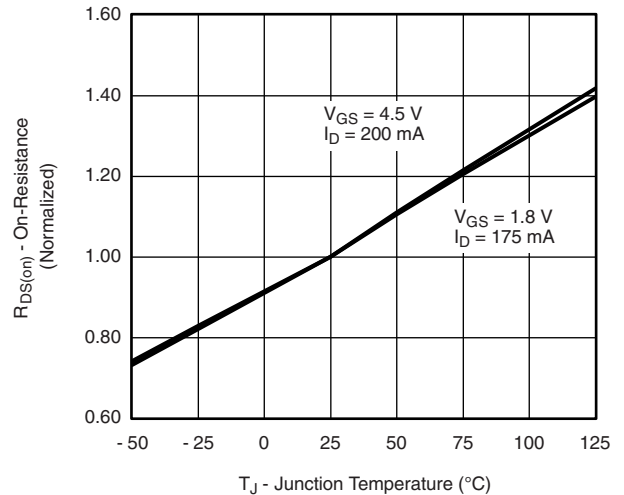
On-Resistance vs. Drain Current



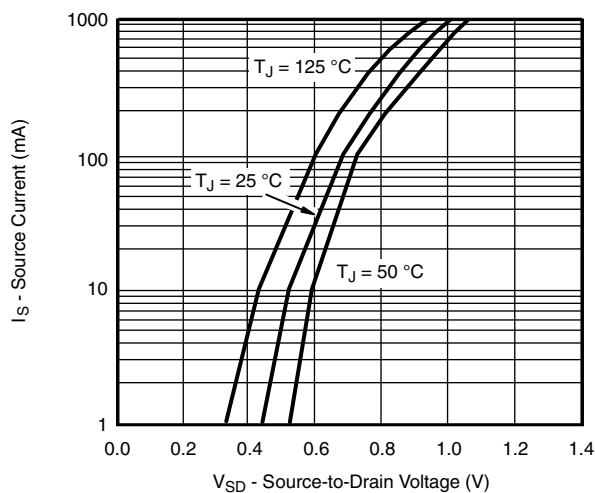
Capacitance



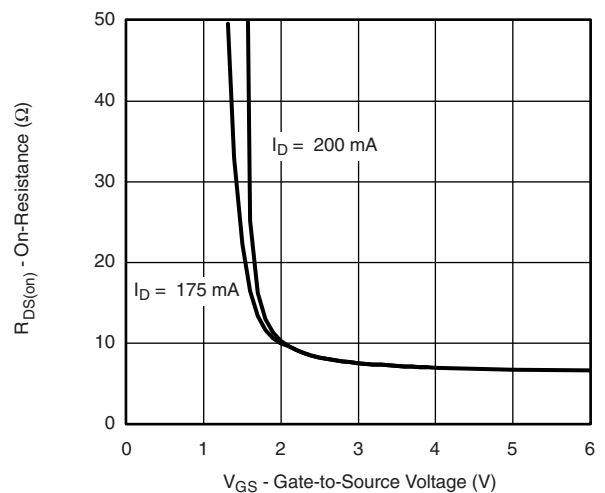
Gate Charge



On-Resistance vs. Junction Temperature

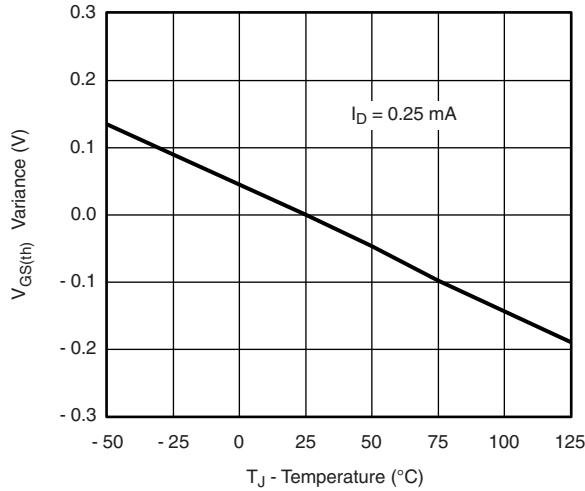


Surge-Drain Diode Forward Voltage

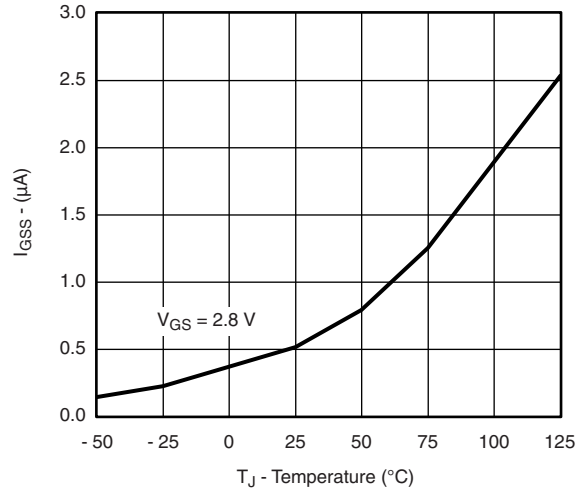


On-Resistance vs. Gate-to-Source Voltage

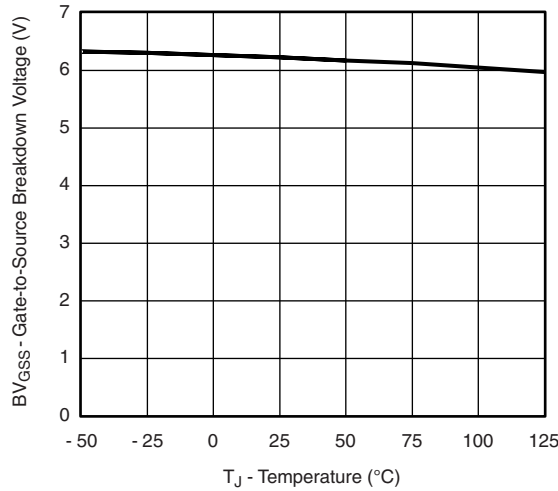
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



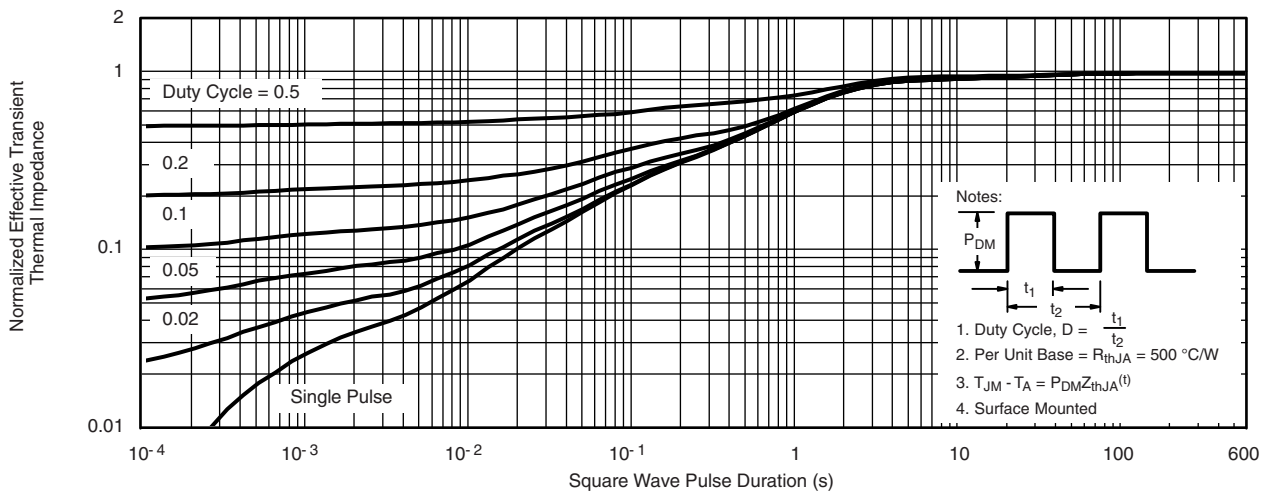
Threshold Voltage Variance vs. Temperature



I_{GSS} vs. Temperature



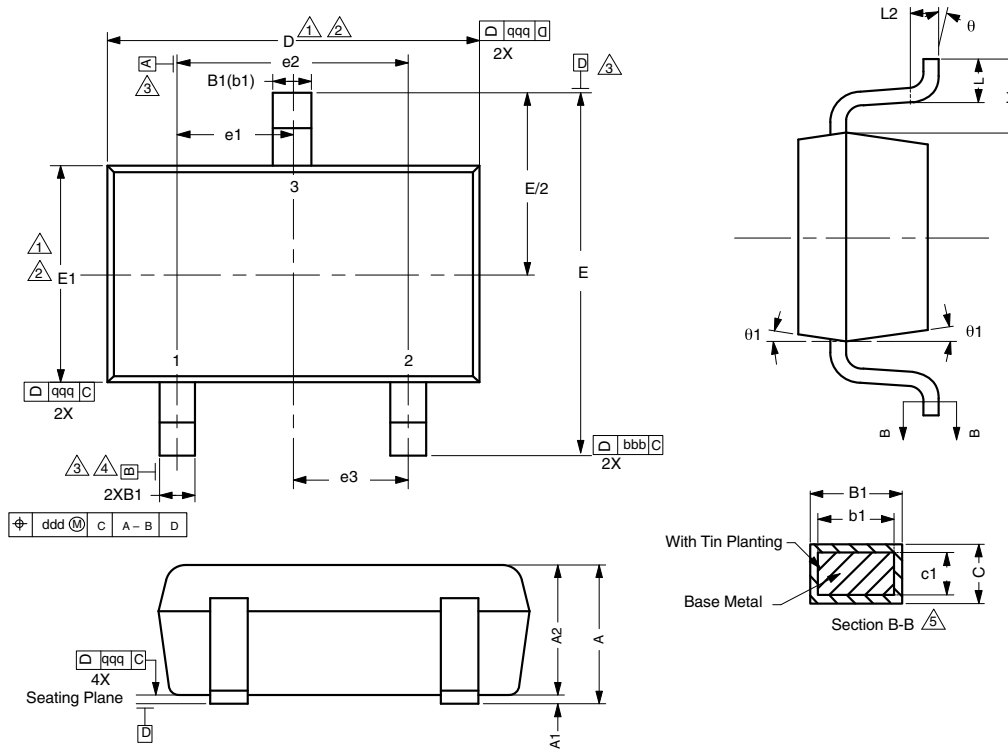
BV_{GSS} vs. Temperature



Normalized Thermal Transient Impedance, Junction-to-Ambient (SC-75A, Si1032R Only)

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71172.

SC-75A: 3 Leads



DWG: 5868

Notes

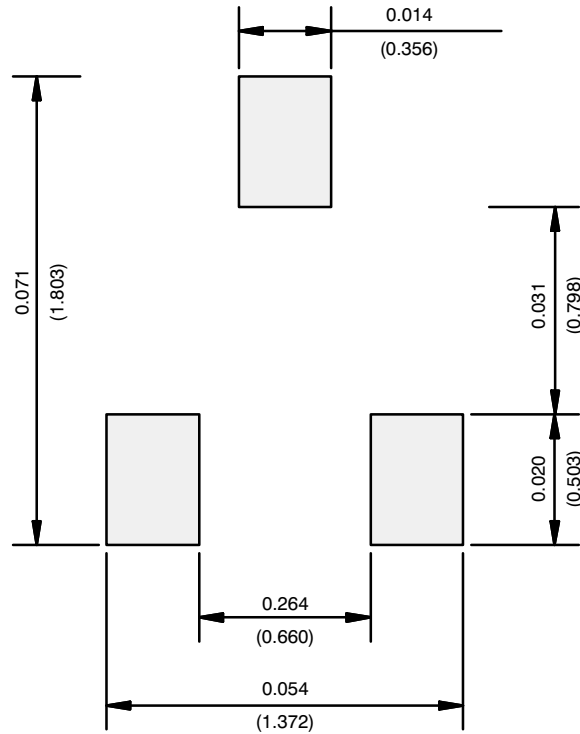
Dimensions in millimeters will govern.

- 1. Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.
- 2. Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
- 3. Datums A, B and D to be determined 0.10 mm from the lead tip.
- 4. Terminal positions are shown for reference only.
- 5. These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES
aaa	0.10
bbb	0.10
ccc	0.10
ddd	0.10

DIM.	MILLIMETERS			NOTE
	MIN.	NOM.	MAX.	
A	-	-	0.80	
A1	0.00	-	0.10	
A2	0.65	0.70	0.80	
B1	0.19	-	0.24	5
b1	0.17	-	0.21	
c	0.13	-	0.15	5
c1	0.10	-	0.12	5
D	1.48	1.575	1.68	1, 2
E	1.50	1.60	1.70	
E1	0.66	0.76	0.86	1, 2
e1	0.50 BSC			
e2	1.00 BSC			
e3	0.50 BSC			
L	0.15	0.205	0.30	
L1	0.40 ref.			
L2	0.15 BSC			
q	0°	-	8°	
q1	4°	-	10°	

RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

[Return to Index](#)



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.