

**Vishay Siliconix** 

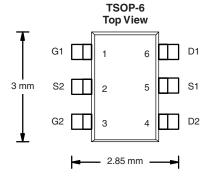
## Dual N-Channel 20-V (D-S) MOSFET

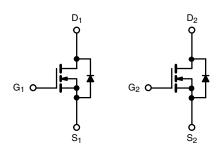
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
20	0.125 at V <sub>GS</sub> = 4.5 V	2.4		
	0.200 at V <sub>GS</sub> = 2.5 V	1.8		

#### FEATURES

- Halogen-free According to IEC 61249-2-21
   Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

Unit

**Steady State** 

N-Channel MOSFET

Ordering Information: Si3900DV-T1-E3 (Lead (Pb)-free) Si3900DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

 ABSOLUTE MAXIMUM RATINGS T<sub>A</sub> = 25 °C, unless otherwise noted

 Parameter
 Symbol
 5 s

 Drain-Source Voltage
 V<sub>DS</sub>
 V

Drain-Source Voltage		V <sub>DS</sub>	20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 12		v	
Continuous Drain Current (T 150 °C)8	T <sub>A</sub> = 25 °C	Ι <sub>D</sub>	2.4	2.0		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		1.7	1.4	1	
Pulsed Drain Current (10 µs Pulse Width)		I <sub>DM</sub>	8		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.05	0.75		
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.15	0.83	w	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		0.59	0.53	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 5 s	R <sub>thJA</sub>	93	110		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		130	150	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	75	90		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.6		1.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			±100	nA	
Zaus Cata Maltana Dusin Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \geq 5$ V, $V_{GS}$ = 4.5 V	5			А	
	Б	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 2.4 \text{ A}$		0.100	0.125	0	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 1.0 \text{ A}$		0.160	0.200	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 5 V, I_{D} = 2.4 A$		5		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 1.05 \text{ A}, V_{GS} = 0 \text{ V}$		0.79	1.10	V	
Dynamic <sup>b</sup>				•			
Total Gate Charge	Qg			2.1	4.0		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 2.4 A		0.3		nC	
Gate-Drain Charge	Q <sub>gd</sub>			0.4		1	
Turn-On Delay Time	t <sub>d(on)</sub>			10	17		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$		30	50	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_{\text{D}}\cong$ 1 A, $\text{V}_{\text{GEN}}$ = 4.5 V, $\text{R}_{\text{g}}$ = 6 $\Omega$		14	25		
Fall Time	t <sub>f</sub>			6	12		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 3.0 A, dl/dt = 100 A/μs		30	50		

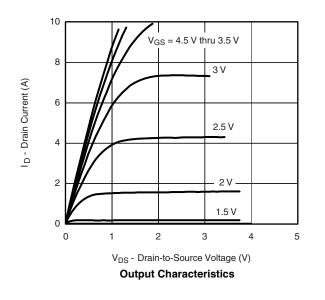
Notes:

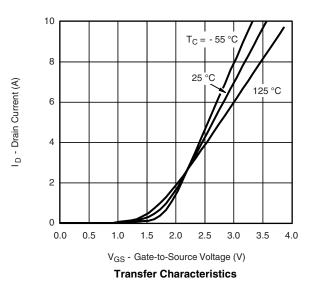
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



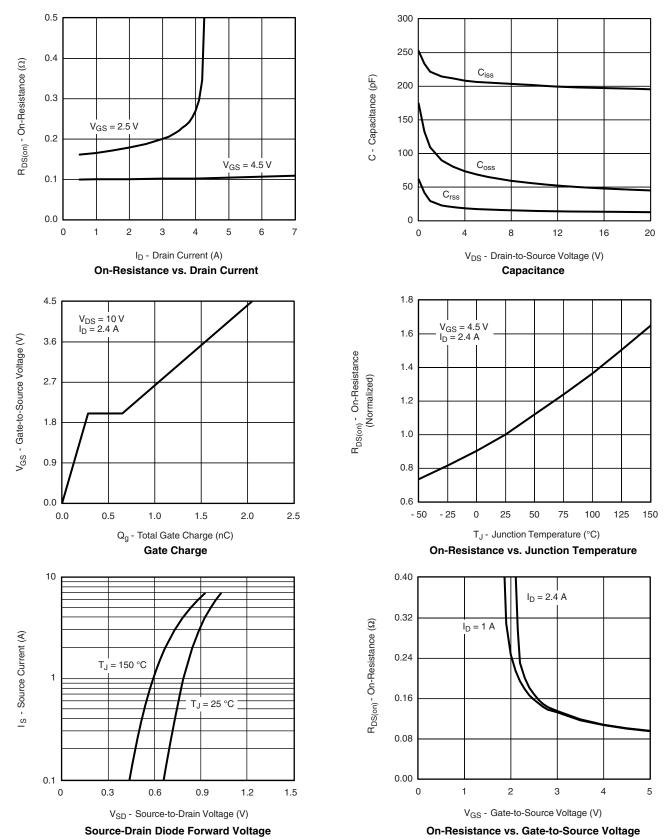




# Si3900DV

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

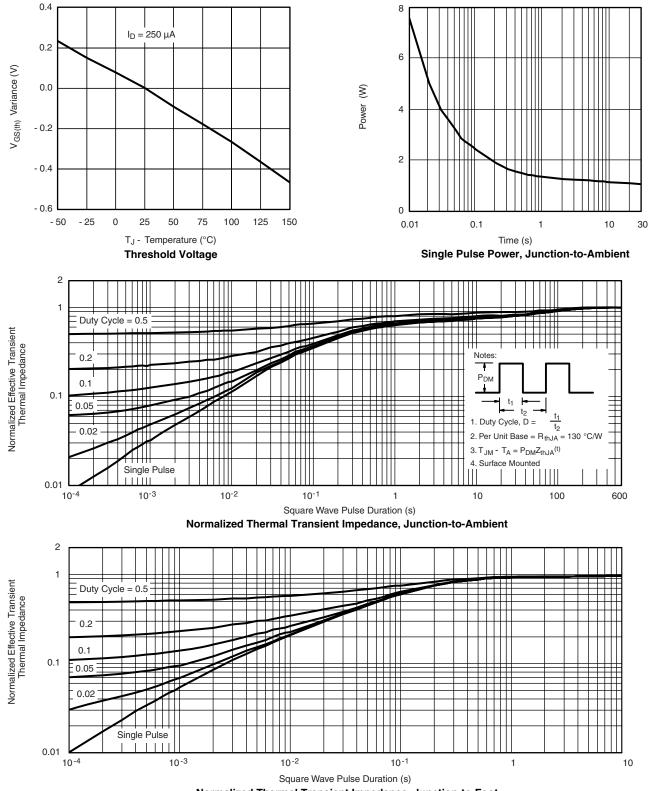


Document Number: 71178 S09-2275-Rev. D, 02-Nov-09

### Si3900DV

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71178">www.vishay.com/ppg?71178</a>.

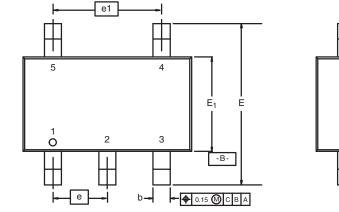




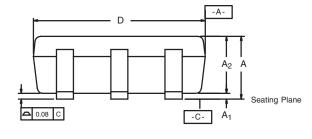
Package Information

Vishay Siliconix

TSOP: 5/6-LEAD JEDEC Part Number: MO-193C



5-LEAD TSOP





6-LEAD TSOP



	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
<b>A</b> <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004	
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
<b>e</b> <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L <sub>1</sub>		0.60 Ref		0.024 Ref			
L <sub>2</sub>	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
$\theta_1$	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							



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