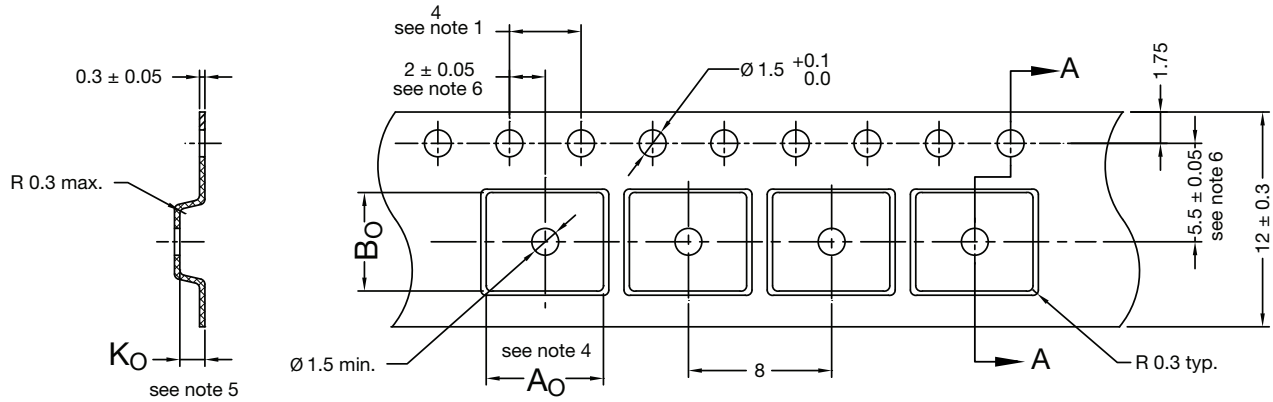




## PowerPAK® SOIC-8



Section A-A

$A_O = 6.5 \text{ mm} \pm 0.1 \text{ mm}$   
 $B_O = 5.5 \text{ mm} \pm 0.1 \text{ mm}$   
 $K_O = 1.4 \text{ mm} \pm 0.1 \text{ mm}$

### Notes

- (1) 10 sprocket hole pitch cumulative tolerance  $\pm 0.2$  mm.
- (2) Camber not to exceed 1 mm in 100 mm, also not to exceed 1.5 cm in 1 m actually.
- (3) Material: Black conductive or black static dissipative.
- (4) For advantek carrier tape,  $A_O$  and  $B_O$  are calculate on a plane at a distance "R" above the bottom of the pocket.
- (5)  $K_O$  measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
- (6) It should be measured from:
  - a. sprocket hole to pocket center
  - and
  - b. sprocket hole to pocket hole
- (7) All sizes in mm unless specified.
- (8) Tolerances will be  $\pm 0.1$  mm unless specified.
- (9) Vishay part number must be labeled at all reels of carrier tape.
- (10) Surface resistivity:  $10^4$  to  $10^{11} \Omega$ .

QUANTITY PER REEL	
T1	3000

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