

Vishay Siliconix

# N-Channel 2.5-V (G-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)			
20	0.057 at V <sub>GS</sub> = 4.5 V	4.2			
20	0.090 at V <sub>GS</sub> = 2.5 V	3.4			

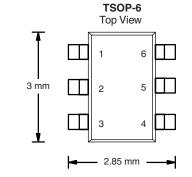


- Halogen-free According to IEC 61249-2-21 • Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC ٠

(3) G C



Available



2Bxxx

Ordering Information: Si3442BDV-T1-E3 (Lead (Pb)-free) Si3442BDV-T1-GE3 (Lead (Pb)-free and Halogen-free)

(4) S N-Channel MOSFET

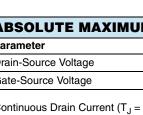
(1, 2, 5, 6) D

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	5 s	Steady State	Unit		
Drain-Source Voltage		V <sub>DS</sub>	20		V	
Gate-Source Voltage	V <sub>GS</sub>	± 12				
Continuous Drain Current (T 150 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	4.2	3.0	•	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		3.4	2.4		
Pulsed Drain Current		I <sub>DM</sub>	20		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>	۱ <sub>S</sub>	1.4	0.72			
	T <sub>A</sub> = 25 °C	Р	1.67	0.86	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	- P <sub>D</sub>	1.07	0.55	vv	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 t	io 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	R <sub>thJA</sub>	75	100	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	120	145	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	70	85	

Note:

a. Surface Mounted on FR4 board,  $t \leq 5 \; s.$ 



Marking Code:

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•		•				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.6		1.8	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current		$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			5		
	I	$V_{DS} = 5 V, V_{GS} = 4.5 V$	10			^	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 2.5 V$	4		A		
	Р	$V_{GS} = 4.5 \text{ V}, I_D = 4 \text{ A}$		0.045	0.057	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.4 \text{ A}$		0.070	0.090		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 4.0 \text{ A}$		11.3		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.6 A, V <sub>GS</sub> = 0 V		0.75	1.2	V	
Dynamic <sup>b</sup>				1			
Input Capacitance	C <sub>iss</sub>			295			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 0 V, f = 1 MHz		75		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			45			
Total Gate Charge	Qg			3	5		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 4.0 \text{ A}$		0.65		nC	
Gate-Drain Charge	Q <sub>gd</sub>			0.95		1	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		2.7		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			35	55		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$		50	75	1	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 1 A, $V_{GEN}$ = 4.5 V, $R_g$ = 6 $\Omega$		20	30	ns	
Fall Time	t <sub>f</sub>			15	25		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.6 A, dl/dt = 100 A/μs	1	30	60		

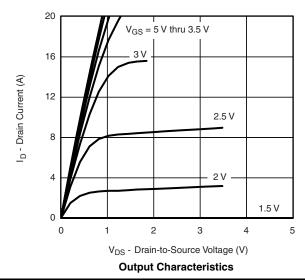
Notes:

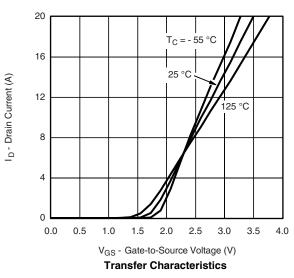
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



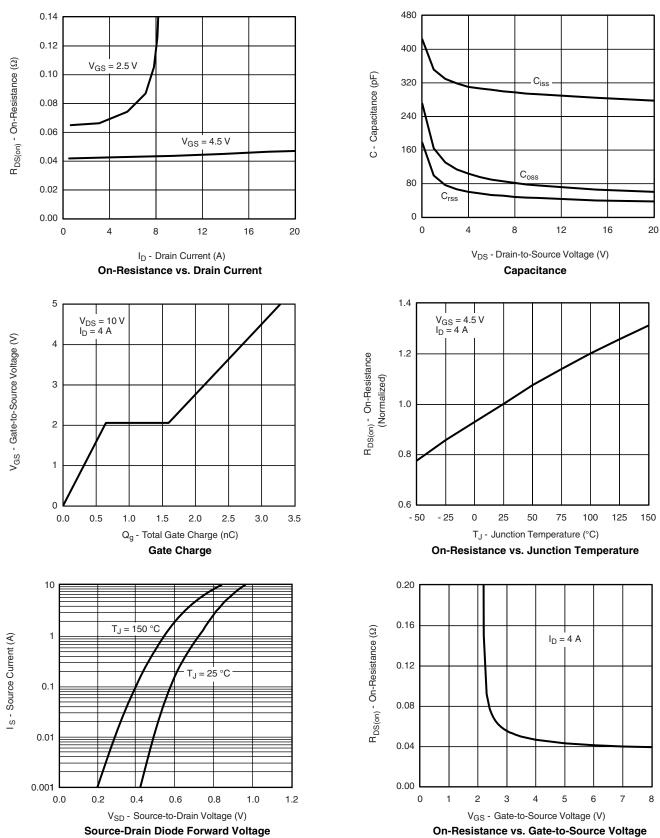




# Si3442BDV

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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

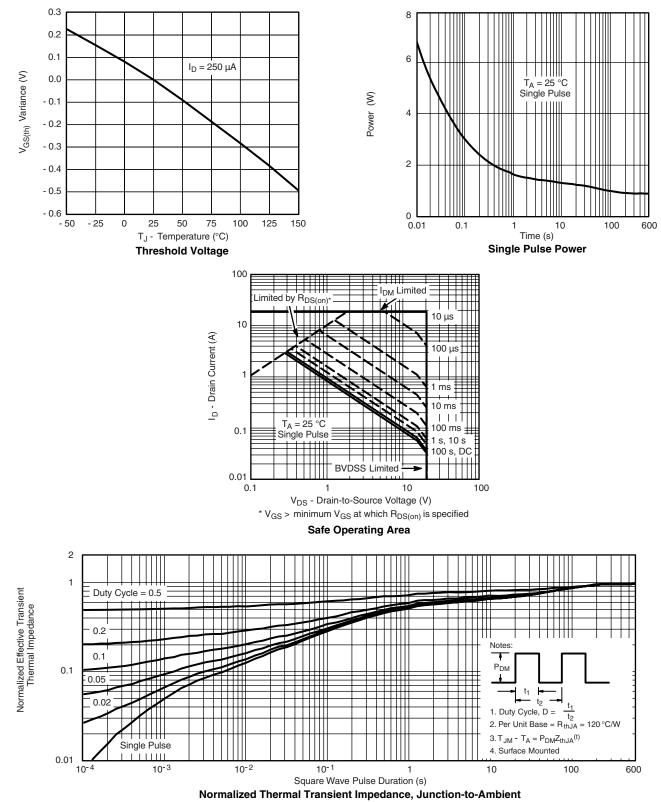


# Si3442BDV

## **Vishay Siliconix**

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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



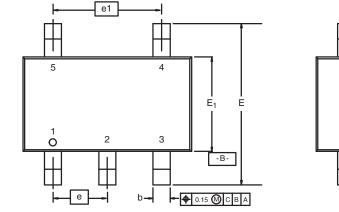
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?72504">www.vishay.com/ppg?72504</a>.



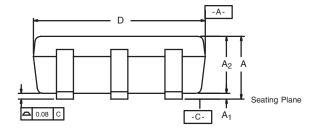
Package Information

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TSOP: 5/6-LEAD JEDEC Part Number: MO-193C



5-LEAD TSOP





6-LEAD TSOP



	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
<b>A</b> <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004	
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
<b>e</b> <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L <sub>1</sub>	0.60 Ref				0.024 Ref		
L <sub>2</sub>	0.25 BSC				0.010 BSC		
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
$\theta_1$	7° Nom				7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

## **PAD** Pattern



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# **Recommended Land Pattern For TSOP-5L / TSOP-6L**





TSOP 5L





#### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022	
DWG: 3010	

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