

Vishay Siliconix

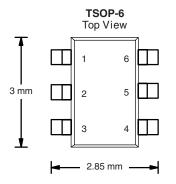
N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω) I _D (
30	0.035 at V _{GS} = 10 V	6.0		
30	0.052 at V _{GS} = 4.5 V	4.9		

FEATURES

- Halogen free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % Rg Tested
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3456BDV-T1-E3 (Lead (Pb)-free) Si3456BDV-T1-GE3 (Lead (Pb)-free and Halogen-free) Marking Code: 6Bxxx

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(3) G	o—		╺ ╺ ╺ ┥	:
			(4) S	

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise r	noted		
Parameter	Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Quarant (T. 150 00)	T _A = 25 °C	- I _D	6.0	4.5	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		4.8	3.6	
Pulsed Drain Current		I _{DM}	± 30		A
Continuous Source Current (Diode Conduction) ^a	۱ _S	1.7	0.9		
	T _A = 25 °C	Р	2.0	1.1	14/
Maximum Power Dissipation ^a	T _A = 70 °C	P _D	1.3	0.7	W
Operating Junction and Storage Temperature Rang	T _J , T _{stg}	- 55	to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum hunstion to Ambienta	t ≤ 5 s	R _{thJA}	55	62.5		
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	92	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	28	40		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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SPECIFICATIONS $T_J = 25$	°C, unless	otherwise noted				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.0		3.0	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	
		$V_{DS} = 30$ V, $V_{GS} = 0$ V, $T_{J} = 55$ °C			5	μΑ
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = 10$ V	30			А
	R _{DS(on)}	V _{GS} = 10 V, I _D = 6 A		0.028	0.035	0
Drain-Source On-State Resistance ^a		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 4.9 \text{ A}$		0.041	0.052	Ω
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		12		S
Diode Forward Voltage ^a	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V		0.8	1.2	V
Dynamic ^b						
Total Gate Charge	Qg			8.6	13	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 6 \text{ A}$		1.8		nC
Gate-Drain Charge	Q _{gd}			1.5		
					1	

Source-Drain Reverse Recovery Time Notes:

Turn-On Delay Time

Turn-Off Delay Time

Rise Time

Fall Time

Diode Forward Vol Dynamic^b **Total Gate Charge** Gate-Source Char Gate-Drain Charge Gate Resistance

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

f = 1 MHz

 V_{DD} = 15 V, R_L = 15 Ω

 $I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_q = 6 \Omega$

 $I_F = 1.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

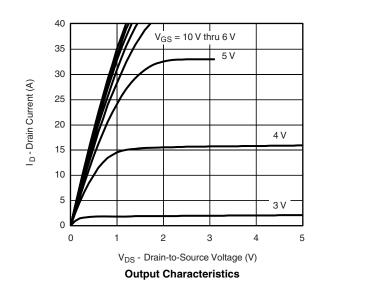
Rg

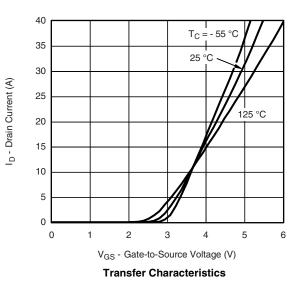
t_{d(on)} t_r

t_{d(off)}

t_f

t_{rr}





1.4

2.8

10

15

25

10

20

4.8

15

25

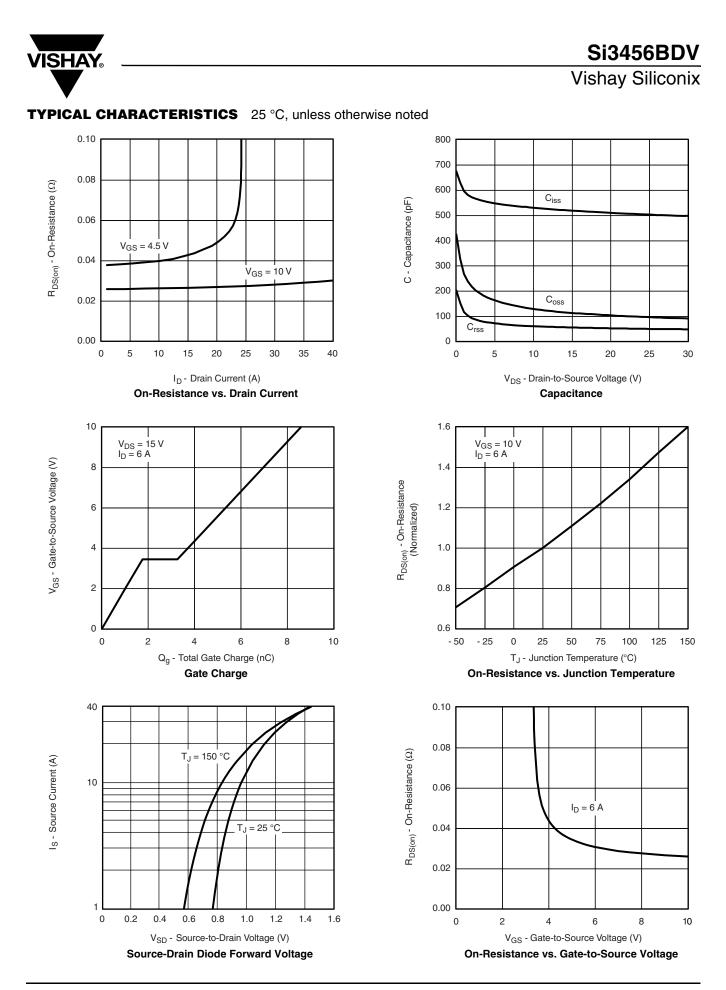
40

15

40

Ω

ns

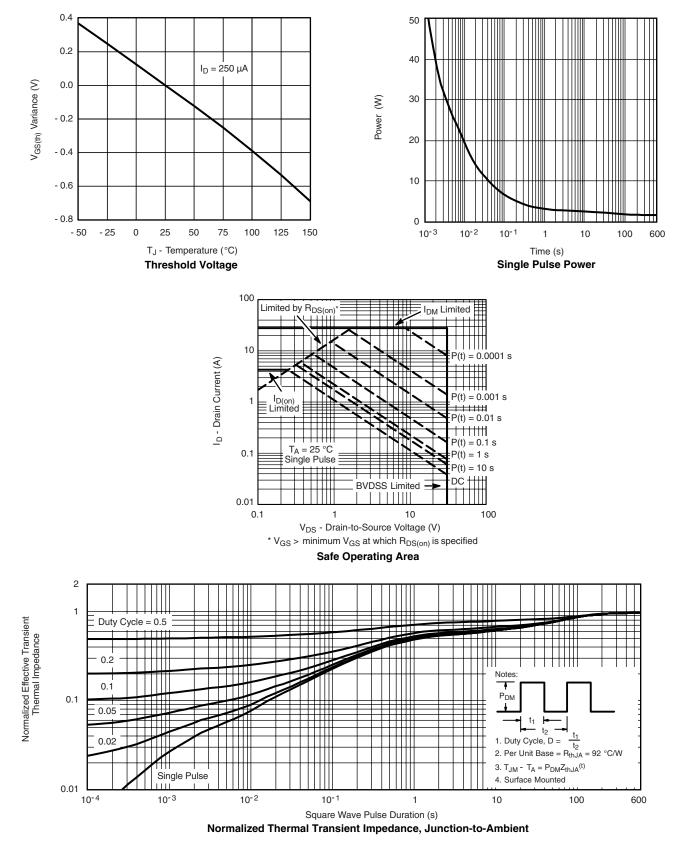


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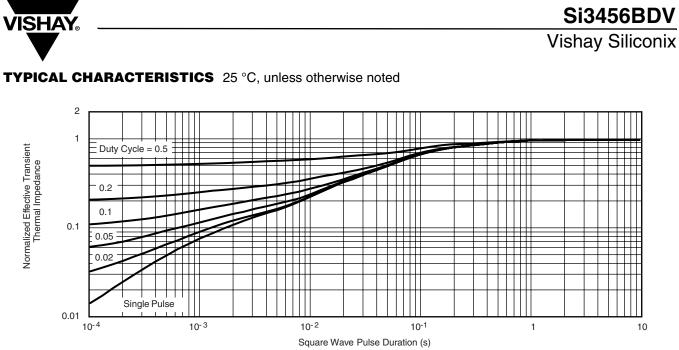
Si3456BDV

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg772544.



Package Information

Vishay Siliconix

TSOP: 5/6-LEAD JEDEC Part Number: MO-193C









6-LEAD TSOP



	MIL	LIMETER	RS	INCHES					
Dim	Min	Nom	Max	Min	Nom	Max			
Α	0.91	-	1.10	0.036	-	0.043			
A ₁	0.01	-	0.10	0.0004	-	0.004			
A ₂	0.90	-	1.00	0.035	0.038	0.039			
b	0.30	0.32	0.45	0.012	0.013	0.018			
С	0.10	0.15	0.20	0.004	0.006	0.008			
D	2.95	3.05	3.10	0.116	0.120	0.122			
Е	2.70	2.85	2.98	0.106	0.112	0.117			
E ₁	1.55	1.65	1.70	0.061	0.065	0.067			
е	0.95 BSC			(0.0374 BSC	;			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079			
L	0.32	-	0.50	0.012	-	0.020			
L ₁		0.60 Ref			0.024 Ref				
L ₂	0.25 BSC				0.010 BSC				
R	0.10	-	-	0.004	-	-			
θ	0°	4°	8°	0°	4°	8°			
θ_1	7° Nom				7° Nom				
		ev. I, 18-Dec	c-06		ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540				

PAD Pattern



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Recommended Land Pattern For TSOP-5L / TSOP-6L





TSOP 5L





Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022	
DWG: 3010	

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