COMPLIANT

FREE





## **Load Switch with Level-Shift**

PRODUCT SUMMARY				
V <sub>DS2</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
1.8 to 8	$0.060 \text{ at V}_{IN} = 4.5 \text{ V}$	2.9		
	0.100 at V <sub>IN</sub> = 2.5 V	2.2		
	0.175 at V <sub>IN</sub> = 1.8 V	1.7		

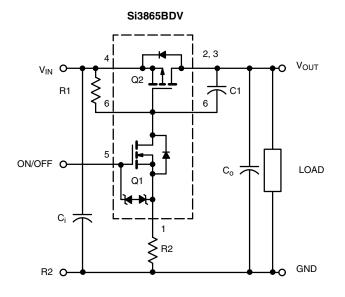
#### **DESCRIPTION**

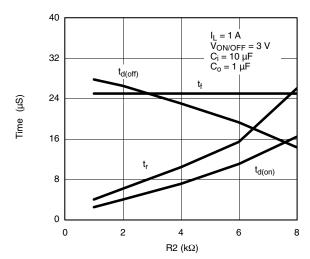
The Si3865BDV includes a p- and n-channel MOSFET in a single TSOP-6 package. The low on-resistance p-channel TrenchFET® is tailored for use as a load switch. The n-channel, with an external resistor, can be used as a level-shift to drive the p-channel load-switch. The n-channel MOSFET has internal ESD protection and can be driven by logic signals as low as 1.5 V. The Si3865BDV operates on supply lines from 1.8 V to 8 V, and can drive loads up to 2.9 A.

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
   Definition
- 60 m $\Omega$  Low R<sub>DS(on)</sub> TrenchFET<sup>®</sup>: 1.8 V Rated
- 1.8 V to 8 V Input
- 1.5 V to 8 V Logic Level Control
- Low Profile, Small Footprint TSOP-6 Package
- 3000 V ESD Protection On Input Switch, V<sub>ON/OFF</sub>
- Adjustable Slew-Rate
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATION CIRCUITS**





Note: For R2 switching variations with other  $V_{IN}/R1$  combinations See Typical Characteristics

Switching Variation R2 at  $V_{IN}$  = 2.5 V, R1 = 20 k $\Omega$ 

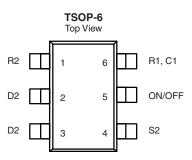
COMPONENTS					
R1	Pull-Up Resistor	Typical 10 k $\Omega$ to 1 M $\Omega^*$			
R2	Optional Slew-Rate Control	Typical 0 to 100 kΩ*			
C1	Optional Slew-Rate Control	Typical 1000 pF			

<sup>\*</sup> Minimum R1 value should be at least 10 x R2 to ensure Q1 turn-on.

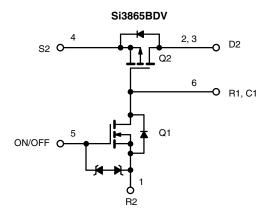
The Si3865BDV is ideally suited for high-side load switching in portable applications. The integrated N-Channel level-shift device saves space by reducing external components. The slew rate is set externally so that rise-times can be tailored to different load types.

# Vishay Siliconix

#### **FUNCTIONAL BLOCK DIAGRAM**



Ordering Information: Si3865BDV-T1-E3 (Lead (Pb)-free) Si3865BDV-T1-GE3 (Lead (Pb)-free and Halogen-free)



ABSOLUTE MAXIMUM RATINGS (TA =	= 25 °C, unle	ss otherwise	noted)	
Parameter		Symbol	Limit	Unit
Input Voltage On/Off Voltage		V <sub>IN</sub>	8	V
		V <sub>ON/OFF</sub>	8	
Load Current	Continuous <sup>a, b</sup>	IL	± 2.9	
Load Current	Pulsed <sup>b, c</sup>		± 6	Α
Continuous Intrinsic Diode Conduction <sup>a</sup>		I <sub>S</sub>	- 1	
Maximum Power Dissipation <sup>a</sup>		P <sub>D</sub>	0.83	W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C
ESD Rating, MIL-STD-883D Human Body Model (100 pF, 1500 Ω)		ESD	3	kV

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient (continuous current) <sup>a</sup>	R <sub>thJA</sub>	125	150	°C/W	
Maximum Junction-to-Foot (Q2)	R <sub>thJC</sub>	40	55	G/VV	

<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Off Characteristics								
Reverse Leakage Current	I <sub>FL</sub>	$V_{IN} = 8 \text{ V}, V_{ON/OFF} =$			1	μΑ		
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> = - 1 A		- 0.77	- 1	V		
OnN Characteristics								
Input Voltage Range	$V_{IN}$		1.8		8	V		
On-Resistance (P-Channel) at 1 A	R <sub>DS(on)</sub>	V <sub>ON/OFF</sub> = 1.5 V, I <sub>D</sub> = 1 A	V <sub>IN</sub> = 4.5 V		0.045	0.060	Ω	
			V <sub>IN</sub> = 2.5 V		0.075	0.100		
			V <sub>IN</sub> = 1.8 V		0.135	0.175		
On-State (P-Channel) Drain-Current	I <sub>D(on)</sub>	$V_{IN-OUT} \le 0.2 \text{ V}, V_{IN} = 5 \text{ V}, V_{ON/OFF} = 1.5 \text{ V}$		1			Α	
		$V_{IN-OUT} \le 0.3 \text{ V}, V_{IN} = 3 \text{ V}, V_{O}$	1			] ^		

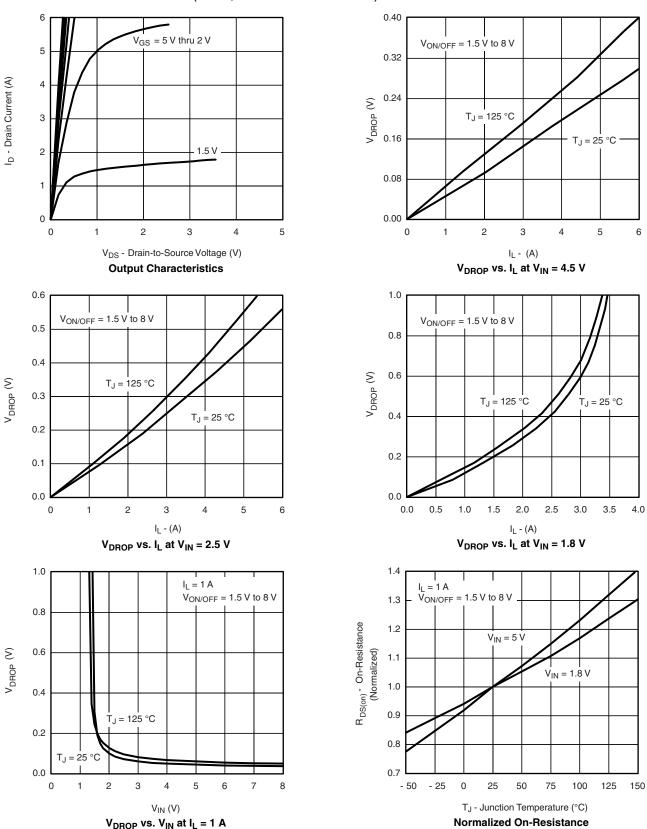
- a. Surface Mounted on FR4 board.
- b.  $V_{IN}$  = 8 V,  $V_{ON/OFF}$  = 8 V,  $T_A$  = 25 °C. c. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

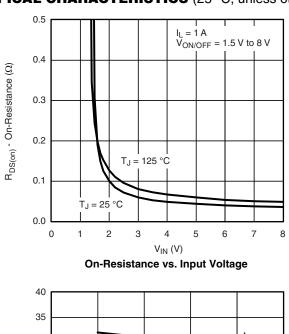


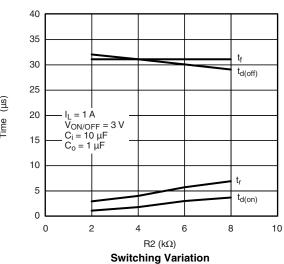
vs. Junction Temperature

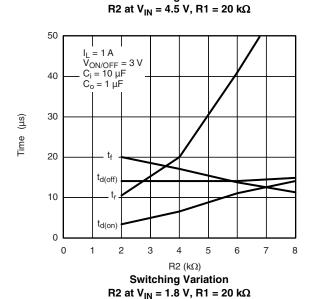
# Vishay Siliconix

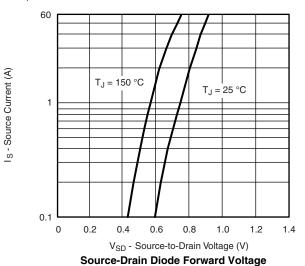
# VISHAY.

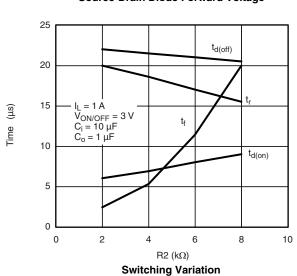
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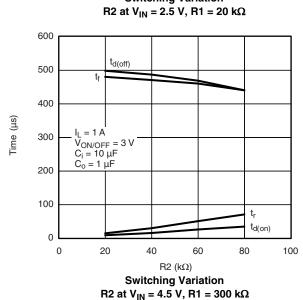


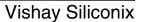






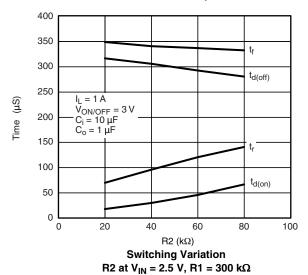






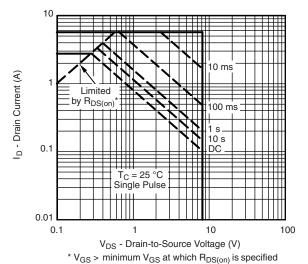


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



350 300 250 t<sub>d(off)</sub> 200  $I_L = 1 A$  $V_{ON/OFF} = 3 V$ Time  $C_i = 10 \, \mu F$ 150  $C_0 = 1 \mu F$  $t_{d(on)}$ 100 50 0 0 20 80 100 R2 ( $k\Omega$ )

**Switching Variation** R2 at  $V_{IN}$  = 1.8 V, R1 = 300 k $\Omega$ 



Safe Operating Area, Junction-to-Case

Duty Cycle = 0.5 Normalized Effective Transient Thermal Impedance 0.2  $P_{DM}$ 0.05 1. Duty Cycle, D =  $\frac{t_1}{t_2}$ 0.02 2. Per Unit Base = R<sub>thJA</sub> = 150 °C/W 3.  $T_{JM}$  -  $T_A = P_{DM}Z_{thJA}^{(t)}$ Single Pulse 4. Surface Mounted 0.01

Square Wave Pulse Dureation (s) Normalized Thermal Transient Impedance, Junction-to-Ambient

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10-3

10-2

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