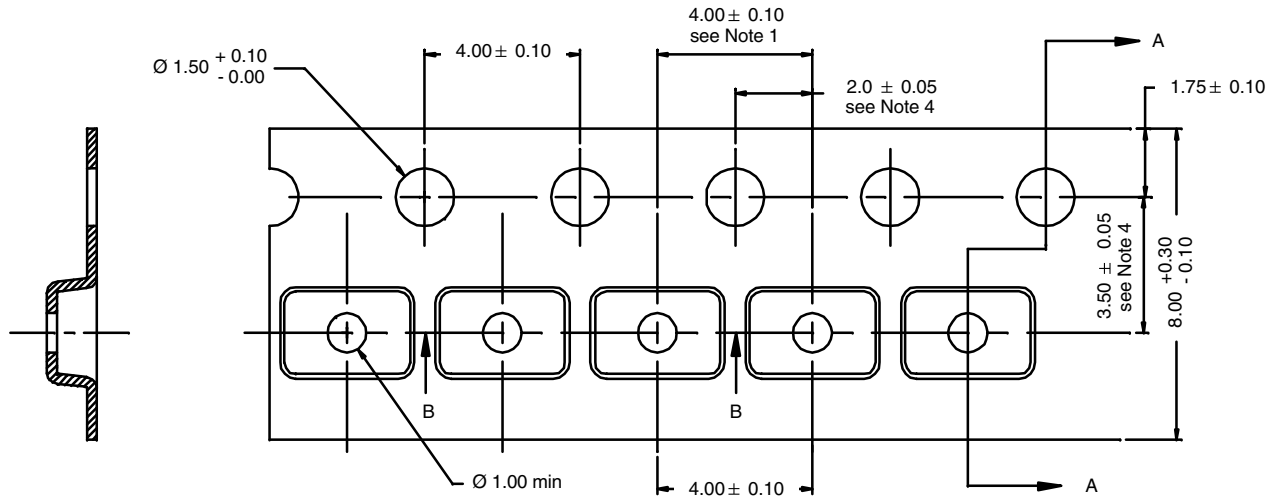
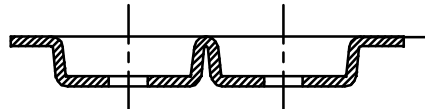


PowerPAK® ChipFET®



SECTION A-A



SECTION B-B

Notes

1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1 mm in 100 mm, also not to exceed 1.5 cm in 1 m actually.
3. Material: Black conductive or black static dissipative.
4. A_0 and B_0 measured on a plane 0.3 mm above the bottom of the pocket.
5. K_0 measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. It should be measured from:
 - a. sprocket hole to pocket center
 - and
 - b. sprocket hole to pocket hole
7. All sizes in mm unless specified.
8. Tolerances will be ± 0.1 mm unless specified.
9. Vishay part number must be labeled at all reels of carrier tape.
10. Surface resistivity - 10^5 max. Ω/sq .

QUANTITY PER REEL	
T1	3000
ECN: C10-0945-Rev. B, 04-Oct-10 DWG: 93-5248-X	