

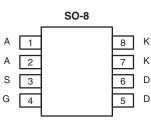
Vishay Siliconix

P-Channel 30 V (D-S) MOSFET with Schottky Diode

MOSFET PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)		
- 30	0.072 at V _{GS} = - 10 V	- 4.6	- 4.6		
- 30	0.110 at V _{GS} = - 4.5 V	- 3.4	- 4.0		

SCHOTTKY PRODUCT SUMMARY

	V _F (V)	
V _{KA} (V)	Diode Forward Voltage	I _D (A) ^a
30	0.50 V at 1 A	2.4



Top View

LITTLE FOOT[®] *Plus* Power MOSFET Compliant to PoHS Directive 2002/05/

FEATURES

Definition

Compliant to RoHS Directive 2002/95/EC

• Halogen-free According to IEC 61249-2-21



FREE

Available

P-Channel MOSFET

Ordering Information: Si4833ADY-T1-E3 (Lead (Pb)-free) Si4833ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage (MOSFET)		V _{DS}	- 30		
Reverse Voltage (Schottky)		V _{KA}	- 30	V	
Gate-Source Voltage (MOSFET)		V _{GS}	± 20		
	T _C = 25 °C		- 4.6		
Continuous Drain Current (T _{.1} = 150 °C) (MOSFET)	T _C = 70 °C	I _D	- 3.6		
	T _A = 25 °C	טי	- 3.85 ^{b, c}		
	T _A = 70 °C		- 3.08 ^{b, c}		
Pulsed Drain Current (MOSFET)	I _{DM}	- 20	A		
Continuous Courses Current (MOCEET Diada Conduction)		I _S	- 2.3		
Continuous Source Current (MOSFET Diode Conduction)		'5	- 1.4 ^{b, c}		
Average Forward Current (Schottky)	١ _F	- 1.4 ^b			
Pulsed Forward Current (Schottky)	I _{FM}	- 20			
	T _C = 25 °C		2.75		
Manianan Dissignation (MOCEET and Cohottlan)	T _C = 70 °C	P _D	1.75	w	
Maximum Power Dissipation (MOSFET and Schottky)	T _A = 25 °C	' D	1.93 ^{b, c}	~~~	
	T _A = 70 °C		1.23 ^{b, c}		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient (MOSFET and Schottky) ^{b, c, d}	R _{thJA}	60	65	°C/W	
Maximum Junction-to-Foot (Drain) (MOSFET and Schottky)	R _{thJF}	35	45	0/11	

Notes:

a. Based on T_C = 25 °C.

b. Surface mounted on FR4 board.

c. t ≤ 10 s.

d. Maximum under steady state conditions is 120 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_{D} = -250 \mu A$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS/TJ}$	L 050 ··· A		- 28			
V _{GS(th)} Temperature Coefficient	∆V _{GS(th)/TJ}	I _D = 250 μA		3.5		mV/°C	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1	- 2	- 2.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 75 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 V$, $V_{GS} = -10 V$	- 5			Α	
		V _{GS} = - 10 V, I _D = - 3.6 A		0.059	0.072	- Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 2.8 A		0.090	0.110		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 3.6 A		7		S	
Dynamic ^b		-					
Input Capacitance	C _{iss}			380	750		
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		100		pF	
Reverse Transfer Capacitance	C _{rss}			75			
Total Gate Charge		V_{DS} = - 15 V, V_{GS} = - 10 V, I_{D} = - 3 A		9.8	15		
				4.6	7.0		
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 3 A		1.4		nC	
Gate-Drain Charge	Q _{gd}			2.4			
Gate Resistance	R _g	f = 1 MHz		8	16	Ω	
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 7.5 Ω		59	90		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 2 A, V_GEN = - 4.5 V, R_g = 1 Ω		26	40	1	
Fall Time	t _f			19	30		
Turn-On Delay Time	t _{d(on)}			7	14	ns	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 7.5 Ω		11	17		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 2 A, V_GEN = - 10 V, R_g = 1 Ω		19	30		
Fall Time	t _f			8	15]	
Drain-Source Body Diode Characteristi	cs						
Continous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 4.6	^	
Pulse Diode Forward Current ^a	I _{SM}				- 20	A	
Body Diode Voltage	V _{SD}	I _S = - 1.4 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			23	40	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 2 A, dl/dt = 100 A/μs, T _J = 25 °C		12	20	nC	
Reverse Recovery Fall Time	t _a	$_{1}$ $_{F}$ = $^{-2}$ A, $_{U}$ $_{U}$ $_{U}$ = 100 A/µs, $_{1}$ $_{J}$ = 25 °C		10			
Reverse Recovery Rise Time	t _b			13		ns	

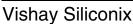
Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.



SCHOTTKY SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage Drop	V _F	I _F = 1 A		0.45	0.50	V	
		I _F = 1 A, T _J = 125 °C		0.36	0.42	v	
Maximum Reverse Leakage Current	I _{rm}	V _R = 30 V		0.004	0.1		
		V _R = 30 V, T _J = 75 °C		0.1	2	mA	
		V _R = 30 V, T _J = 125 °C		3	20		
Junction Capacitance	CT	V _R = 10 V		62		pF	

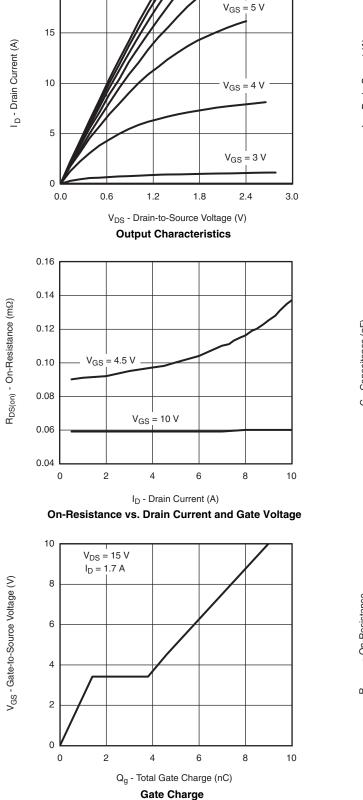
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

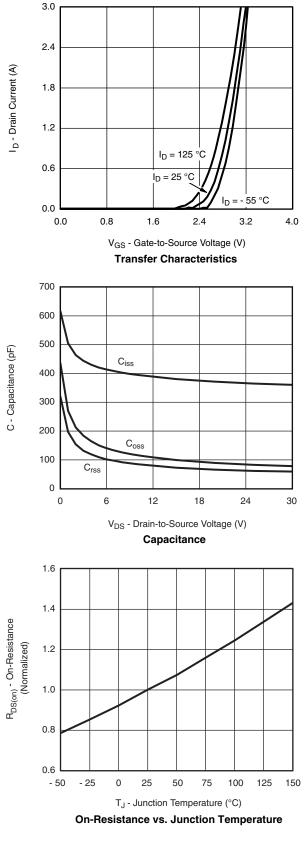


20

 $V_{GS} = 10$ V thru 6 V

MOSFET TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



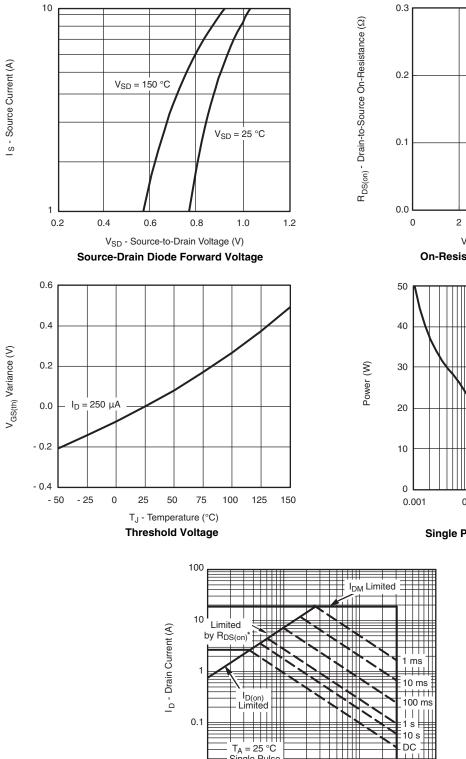


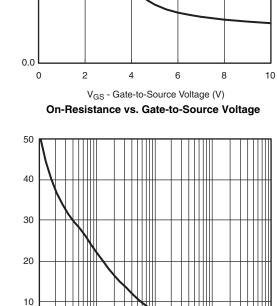
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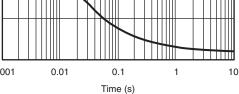


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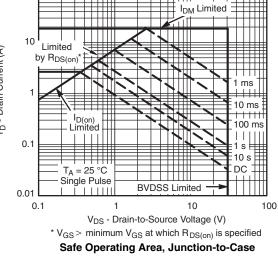
MOSFET TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







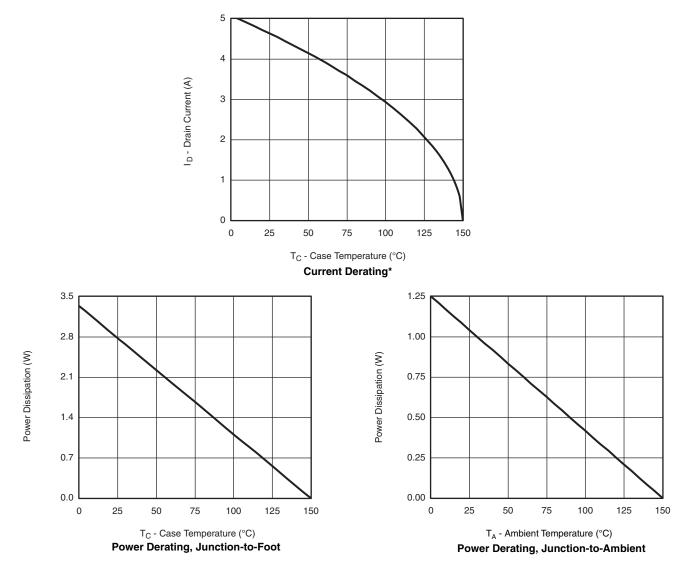
Single Pulse Power, Junction-to-Ambient



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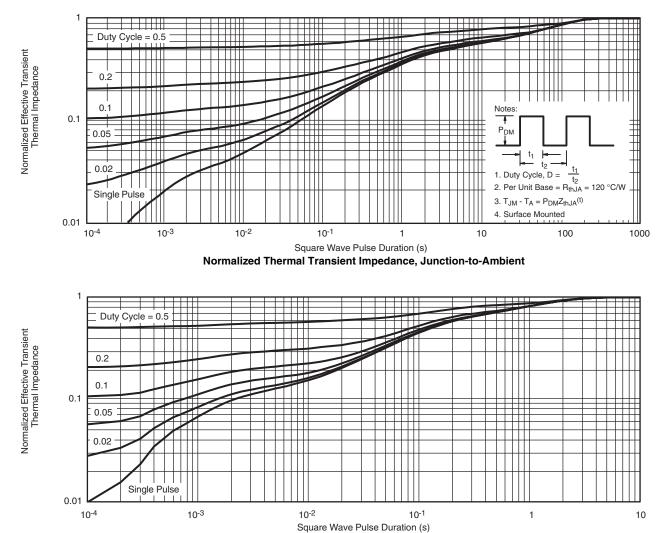
MOSFET TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



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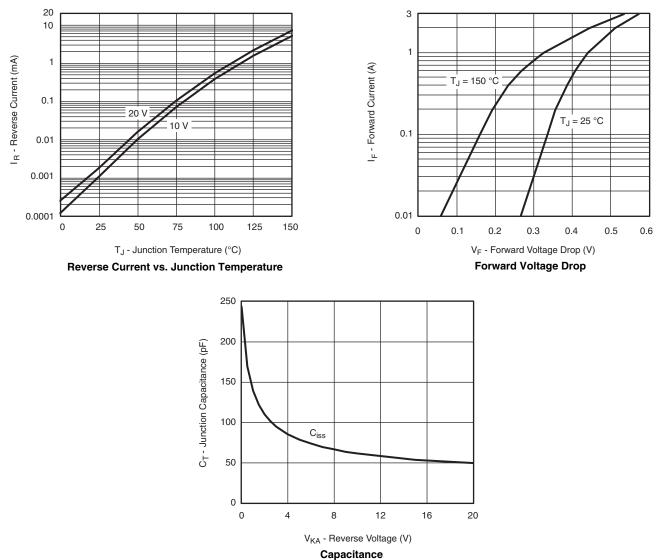
Normalized Thermal Transient Impedance, Junction-to-Foot

MOSFETS TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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SCHOTTKY TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?73627.



Package Information

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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INCHES			
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

Application Note 826

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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1