



Dual N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A)	Q _g (Typ.)			
30	$0.035 \text{ at V}_{GS} = 10 \text{ V}$	6.9	4.5 nC			
30	0.051 at $V_{GS} = 4.5 \text{ V}$	5.7	4.5110			

FEATURES

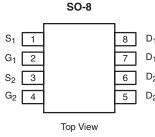
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

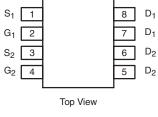


FREE

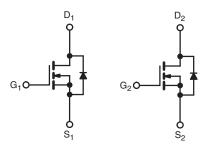
APPLICATIONS

- Low Current DC/DC Conversion
- Notebook System Power





Ordering Information: Si4936BDY-T1-E3 (Lead (Pb)-free) Si4936BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V_{DS}	30	V		
Gate-Source Voltage		V_{GS}	± 20	7 v		
	T _C = 25 °C	I _D	6.9			
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C		5.5			
Continuous Diam Guitent (1) = 150 G)	T _A = 25 °C		5.9 ^{a, b}	A		
	T _A = 70 °C		4.7 ^{a, b}			
Pulsed Drain Current	•	I _{DM}	30			
Continuous Source-Drain Diode Current	T _C = 25 °C	- I _S	2.3			
Continuous Source-Drain Diode Current	T _A = 25 °C		1.7 ^{a, b}			
	T _C = 25 °C	P _D	2.8			
Maximum Power Dissipation	T _C = 70 °C		1.8	w		
Maximum Power Dissipation	T _A = 25 °C		2 ^{a, b}	7 vv		
	T _A = 70 °C	1	1.3 ^{a, b}	1		
Operating Junction and Storage Temperature	e Range	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R_{thJA}	58	62.5	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	38	45	O/ V V		

Notes:

- a. Surface Mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 110 °C/W.

Si4936BDY

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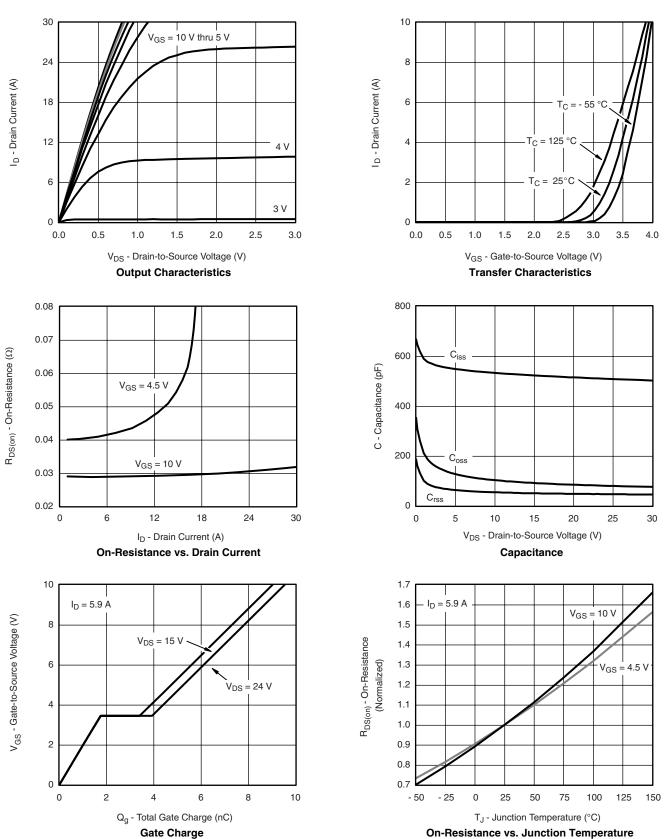
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V, } I_D = 250 \mu\text{A}$	30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		26.5		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5.6		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.5		3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	ns
·	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	μΑ
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α
		$V_{GS} = 10 \text{ V}, I_D = 5.9 \text{ A}$		0.029 0.035		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 4.9 \text{ A}$			0.051	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 5.9 \text{ A}$		12		S
Dynamic ^b					l	
Input Capacitance	C _{iss}			530		pF
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		100		
Reverse Transfer Capacitance	C _{rss}			55		
Total Gate Charge	0	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5.9 \text{ A}$		9.1	15	nC
	Q _g			4.5	7	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5.9 \text{ A}$		1.8		
Gate-Drain Charge	Q _{gd}			1.7		
Gate Resistance	R_g	f = 1 MHz		3		Ω
Turn-On Delay Time	t _{d(on)}			20	30	
Rise Time	t _r	V_{DD} = 15 V, R_L = 3.2 Ω		130	195	- - -
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 4.7$ A, V_{GEN} = 4.5 V, R_g = 1 Ω		12	20	
Fall Time	t _f			32	50	
Turn-On Delay Time	t _{d(on)}			5	10	ns
Rise Time	t _r	V_{DD} = 15 V, R_L = 3.2 Ω		25	40	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 4.7$ A, V_{GEN} = 10 V, R_g = 1 Ω		12	20	
Fall Time	t _f			10	15	
Drain-Source Body Diode Characteristi	cs				•	
Continuous Source-Drain Diode Current	I _S	$T_C = 25 ^{\circ}C$			6.9	۸
Pulse Diode Forward Current	I _{SM}				30	A
Body Diode Voltage	V_{SD}	$I_S = 4.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			20	40	ns
Body Diode Reverse Recovery Charge	Q _{rr}	L = 4.7.4 dl/dt = 100.4/ T = 05.90		12	24	nC
Reverse Recovery Fall Time	t _a	$I_F = 4.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		11		
Reverse Recovery Rise Time t _b				9		ns

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

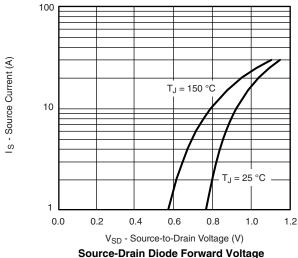


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

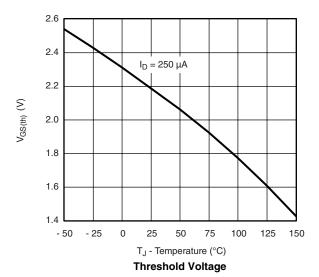


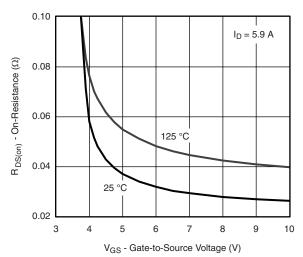
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

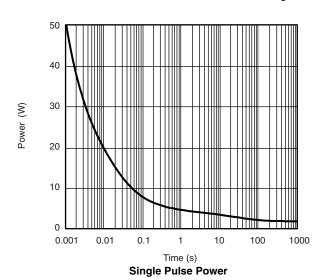


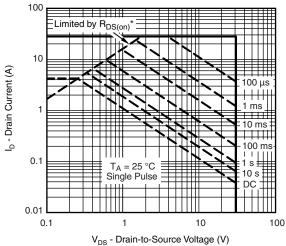
Source-Drain Diode Forward Voltage





On-Resistance vs. Gate-to-Source Voltage





 $\rm V_{DS}$ - Drain-to-Source Voltage (V) * $\rm V_{DS}$ > minimum $\rm V_{GS}$ at which $\rm R_{DS(on)}$ is specified

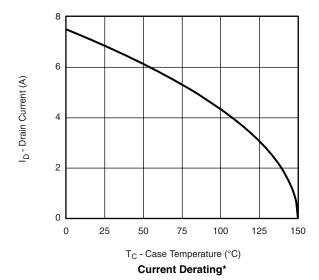
Safe Operating Area, Junction-to-Ambient

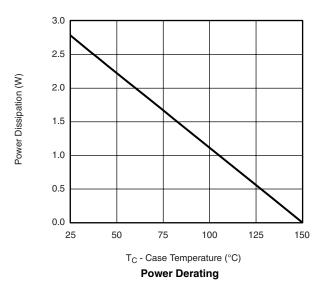




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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



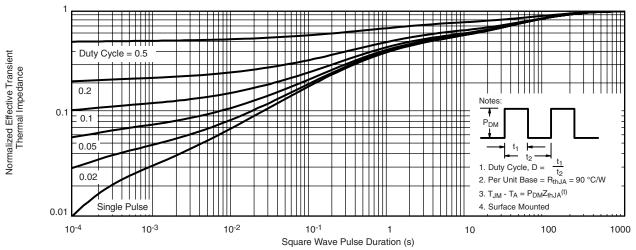


^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

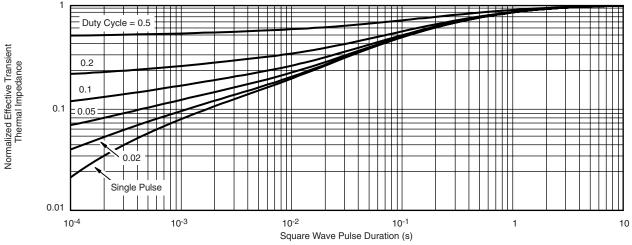
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?74469.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INC	INCHES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
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DWG: 5498

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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