## SUP90330E

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Vishay Siliconix

# N-Channel 200 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V <sub>DS</sub> (V)	200			
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 10 V	0.0375			
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_GS$ = 7.5 V	0.0422			
Q <sub>g</sub> typ. (nC)	21			
I <sub>D</sub> (A)	35.1			
Configuration	Single			

### **FEATURES**

- ThunderFET<sup>®</sup> power MOSFET
- Low R<sub>DS</sub> Q<sub>g</sub> figure-of-merit (FOM)
- Maximum 175 °C junction temperature
- 100 % R<sub>g</sub> and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- Synchronous rectification
- Power supplies
- DC/AC inverter
- DC/DC converter
- Solar micro inverter
- Motor drive switch

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free and halogen-free	SUP90330E-GE3

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ , unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V <sub>DS</sub>	200	V
Gate-source voltage		V <sub>GS</sub>	± 20	V
Continuous drain current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	35.8	
Continuous drain current	T <sub>C</sub> = 125 °C		20.7	
Pulsed drain current (t = 100 µs)		I <sub>DM</sub>	70	A
Continuous source-drain diode current		I <sub>S</sub>	12.5	
Single pulse avalanche current <sup>a</sup>		I <sub>AS</sub>	33	
Single pulse avalanche energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	54.45	mJ
Manimum and a disain ation	T <sub>C</sub> = 25 °C	_	125 <sup>b</sup>	
Maximum power dissipation	T <sub>C</sub> = 125 °C	P <sub>D</sub>	41.7 <sup>b</sup>	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	*0
Soldering recommendations (peak temperature) <sup>c</sup>		-	260	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	MAXIMUM	UNIT
Maximum junction-to-ambient (PCB mount) <sup>c</sup>		R <sub>thJA</sub>	40	°C/W
Maximum junction-to-case (drain)	Steady state	R <sub>thJC</sub>	1.2	0/10

#### Notes

a. Duty cycle  $\leq$  1 %

b. See SOA curve for voltage derating

c. When mounted on 1" square PCB (FR4 material)

S17-0649-Rev. A, 01-May-17

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COMPLIANT

HALOGEN

FREE

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PARAMETER	SYMBOL	IBOL TEST CONDITIONS		TYP.	MAX.	UNIT	
Static						•	
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	200	-	-	V	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	2	-	4	V	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	250	nA	
		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1		
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS}$ = 200 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C	-	-	150	μA	
		$V_{DS}$ = 200 V, $V_{GS}$ = 0 V, $T_{J}$ = 175 °C	-	-	5	mA	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \geq 10 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	20	-	-	Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12.2 A	-	0.0312	0.0375	0	
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 7.5 \text{ V}, \text{ I}_{D} = 11.5 \text{ A}$	-	0.0337	0.0422	Ω	
Forward transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	-	28	-	S	
Dynamic <sup>b</sup>					•	<u> </u>	
Input capacitance	Ciss		-	1172	-		
Output capacitance	C <sub>oss</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	-	150	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>		-	11	-		
Total gate charge	Qg		-	21	32		
Gate-source charge	Q <sub>gs</sub>	$V_{DS}$ = 100 V, $V_{GS}$ =10 V, $I_{D}$ = 12.2 A	-	6	-	nC	
Gate-drain charge	Q <sub>gd</sub>		-	5.3	-		
Gate resistance	Rg	f = 1 MHz	0.76	3.8	7.6	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	12	24		
Rise time	t <sub>r</sub>	$V_{DD} = 100 \text{ V}, \text{ R}_{\text{L}} = 14.2 \Omega, \text{ I}_{\text{D}} \cong 7 \text{ A},$	-	25	50	ns	
Turn-off delay time	t <sub>d(off)</sub>	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	30	50		
Fall time	t <sub>f</sub>		-	22	44		
Drain-Source Body Diode Characteristi	cs					1	
Pulse diode forward current (t = 100 µs)	I <sub>SM</sub>		-	-	70	Α	
Body diode voltage	V <sub>SD</sub>	I <sub>F</sub> = 7 A, V <sub>GS</sub> = 0 V	-	0.8	1.5	V	
Body diode reverse recovery time	t <sub>rr</sub>		-	111	170	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>		-	0.51	1	μC	
Reverse recovery fall time	ta	I <sub>F</sub> = 7 A, di/dt = 100 A/μs	-	94	-		
Reverse recovery rise time	t <sub>b</sub>		-	17	-	ns	
Body diode peak reverse recovery charge	I <sub>RM(REC)</sub>		-	8.5	17	А	

#### Notes

a. Pulse test; pulse width  $\leq 300~\mu\text{s},\,duty~cycle \leq 2~\%$ 

b. Guaranteed by design, not subject to production testing

c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

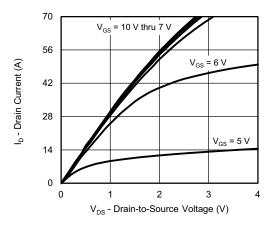
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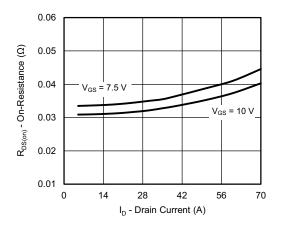
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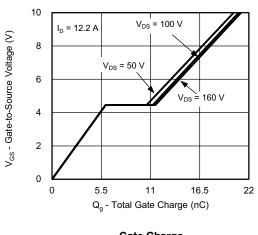
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



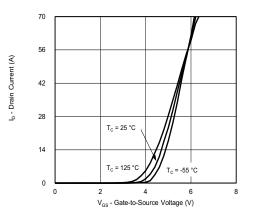
**Output Characteristics** 



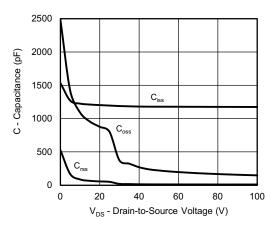
**On-Resistance vs. Drain Current and Gate Voltage** 



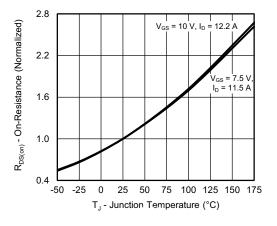
Gate Charge



**Transfer Characteristics** 



Capacitance



**On-Resistance vs. Junction Temperature** 

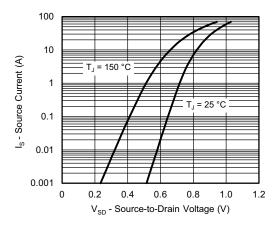
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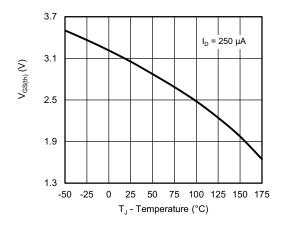
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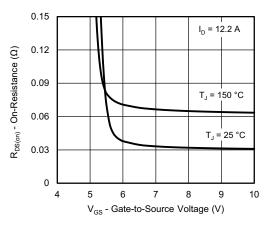
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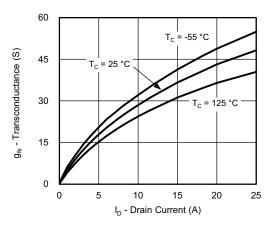
Source-Drain Diode Forward Voltage



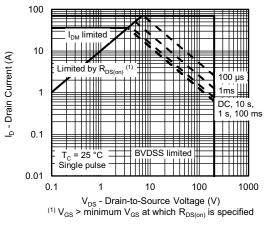
**Threshold Voltage** 



**On-Resistance vs. Gate-to-Source Voltage** 



Transconductance

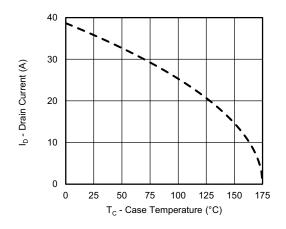


Safe Operating Area, Junction-to-Ambient

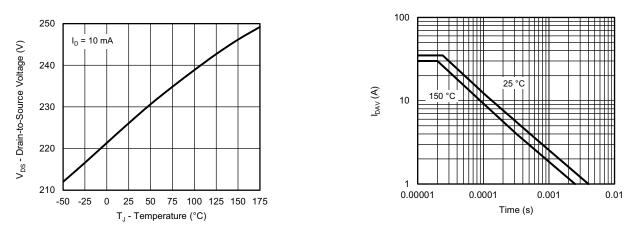
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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating <sup>a</sup>



Drain Source Breakdown vs. Junction Temperature

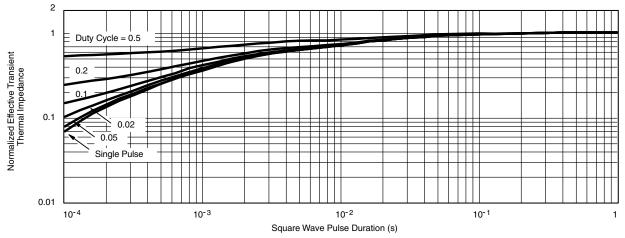
Avalanche vs. Time

#### Note

a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 25 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

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# **TO-220AB**



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
E	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØР	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
	0413-Rev. P,		0.102	0.118

Note

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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