



Vishay Siliconix

650M SG PROCESS TECHNOLOGY	
Sample size	7872
Equivalent device hours	1 365 016 063
Failure rate in FIT	0.667

Failure rate in FIT is calculated according to JEDEC® standard JESD85, methods for calculating failure rates in units of FITs, based on accelerated high temperature operating life test results by using an apparent activation energy of 0.7 eV. The junction temperature of the device at use is assumed to be 55 °C. A constant failure rate distribution is assumed. The upper confidence bound of the failure rate is 60 %.

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