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Power ICs

White Paper

microBRICK[™]: A Simple Solution to Building Efficient High Density DC/DC Converters With a Wide Input Voltage Range

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One of the biggest challenges in system design is powering the next generation of microprocessors, DSPs, FGPAs, and ASICs used in servers, telecom equipment, and IoT systems. Maximizing performance and high power delivery becomes even more difficult as processors get faster, space availability diminishes, and power requirements increase such as in 5G networks and small cells, radio units, and robots. Consequently, these emerging applications demand high efficiency, high power density DC/DC conversion solutions. System designers need these DC/DC solutions to be simple to work with, even while delivering features such as a wide input voltage range and ultrafast transient response. For maximum performance, the best solution will integrate multiple functions into a single, compact package.

Today's designers have many options to choose from, varying not only by supplier but also by the level of integration. At one end of the spectrum are traditional solutions that combine an IC controller, discrete MOSFETs, and numerous passive components. This "discrete" approach provides a high level of flexibility and potentially lowers bill of materials costs, but on the other hand requires a longer design and validation time. It is a riskier strategy too, given the high level of technical expertise it requires. More recently, solutions integrating the IC controller and power MOSFETs into a single package have emerged. This approach provides higher efficiency, while enabling faster switching frequencies and more features and protections in compact packages. On the far end of the integration spectrum are various module solutions in which both active and passive devices are combined into a single package. Until now, however, they have often remained out of reach due to higher prices. This is mainly because the large component size and low production volumes tend to increase manufacturing costs.

Semiconductor manufacturers are working to improve product performance and lower costs with innovations in silicon and new wide band gap materials. This same process is resulting in smaller and smaller package sizes.

A smaller package comes at a cost, however. One of the main challenges in DC/DC converters is how to dissipate the heat caused by a much higher level of power density. As the component cross section gets smaller and smaller, component density is pushed higher and thus the PCB temperature goes up, since most of the component heat is dissipated through the PCB even if a heatsink is available. This approach might be acceptable for a given design but poses an obstacle to scaling in future products.

With this challenge in mind, Vishay engineers have developed a new type of DC/DC converter module, microBRICKTM, in which the package size is almost equal to just the inductor, with the same respective dimensions. In other words, the real estate area occupied by the IC and power MOSFET has been shrunk to nearly zero, yet they still provide improved performance over typical solutions.

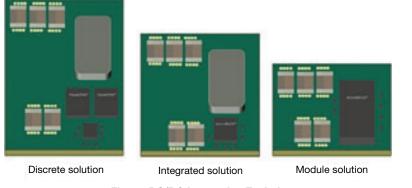


Fig. 1 - DC/DC Integration Evolution

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As an innovative packaging solution, the microBRICK module offers several advantages, both thermal and electrical. It addresses the problem of heat dissipation in two ways. In the first, the highest temperature component (typically the power MOSFET) is thermally coupled to a larger cooler component (the inductor). This thermal structure provides a superior thermal solution, as the inductor acts like a built-in heatsink.

The second advantage, which is also thermal, is to better utilize the large area available under the inductor for improved MOSFET power dissipation purposes. Placing the MOSFET underneath the inductor allows for a much larger effective cross section to the PCB without any additional area penalty.

From an electrical perspective, the 3D structure of the Vishay module eliminates the PCB interconnect resistance between the inductor and the switching node. This parasitic resistance is governed by the PCB's top Cu layer thickness (typically less than 2 oz), and as a part of total losses is of the same magnitude as the MOSFET's $R_{DS(on)}$ and the inductor's DCR.

This unique structure provides lower conduction losses and superior thermal performance, which results in a lower junction temperature.

Meanwhile, the lower temperature of the high efficiency microBRICK module enables improved reliability and a wider safe operating area to support higher ambient temperatures or to compensate for smaller board space.

Measuring only 10.6 mm by 6.5 mm with an industry-low 3 mm profile, Vishay's microBRICK modular solution offers a < 30 % smaller area and > 50 % smaller volume when compared to the closest competing module. Unlike BGA and LGA packages, the device's wettable flank power QFN package improves board-level reliability (BLR) and simplifies assembly and testing, while providing production with automatic inspection (AOI) capability.

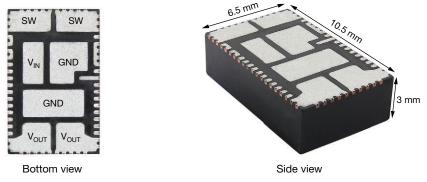


Fig. 2 - microBRICK Package

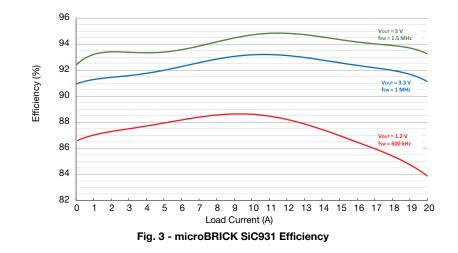
Perhaps most impressively, the SiC931 is capable of delivering 20 A continuous current at a switching frequency up to 2 MHz, while providing an adjustable output voltage down to 0.6 V from an input rail of 4.5 V to 18 V. Additionally,

the architecture supports ultrafast transient response with minimal output capacitance and tight ripple regulation at very light loads.



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The efficiency of the SiC931, as shown in Figure 3, was measured at room temperature on a 2 in x 2 in, 6-layer PCB with no airflow.

Building a high efficiency DC/DC converter with a wide input voltage range is greatly simplified with the microBRICK solution, which integrates the controller, power stage, and inductor in a single compact package. Just a few off the shelf resistors and capacitors are required in order to complete the design using a microBRICK module. The overall smaller solution size enabled by the microBRICK products provides a more affordable total solution compared to other modules, while still improving the DC/DC performance, such as efficiency and transient response.

The SiC931 is the first member of the microBRICK family. Other members, sharing the same form factor, include the SiC967 and SiC951. The SiC967 features a 4.5 V to 60 V input voltage range with 6 A output current. The SiC951 offers a 4.5 V to 20 V input voltage range and 20 A output current supporting a PMBUS 1.3 compliant digital interface with full configuration and telemetry capabilities.

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