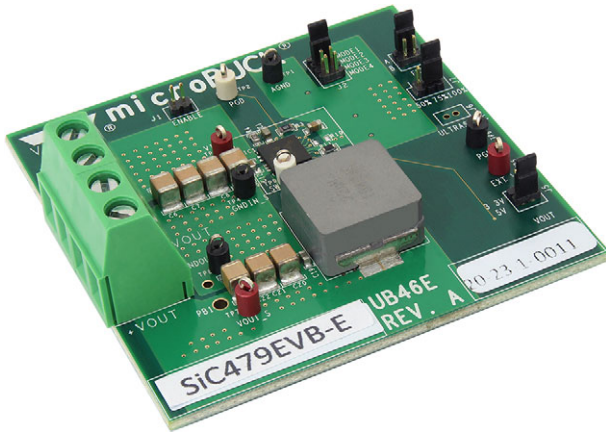


# Reference Board User's Manual for SiC468, SiC469, SiC478, and SiC479 Synchronous Buck Regulators



## DESCRIPTION

The SiC46x and SiC47x are families of wide input voltage high efficiency synchronous buck regulator with integrated high side and low side power MOSFETs. Its power stage is capable of supplying high continuous current at up to 2 MHz switching frequency. These regulators produce an adjustable output voltage down to 0.8 V from 4.5 V to 60 V input voltage for SiC46x, and 55 V for SiC47x input rail to accommodate a variety of applications, including computing, consumer electronics, telecom, and industrial.

SiC46x's and SiC47x's architecture deliver ultrafast transient response with minimum output capacitance and tight ripple regulation at very light load. The device is internally compensated and is stable with any capacitor. No external ESR network is required for loop stability purpose. The device also incorporate a power saving scheme that significantly increases light load efficiency. The regulators integrates a full protection set, including over current protection (OCP), output overvoltage protection (OVP), short circuit protection (SCP), output undervoltage protection (UVP) and thermal shutdown (OTP). They also have UVLO for input rail and a user programmable soft start.

SiC46x family is available in 2 A, 4 A, 6 A, 10 A pin compatible and SiC47x family is available in 3 A, 5 A, 8 A, 12 A pin compatible 5 mm by 5 mm lead (Pb)-free power enhanced MLP55-27L package.

ORDERING INFORMATION		
BOARD PART NUMBER	MAX. OUTPUT CURRENT	BOARD MARKING
SiC468EV-B-E	4 A	UB46E
SiC469EV-B-E	2 A	UB46E
SiC478EV-B-E	5 A	UB46E
SiC479EV-B-E	3 A	UB46E

## FEATURES

- Versatile
  - Single supply operation from 4.5 V to 60 V input voltage for SiC468 and SiC469, and 55 V for SiC478 and SiC479
  - Adjustable output voltage down to 0.8 V
  - Scalable solution from 2 A to 5 A
  - Support start-up with pre-bias output voltage
  - $\pm 1\%$  output voltage accuracy at  $-40\text{ }^{\circ}\text{C}$  to  $+125\text{ }^{\circ}\text{C}$
- Internal compensation
- Highly efficient
  - 98 % peak efficiency
  - 4  $\mu\text{A}$  supply current at shutdown
  - 156  $\mu\text{A}$  operating current not switching
- Highly configurable
  - Adjustable switching frequency from 100 kHz to 2 MHz
  - Adjustable soft start and selectable present 100 %, 75 %, and 50 % current limit
  - 2 modes of operation, forced continuous conduction, power save
- Robust and reliable
  - Output over-voltage protection
  - Output under-voltage / short circuit protection with auto retry
  - Power good flag and over temperature protection
  - Supported by Vishay PowerCAD online design simulation
- Design support tools
  - Inductor selection tool  
[www.vishay.com/inductors/calculator/calculator](http://www.vishay.com/inductors/calculator/calculator)
  - Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

## APPLICATIONS

- Industrial and automation
- Home automation
- Industrial and server computing
- Networking, telecom, and base station power supplies
- Wall transformer regulation
- Robotics
- High end hobby electronics: remote control cars, planes, and drones
- Battery management systems
- Power tools
- Vending, ATM, and slot machine

## SPECIFICATIONS

This reference board allows the user to evaluate the SiC468, SiC469, SiC478, and SiC479 microBUCK® regulators for their features and functionalities. It can also be a reference design for a user’s application. The board is configured for 4.5 V to 50 V input. Output current capability is model dependent. The operating range may be modified by changing components such as the inductor, and feedback resistor.

## BOARD CONFIGURATION TABLE

### SiC468, SiC478 EVB TYPICAL PRE-DEFINED OPERATING CONFIGURATIONS

V <sub>IN</sub> (V)	V <sub>OUT</sub> (V)	f <sub>sw</sub> (kHz)	L (μH)	MAXIMUM I <sub>OUT</sub> (A) SiC468 / SiC478
48.0	3.3	300	8.2	4 / 5
48.0	5.0	300	12.0	4 / 5

### SiC469, SiC479 EVB TYPICAL PRE-DEFINED OPERATING CONFIGURATIONS

V <sub>IN</sub> (V)	V <sub>OUT</sub> (V)	f <sub>sw</sub> (kHz)	L (μH)	MAXIMUM I <sub>OUT</sub> (A) SiC469 / SiC479
48.0	3.3	300	15.0	2 / 3
48.0	5.0	300	22.0	2 / 3

## CONNECTION AND SIGNAL / TEST POINTS

### Power Terminals (J4)

- V<sub>IN</sub>, GND (J4, pin 1 and pin 2): (see Fig. 1)

Fig. 1

Connect to a voltage source: 36 V to 50 V. This reference board configures 32 V as the minimal value of input voltage to enable the chip, and 27 V as the input voltage’s under-voltage lockout (V<sub>IN</sub> UVLO) voltage.

Both values can be modified by changing resistance of R2 using the equation in (1) and (2).

$$R2 = \frac{(PVIN_{EN} - 1.4) \times R1}{1.4} \quad (1)$$

where PVIN<sub>EN</sub> is the level of input voltage, in volts, enabling the chip, R1 is the resistance of resistor R1 populated as 2 kΩ on this reference board.

$$PVIN_{UVLO} = 1.2 \times \frac{(R1 + R2)}{R1} \quad (2)$$

where PVIN<sub>UVLO</sub> is the level of input voltage, in volts, applying V<sub>IN</sub> UVLO protection.

EVB provides another option to configure V<sub>IN</sub> UVLO by removing R2 and populate a Zener diode D1 on board. In this case, PVIN<sub>EN</sub> will be 1.4 V above reverse breakdown voltage of D1.

The minimum value of PVIN<sub>EN</sub> has to be 4.4 V, which shall be higher than V<sub>DD</sub> UVLO threshold voltage.

- V<sub>OUT</sub>, GND (J4, pin 3 and pin 4): (see Fig. 1)

Connect to a load that draws no more than:

SiC468 - 4 A, SiC469 - 2 A

SiC478 - 5 A, SiC479 - 3 A

The output voltage is selectable on J5.

### Note

- The output capacitors are rated to 25 V. Should a higher output voltage be required, the output capacitors should be changed to ones with an appropriate higher voltage rating

## SELECTION JUMPERS

### Operation Mode Select Using J2 and J6

J2 allows the user to select modes of power saving and whether using an internal LDO regulator or an external power supply as V<sub>DRV</sub> for operation.

J6 is reserved for future use, please always leave the jumper open.

The Table below lists all modes of operation and their related jumper setup.

OPERATION MODES				
MODE	POWER SAVE MODE	V <sub>DRV</sub> SUPPLY	J2 JUMPER SETUP (FOLLOWING SILKSCREEN LABEL)	J6 JUMPER SETUP
1	Enabled	Internal LDO	Pin 1 - 2 shorted	Open
2	Disabled	Internal LDO	Pin 3 - 4 shorted	Open
3	Disabled	External supply	Pin 5 - 6 shorted	Open
4	Enabled	External supply	Pin 7 - 8 shorted	Open

### V<sub>DRV</sub> External Supply

**5 V (TP5), GND (TP6):** this is the terminal that enables the user to use an external 5 V<sub>DC</sub> supply as MOSFET gate driver supply when mode 3 or 4 in Table “Operation Modes” is selected. This should only be used in mode 3 and 4.



### Enable

**J1** is a 2 pin header crossing EN pin to ground. The EN pin has an internal high impedance pull down resistor and requires an DC voltage higher than 1.43 V to enable chip operation. Shorting **J1** with a jumper will disable the chip operation

### Output Voltage $V_{OUT}$ Setting

**J5** allows the user to choose four options of output voltage divider to set different output voltage  $V_{OUT}$ . **J5** is a 2 x 2 eight pin header and table “Output Voltage Setting” lists the available voltage setting and related jumper setup. The user can use equation in datasheet to calculate required resistance for a designated output voltage.

OUTPUT VOLTAGE SETTING		
$V_{OUT}$ DESIRED (V)	J5 JUMPER SETUP (FOLLOWING SILKSCREEN LABEL)	RESISTANCE BETWEEN $V_{OUT}$ AND FB PIN (k $\Omega$ )
3.3	Pin 1 - 3 shorted	31.6
5.0	Pin 2 - 4 shorted	53.6

### Switching Frequency $f_{sw}$ Setting

**J3** allows the user to choose correct resistance to achieve 300 kHz switching frequency after the users set up output voltage following table “Output Voltage Setting”. **J3** is a 2 x 2 four pin header and it generates logic level of input pins of **U1** (DG2034, multiplexers). Table “Switching Frequency Setting” lists the related logic level of **U1** matching different output voltages in table “Output Voltage Setting” and related **J3** jumper setup. The user can use equation in datasheet to calculate required resistance to achieve a desired switching frequency with a designated output voltage.

SWITCHING FREQUENCY SETTING				
SWITCHING FREQUENCY DESIRED (kHz)	$V_{OUT}$ (V) <sup>(1)</sup>	U1 INPUT LOGIC	J3 JUMPER SETUP	RESISTANCE BETWEEN $f_{sw}$ AND GND (k $\Omega$ )
300	3.3	2'b01	Pin 1 - 2 shorted	57.6
	5.0	2'b10	Pin 3 - 4 shorted	88.7
Others		2'b00	All pins leave open	Not populated. Customized R10 following datasheet
		2'b11	Both pin 1 - 2 and pin 3 - 4 shorted	

### Note

(1)  $V_{OUT}$  set in table “Output Voltage Setting”

### Maximum inductor valley current limit $I_{LM}$ setting

**J7** allows the user to choose three options of the maximum inductor valley current limit. **J7** is a 3 x 1 three pins header and its middle pin (pin 2) connects to number 25 pin  $I_{LIMIT}$  of

IC1 (SiC468, SiC469, SiC478, or SiC479). Pin 1 of **J7** is facing the EVB edge with silkscreen label “100 %”, and Pin 3 of **J7** is facing IC1 with silkscreen label “50 %”. Table “Maximum Valley Current Limit” lists the three optional values of the maximum valley current limit and their related jumper setup.

MAXIMUM VALLEY CURRENT LIMIT SETTING					
$I_{OCP}$ (A)				J7 JUMPER SETUP	IC1 $I_{LIMIT}$ PIN CONNECTION
SiC468	SiC469	SiC478	SiC479		
6	4	6	4	Pin 2 - 1 shorted	$I_{LIMIT}$ tied to $V_{DD}$
4.2	3	4.2	3	Three pins leave open	$I_{LIMIT}$ is not connected
3	2	3	2	Pin 2 - 3 shorted	$I_{LIMIT}$ tied to $A_{GND}$

### SIGNALS AND TEST LEADS

#### Input Voltage Sense

**$V_{IN\_SENSE}$  (TP3),  $GND_{IN\_SENSE}$  (TP4):** this allows the user to measure the voltage directly at the input of the regulator bypassing any losses generated by connections to the board. These test points can also be as a remote sense port of a power source with remote sense capability.

#### Output Voltage Sense

**$V_{OUT\_SENSE}$  (TP7),  $GND_{OUT\_SENSE}$  (TP8):** This allows the user to measure the output voltage directly at the sense point of the regulator bypassing any losses generated by connections to the board. These test points can also be as a remote sense port of an external load with remote sense capability.

#### Power Good Indicator

**$P_{GOOD}$ :** is an open drain output and is pulled up with a 102 k $\Omega$  resistor, R3, to  $V_{DD1}$  ( $\approx$  5 V). When FB or  $V_{OUT}$  are within -10 % to +20 % of the set voltage this pin will go HI to indicate the output is okay.

#### Power Up Procedure

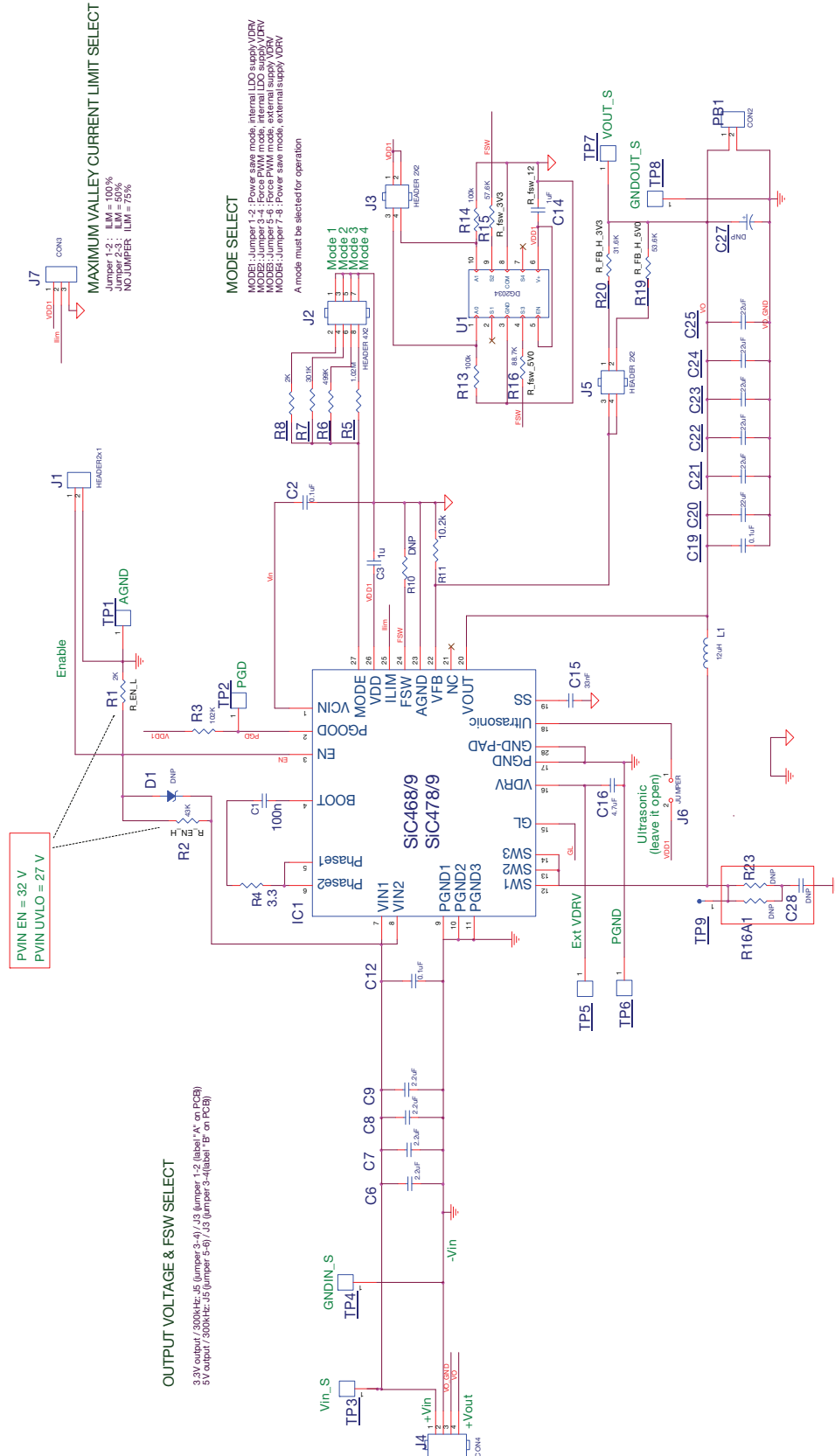
Before turning on the reference board, the user needs to select one of the four modes by shorting one jumper (see section on mode selection). If mode 3 or 4 is selected, make sure  $V_{DRV}$  pin is supplied by external 5 V. There is no specific power sequence requirement to power up the board.

#### Snubber Circuit

Snubber may be used when the user desire to decrease the peak voltage of switching node SW during turn on of the high side switch. There are place holders on the reference board, R16A1, R23, and C28, for the snubber.



SCHEMATIC FOR SiC468, SiC469, SiC478, AND SiC479





## PCB LAYOUT FOR SiC468, SiC469, SiC478, AND SiC479

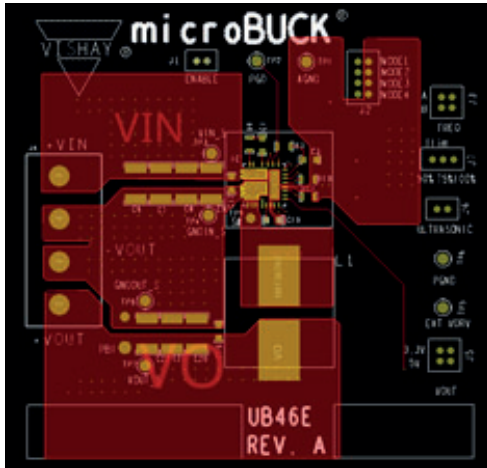


Fig. 2 - Top Layer

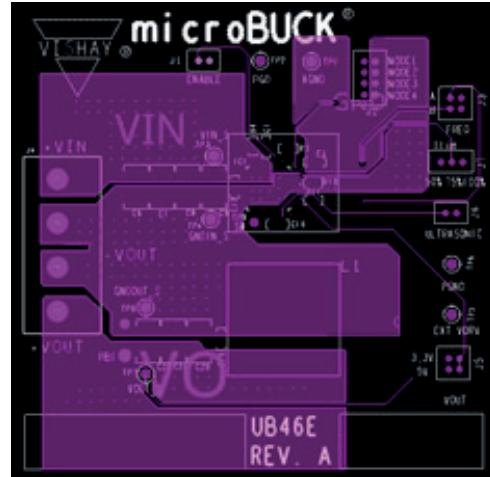


Fig. 4 - Inner Layer 3

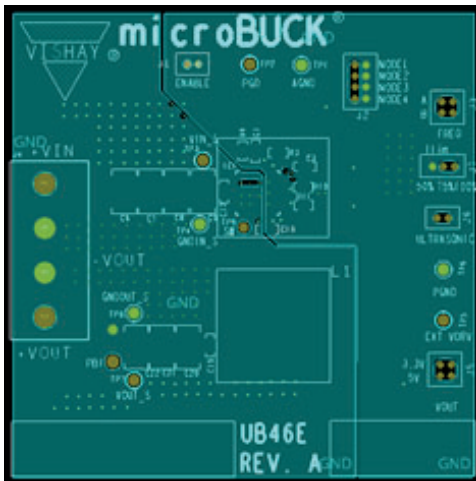


Fig. 3 - Inner Layer 2

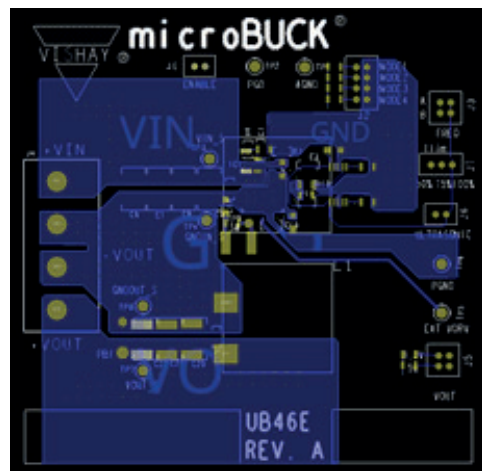


Fig. 5 - Bottom Layer





<b>BILL OF MATERIAL REPORT FOR SiC468, SiC469, SiC478, SiC479</b>					
<b>BOM FOR SiC468, SiC478</b>		<b>BOM FOR SiC469, SiC479</b>		<b>QTY</b>	<b>REFERENCE DESIGNATOR</b>
<b>PART NUMBER</b>	<b>DESCRIPTION</b>	<b>PART NUMBER</b>	<b>DESCRIPTION</b>		
VJ0603Y104KXAAC	0.1 $\mu$ F $\pm$ 10 % 50 V X7R 0603	VJ0603Y104KXAAC	0.1 $\mu$ F $\pm$ 10 % 50 V X7R 0603	2	C1, C19
GRM188R72A104MA35D	0.1 $\mu$ F $\pm$ 10 % 100V X7R 0603	GRM188R72A104MA35D	0.1 $\mu$ F $\pm$ 10 % 100 V X7R 0603	2	C2, C12
GCM188R71E105KA64D	1 $\mu$ F $\pm$ 10 % 25 V X7R 0603	GCM188R71E105KA64D	1 $\mu$ F $\pm$ 10 % 25 V X7R 0603	2	C3, C14
GCJ32DR72A225KA01L	2.2 $\mu$ F $\pm$ 10 % 100 V X7R 1210	GCJ32DR72A225KA01L	2.2 $\mu$ F $\pm$ 10 % 100 V X7R 1210	4	C6, C7, C8, C9
VJ0603Y333KXXAC	33 nF $\pm$ 10 % 25 V X7R 0603	VJ0603Y333KXXAC	33 nF $\pm$ 10 % 25 V X7R 0603	1	C15
VJ0603G475KXJCW1BC	4.7 $\mu$ F $\pm$ 10 % 16 V X5R 0603	VJ0603G475KXJCW1BC	4.7 $\mu$ F $\pm$ 10 % 16 V X5R 0603	1	C16
GRM32ER71E226KE15L	22 $\mu$ F $\pm$ 10 % 25 V X7R 1210	GRM32ER71E226KE15L	22 $\mu$ F $\pm$ 10 % 25 V X7R 1210	6	C20, C21, C22, C23, C24, C25
	DNP		DNP	0	C27, C28
	DNP		DNP	0	D1
SiC468, SiC478	microBUCK® 60 V DC/DC converter 4 A, MicroBuck 55 V DC/DC converter 5 A	SiC469, SiC479	microBUCK 60 V DC/DC converter 2 A, microBUC 55 V DC/DC converter 3 A	1	IC1
GRPB021VWVN-RC	CONN HEADER VERT 2POS 1.27 mm	GRPB021VWVN-RC	CONN HEADER VERT 2POS 1.27 mm	1	J1
GRPB042VWVN-RC	CONN HEADER VERT 8POS 1.27 mm	GRPB042VWVN-RC	CONN HEADER VERT 8POS 1.27 mm	1	J2
GRPB022VWVN-RC	CONN HEADER VERT 4POS 1.27 mm	GRPB022VWVN-RC	CONN HEADER VERT 4POS 1.27 mm	2	J3, J5
ED120/4DS	TERM BLK 4P SIDE ENT 5.08 mm PCB	ED120/4DS	TERM BLK 4P SIDE ENT 5.08 mm PCB	1	J4
	DNP		DNP	0	J6
GRPB031VWVN-RC	CONN HEADER VERT 3POS 1.27 mm	GRPB031VWVN-RC	CONN HEADER VERT 3POS 1.27 mm	1	J7
IHLP5050FDER120M51	12 $\mu$ H, 20 %, 19 m $\Omega$ DCR, 10.6 A I <sub>rms</sub> , 6.9 A I <sub>sat1</sub> at 25 °C (20 % roll off)	IHLP5050FDER220M51	22 $\mu$ H, 20 %, 31.3 m $\Omega$ DCR, 8.3 A I <sub>rms</sub> , 5.5 A I <sub>sat1</sub> at 25 °C (20 % roll off)	1	L1 (recommended for V <sub>OUT</sub> = 5 V)
IHLP5050FDER8R2M01	DNP, 8.2 $\mu$ H, 20 %, 14.5 m $\Omega$ DCR, 10.5 A I <sub>rms</sub> , 16 A I <sub>sat1</sub> at 25°C (20 % roll off)	IHLP5050FDER150M5	DNP, 15 $\mu$ H, 20%, 24 m $\Omega$ DCR, 8.7 A I <sub>rms</sub> , 6.8 A I <sub>sat1</sub> at 25 °C (20 % roll off)	0	L1 (DNP) (off board, recommended for V <sub>OUT</sub> = 3.3 V)
CRCW06032K00FKEAC	RES SMD 2 k $\Omega$ 1 % , 1/10 W 0603	CRCW06032K00FKEAC	RES SMD 2 k $\Omega$ ,1 % 1/10 W 0603	2	R1, R8
CRCW060343K0FKEA	RES SMD 43 k $\Omega$ 1 % 1/10 W 0603	CRCW060343K0FKEA	RES SMD 43 k $\Omega$ ,1 % 1/10 W 0603	1	R2
CRCW0603102KFKEA	RES SMD 102 k $\Omega$ 1 % 1/10 W 0603	CRCW0603102KFKEA	RES SMD 102 k $\Omega$ ,1 % 1/10 W 0603	1	R3
CRCW06033R30FKEA	RES SMD 3.3 $\Omega$ 1 % 1/10 W 0603	CRCW06033R30FKEA	RES SMD 3.3 $\Omega$ ,1 % 1/10W 0603	1	R4
CRCW06031M02FKEA	RES SMD 1.02 m $\Omega$ 1 % 1/10 W 0603	CRCW06031M02FKEA	RES SMD 1.02 m $\Omega$ ,1 % 1/10 W 0603	1	R5
CRCW0603499KFKEA	RES SMD 499 k $\Omega$ 1 % 1/10 W 0603	CRCW0603499KFKEA	RES SMD 499 k $\Omega$ ,1 % 1/10 W 0603	1	R6
CRCW0603301KFKEA	RES SMD 301 k $\Omega$ 1 % 1/10 W 0603	CRCW0603301KFKEA	RES SMD 301 k $\Omega$ ,1 % 1/10 W 0603	1	R7
	DNP		DNP	0	R10, R16A1, R23
CRCW060310K2FKEA	RES SMD 10.2 k $\Omega$ 1 % 1/10 W 0603	CRCW060310K2FKEA	RES SMD 10.2 k $\Omega$ ,1 % 1/10 W 0603	1	R11



<b>BILL OF MATERIAL REPORT FOR SiC468, SiC469, SiC478, SiC479</b>					
<b>BOM FOR SiC468, SiC478</b>		<b>BOM FOR SiC469, SiC479</b>		<b>QTY</b>	<b>REFERENCE DESIGNATOR</b>
<b>PART NUMBER</b>	<b>DESCRIPTION</b>	<b>PART NUMBER</b>	<b>DESCRIPTION</b>		
CRCW0603100KFKEA	RES SMD 100 kΩ, 1 % 1/10 W 0603	CRCW0603100KFKEA	RES SMD 100 kΩ, 1 % 1/10 W 0603	2	R13, R14
CRCW060357K6FKEA	RES SMD 57.6 kΩ, 1 % 1/10 W 0603	CRCW060357K6FKEA	RES SMD 57.6 kΩ, 1 % 1/10 W 0603	1	R15
CRCW060388K7FKEA	RES SMD 88.7 kΩ, 1 % 1/10 W 0603	CRCW060388K7FKEA	RES SMD 88.7 kΩ, 1 % 1/10 W 0603	1	R16
CRCW060353K6FKEA	RES SMD 53.6 kΩ, 1 % 1/10 W 0603	CRCW060353K6FKEA	RES SMD 53.6 kΩ, 1 % 1/10 W 0603	1	R19
CRCW060331K6FKEA	RES SMD 31.6 kΩ, 1 % 1/10 W 0603	CRCW060331K6FKEA	RES SMD 31.6 kΩ, 1 % 1/10 W 0603	1	R20
36-5001-ND	Test point, black	36-5001-ND	Test point, black	4	TP1, TP4, TP6, TP8
36-5002-ND	Test point, white	36-5002-ND	Test point, white	2	TP2, TP9
36-5000-ND	Test point, red	36-5000-ND	Test point, red	3	TP3, TP5, TP7
DG2034EDQ-T1-GE3	IC SWITCH SP4T SINGLE 10MSOP	DG2034EDQ-T1-GE3	IC SWITCH SP4T SINGLE 10MSOP	1	U1
NPB02SVFN-RC	CONN jumper shorting 1.27 mm	NPB02SVFN-RC	CONN jumper shorting 1.27 mm	5	Off board x 5
UB46E	JAAW LP EVB	UB46E	JAAW HP EVB	1	PCB