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(5-2008)



Vishay Semiconductors

Silicon PIN Photodiode



LINKS TO ADDITIONAL RESOURCES





DESCRIPTION

VEMD8081 is a high speed and high sensitive PIN photodiode with enhanced sensitivity for visible light. It is a low profile surface-mount device (SMD) including the chip with a 5.4 mm² sensitive area detecting visible and near infrared radiation.

FEATURES

- Package type: surface-mount
- · Package form: top view



- Radiant sensitive area (in mm²): 5.4
- 0.48 mm low profile package
- Enhanced sensitivity for visible light
- · Suitable for visible and near infrared radiation
- Angle of half sensitivity: $\varphi = \pm 65^{\circ}$
- Floor life: 168 h, MSL 3, according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



- · High speed photo detector
- Wearables

PRODUCT SUMMARY				
COMPONENT	I_{ra} (μA) at E_e = 1.0 mW/cm², λ = 850 nm, V_R = 5.0 V	φ (°)	λ _{0.1} (nm)	
VEMD8081	33	± 65	350 to 1100	

Note

· Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMD8081	Tape and reel	MOQ: 5000 pcs, 5000 pcs/reel	Top view	

Note

• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	20	V
Operating temperature range		T _{amb}	-40 to +85	°C
Storage temperature range		T _{stg}	-40 to +85	°C
Soldering temperature	According to reflow solder profile Fig. 8	T _{sd}	260	°C
ESD safety HBM	± 2000 V, 1.5 kΩ, 100 pF, 3 pulses	ESD _{HBM}	≥2	kV

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BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 50 mA	V_{F}	-	2.3	3.3	V
Reverse dark current	$V_R = 10 \text{ V}, E = 0$	I _{ro}	-	0.5	10	nA
Diode capacitance	$V_R = 0 V, f = 1 MHz, E = 0$	C _D	-	50	-	pF
	$V_R = 3 V, f = 1 MHz, E = 0$	C _D	-	20	40	pF
Reverse light current	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 525 \text{ nm}$, $V_R = 5 \text{ V}$	I _{ra}	15	20	24	μΑ
	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 850 \text{ nm}$, $V_R = 5 \text{ V}$	I _{ra}	29	33	38	μΑ
Angle of half sensitivity		φ	-	± 65	-	0
Wavelength of peak sensitivity		λ_{p}	-	840	-	nm
Range of spectral bandwidth		λ _{0.1}	-	350 to 1100	-	nm
Rise time	V_R = 10 V, R_L = 50 Ω,λ = 830 nm	t _r	1	110	-	ns
Fall time	V_R = 10 V, R_L = 50 Ω,λ = 830 nm	t _f	-	110	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

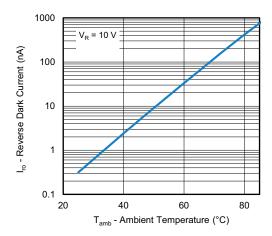


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

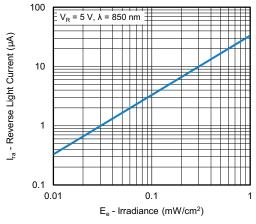


Fig. 3 - Reverse Light Current vs. Irradiance

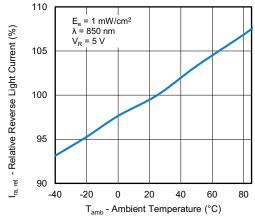


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

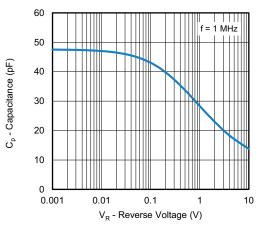


Fig. 4 - Diode Capacitance vs. Reverse Voltage



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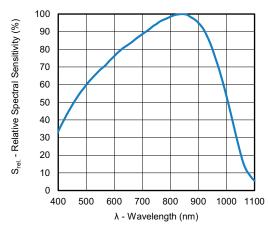


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

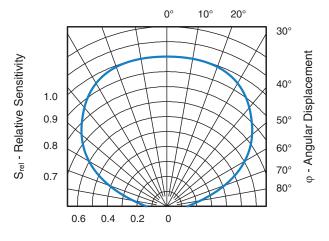
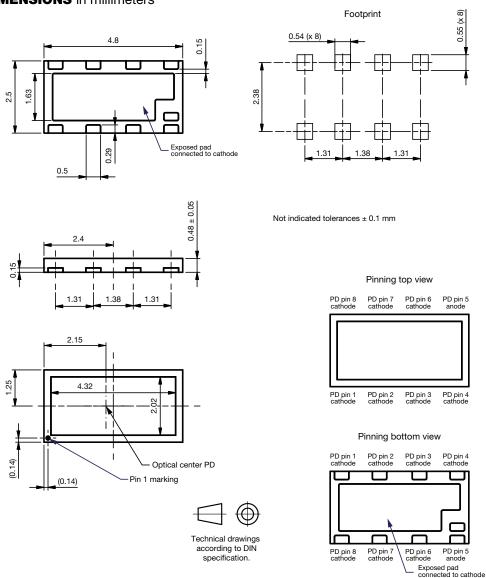


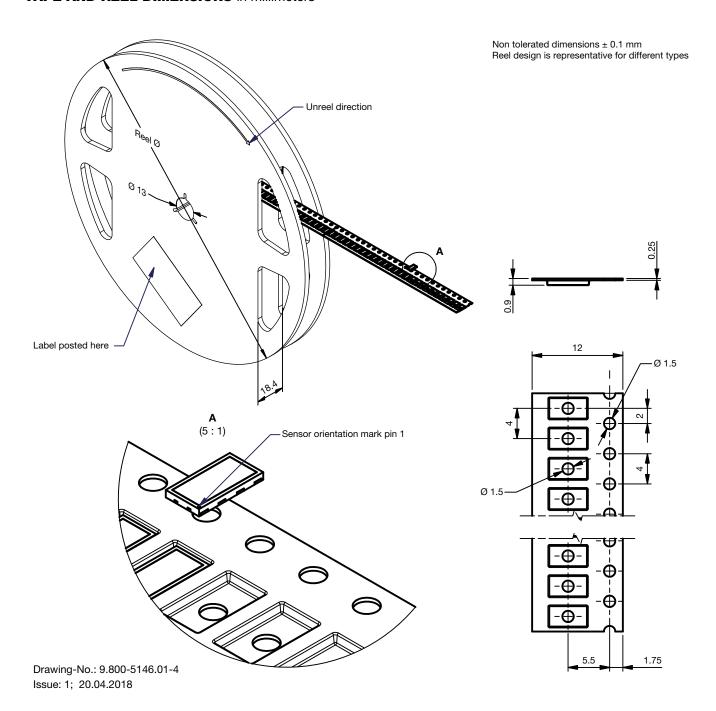
Fig. 6 - Relative Sensitivity vs. Angular Displacement

PACKAGE DIMENSIONS in millimeters



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TAPE AND REEL DIMENSIONS in millimeters





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SOLDER PROFILE

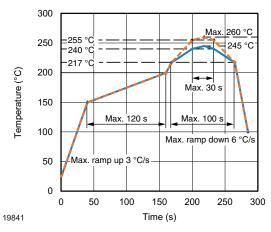


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020D

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

Floor life: 168 h

Conditions: T_{amb} < 30 °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-033D or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 %

or

96 h at 60 °C (+ 5 °C), RH < 5 %



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