VSMY385011X02



Vishay Semiconductors

High Speed Infrared Emitting Diode, 850 nm, Surface Emitter Technology



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The VSMY385011X02 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant intensity, high optical power and high speed, molded in a PLCC-2 package for surface mounting (SMD).

FEATURES

- Package type: surface-mount
- Package form: PLCC-2
- Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75
- AEC-Q102 qualified
- Peak wavelength: $\lambda_p = 850 \text{ nm}$
- Angle of half intensity: $\phi = \pm 60^{\circ}$
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization:
 for definitions of compliance please see
 www.vishay.com/doc?99912

RELEASED FOR APPLICATIONS

- Infrared radiation source for operation with CMOS cameras (illumination)
- High speed IR data transmission
- Automotive sensors
- Light curtain

PRODUCT SUMMARY

COMPONENT	I _e (mW/sr) at I _F = 100 mA	φ (°)	λ _P (nm)	t _r (ns)		
VSMY385011X02	12	± 60	850	25		

Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION

ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY385011X02-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2
VSMY385011X02-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2

Note

• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Reverse voltage		V _R	5	V			
Forward current ⁽¹⁾		I _F	100	mA			
Surge forward current	t _p = 100 μs	I _{FSM}	500	mA			
Power dissipation		Pv	180	mW			
Junction temperature		Тj	110	°C			
Operating temperature range		T _{amb}	-40 to +105	°C			
Storage temperature range		T _{stg}	-40 to +110	°C			
Soldering temperature	According to Fig. 7, J-STD-020	T _{sd}	260	°C			
Thermal resistance junction to ambient	J-STD-051, soldered on PCB	R _{thJA}	240	K/W			

Note

⁽¹⁾ Minimum forward current $I_{F min.} = 5 \text{ mA}$

1





COMPLIANT

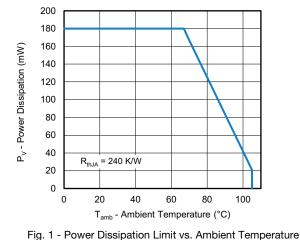
HALOGEN

FREE



VSMY385011X02

Vishay Semiconductors



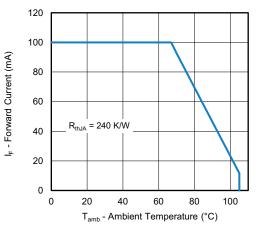


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	MIN. TYP. MAX.		UNIT	
Forward voltage	I _F = 100 mA, t _p = 20 ms	V _F	-	1.6	1.9	V	
Forward voltage	$I_F = 0.5 \text{ A}, t_p = 100 \ \mu \text{s}$	V _F	-	2.1	-	V	
Temperature coefficient of V_F	I _F = 100 mA, t _p = 20 ms	TK _{VF}	-	-1.5	-	mV/K	
Reverse current		I _R	Not designed for reverse operation		μA		
Junction capacitance	V _R = 0 V, f = 1 MHz, E = 0	Cj	-	15	-	pF	
Radiant power	I _F = 100 mA, t _p = 20 ms	φ _e	-	35	-	mW	
Temperature coefficient of ϕ_{e}	I _F = 100 mA	TK¢e	-	-0.4	-	%/K	
Angle of half intensity		φ	-	± 60	-	0	
Peak wavelength	I _F = 100 mA	λ _p	-	850	-	nm	
Spectral bandwidth	I _F = 100 mA	Δλ	-	27	-	nm	
Temperature coefficient of λ_p	I _F = 100 mA	ΤΚλρ	-	0.3	-	nm/K	
Rise time	l _F = 30 mA	t _r	-	25	-	ns	
Fall time	l _F = 30 mA	t _f	-	25	-	ns	

TYPE DEDICATED CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	BINNED GROUP	SYMBOL	MIN.	MAX.	UNIT	
Radiant intensity $I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$		А		9.4	11.1	UNIT mW/sr	
	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	В	l _e	10.9	12.6	mW/sr	
	С		12.4	14.1			

Note

• Each 1500 reel will contain a single group. The label on the bag will indicate which binned group is the bag. A specific group cannot be ordered. Production shipment containing multiple bags will likely include multiple groups. Please design accordingly.





VISHAY. www.vishay.com

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

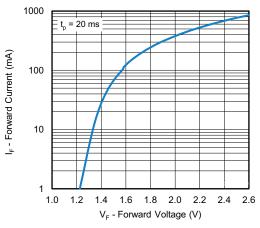


Fig. 3 - Forward Current vs. Forward Voltage

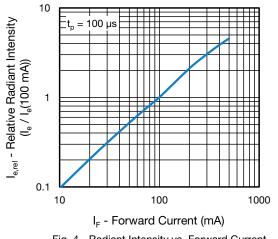


Fig. 4 - Radiant Intensity vs. Forward Current

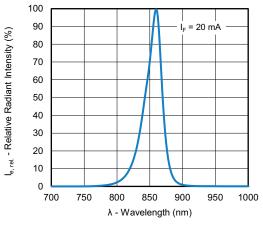


Fig. 5 - Relative Radiant Power vs. Wavelength

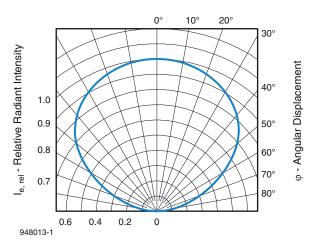


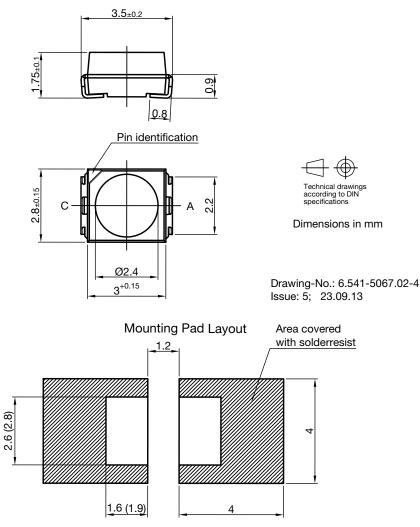
Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

3



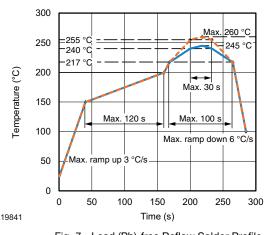
Vishay Semiconductors

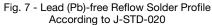
PACKAGE DIMENSIONS in millimeters



Dimensions: Reflow and vapor phase (wave soldering)

SOLDER PROFILE





DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 3, according to J-STD-033D

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



TAPE AND REEL

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

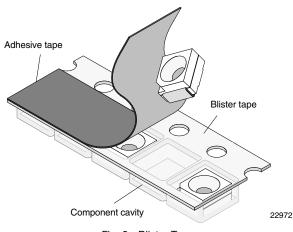


Fig. 8 - Blister Tape

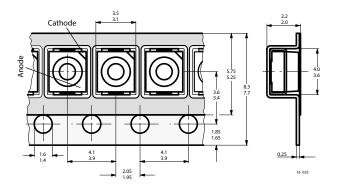
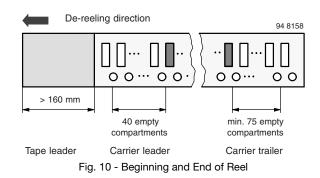


Fig. 9 - Tape Dimensions in mm for PLCC-2

MISSING DEVICES

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.



Vishay Semiconductors

The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.

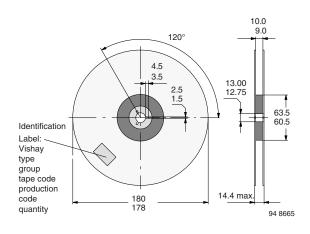


Fig. 11 - Dimensions of Reel-GS08

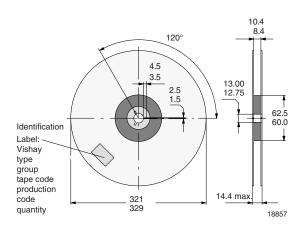


Fig. 12 - Dimensions of Reel-GS18

COVER TAPE REMOVAL FORCE

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.

Rev. 1.1, 04-Mar-2025

5

Document Number: 80308

For technical questions, contact: <u>emittertechsupport@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025

1