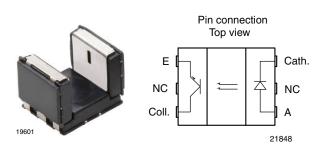
# VT141P

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**Vishay Semiconductors** 

# **Single Channel Transmissive Sensor**



### DESCRIPTION

The VT141P is a compact transmissive sensor that includes an infrared emitter and a phototransistor detector, located face-to-face in a surface mount package. VT141P is especially designed to meet high operating temperature requirements and is released for operating temperature ranges from -25 °C to +85 °C.

### FEATURES

- Package type: surface-mount
- Detector type: phototransistor
- Dimensions (L x W x H in mm): 5.5 x 4 x 4
- Gap (in mm): 3
- Aperture (in mm): 0.3
- Typical output current under test:  $I_C = 1.5 \text{ mA}$
- Emitter wavelength: 950 nmMoisture sensitivity level (MSL): 1

• Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- · Accurate position sensor for encoder
- Detection of motion speed

PRODUCT SUMMARY					
PART NUMBER	GAP WIDTH (mm)	APERTURE WIDTH (mm)	TYPICAL OUTPUT CURRENT UNDER TEST <sup>(1)</sup> (mA)	DAYLIGHT BLOCKING FILTER INTEGRATED	
VT141P	3	0.3	1.5	No	

#### Note

· Conditions like in table basic characteristics / coupler

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	VOLUME <sup>(1)</sup> REMARK		
VT141P	Tape and reel	MOQ: 2000 pcs, 2000 pcs/reel	Drypack, MSL 1	

#### Note

• MOQ: minimum order quantity





(5-2008)

VT141P



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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
COUPLER						
Total power dissipation	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	37.5	mW		
Junction temperature		Tj	105	°C		
Ambient temperature range		T <sub>amb</sub>	-25 to +85	°C		
Storage temperature range		T <sub>stg</sub>	-25 to +105	°C		
Soldering temperature	In accordance with Fig. 16	T <sub>sd</sub>	260	°C		
INPUT (EMITTER)						
Reverse voltage		V <sub>R</sub>	5	V		
Forward current	T <sub>amb</sub> ≤ 85 °C	I <sub>F</sub>	25	mA		
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	200	mA		
Power dissipation	T <sub>amb</sub> ≤ 85 °C	P <sub>V</sub>	37.5	mW		
OUTPUT (DETECTOR)						
Collector emitter voltage		V <sub>CEO</sub>	5	V		
Emitter collector voltage		V <sub>ECO</sub>	7	V		
Collector current		Ι <sub>C</sub>	20	mA		
Collector dark current	$T_{amb} = 85 \ ^{\circ}C, V_{CE} = 5 \ V$	I <sub>CEO</sub>	3.3	μΑ		

#### **ABSOLUTE MAXIMUM RATINGS**

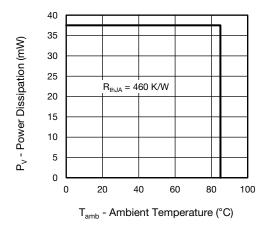


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

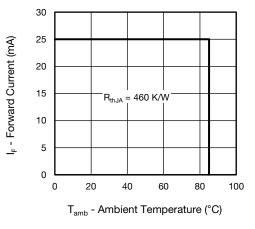


Fig. 2 - Forward Current Limit vs. Ambient Temperature

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**VT141P** 

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
COUPLER							
Collector current	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 15 mA	Ι <sub>C</sub>	0.7	1.5	-	mA	
Collector emitter saturation voltage	I <sub>F</sub> = 15 mA, I <sub>C</sub> = 0.2 mA	V <sub>CEsat</sub>	-	-	0.4	V	
INPUT (EMITTER)							
Forward voltage	I <sub>F</sub> = 15 mA	V <sub>F</sub>	1	1.2	1.4	V	
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μA	
Junction capacitance	$V_{R} = 0 V, f = 1 MHz$	Cj	-	25	-	pF	
OUTPUT (DETECTOR)							
Collector emitter voltage $I_C$	$I_{\rm C} = 1  \rm{mA}$	V <sub>CEO</sub>	20	-	-	V	
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V	
Collector dark current	$V_{CE} = 25 \text{ V}, \text{ I}_{F} = 0 \text{ A}, \text{ E} = 0 \text{ Ix}$	I <sub>CEO</sub>	-	1	100	nA	
SWITCHING CHARACTERISTICS							
Rise time	$I_C$ = 0.7 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$ (see Fig. 3)	t <sub>r</sub>	-	14	150	μs	
Fall time	$I_C$ = 0.7 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$ (see Fig. 3)	t <sub>f</sub>	-	21	150	μs	

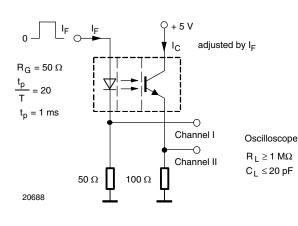


Fig. 3 - Test Circuit for t<sub>r</sub> and t<sub>f</sub>

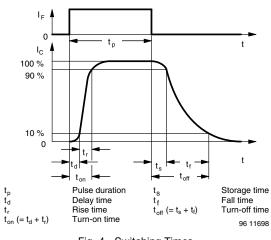
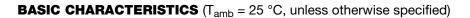


Fig. 4 - Switching Times



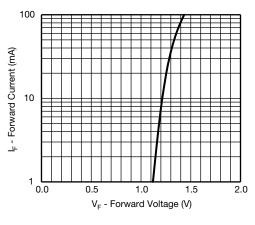
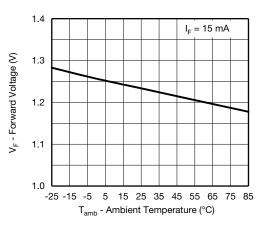
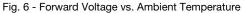


Fig. 5 - Forward Current vs. Forward Voltage





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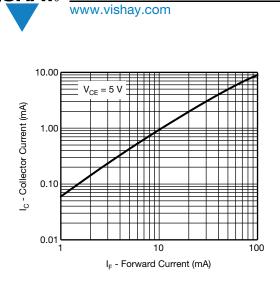


Fig. 7 - Collector Current vs. Forward Current

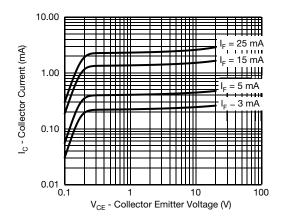


Fig. 8 - Collector Current vs. Collector Emitter Voltage

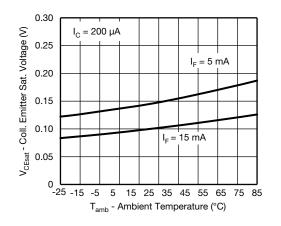


Fig. 9 - Collector Emitter Saturation Voltage vs. Ambient Temperature

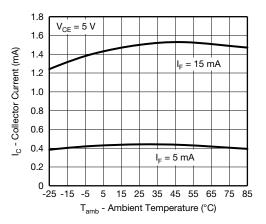


Fig. 10 - Collector Current vs. Ambient Temperature

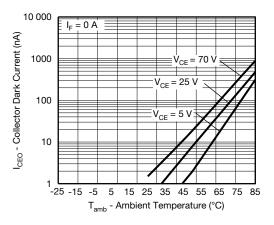


Fig. 11 - Collector Dark Current vs. Ambient Temperature

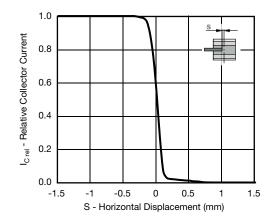


Fig. 12 - Relative Collector Current vs. Horizontal Displacement

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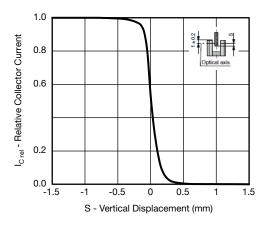


Fig. 13 - Relative Collector Current vs. Vertical Displacement

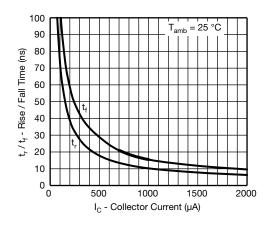


Fig. 14 - Rise / Fall Time vs. Collector Current

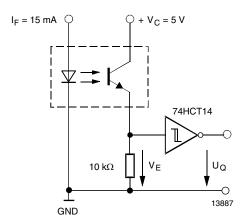


Fig. 15 - Application example

### **REFLOW SOLDER PROFILE**

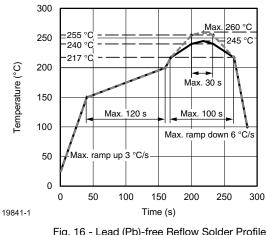


Fig. 16 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

#### **FLOOR LIFE**

No time limit.

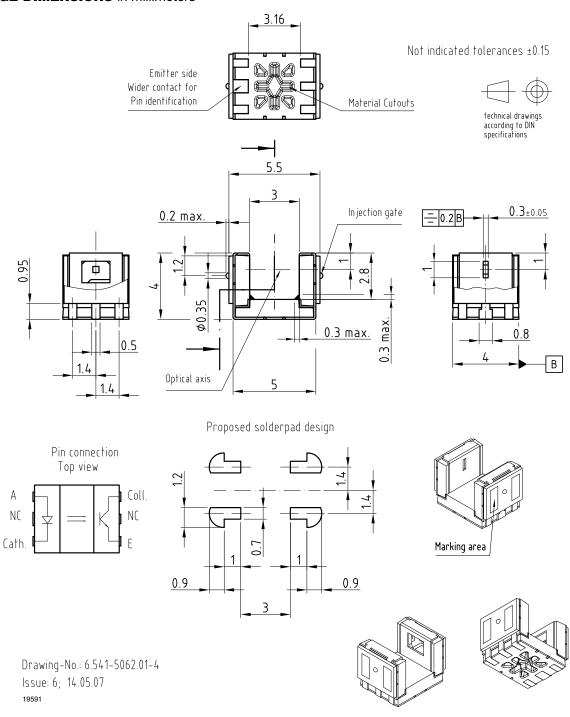
Moisture sensitivity level (MSL) 1, according to JEDEC<sup>®</sup>, J-STD-020.



### PACKAGE DIMENSIONS in millimeters

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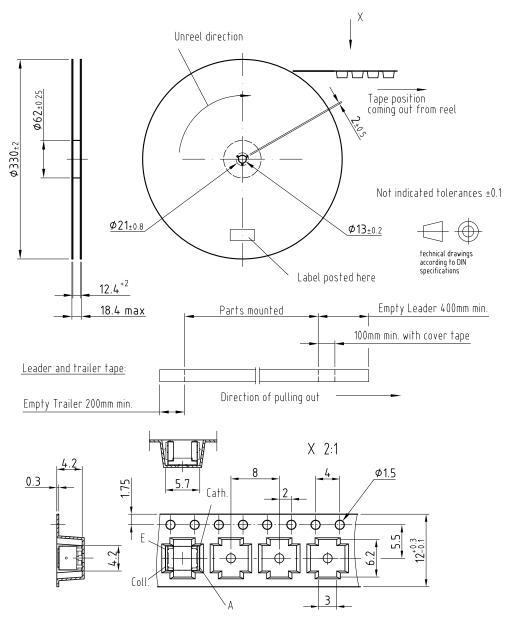


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#### **PACKAGE DIMENSIONS** in millimeters

Volume/reel = 2000 pcs



Drawing-No.: 9.800-5092.02-4 Issue: 1; 14.05.07 20601



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