RoHS

HALOGEN FREE

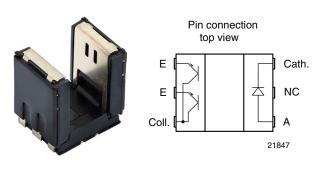
**GREEN** 

(5-2008)



## Vishay Semiconductors

## **Dual Channel Transmissive Sensor**



### **DESCRIPTION**

The VT172U is a compact transmissive sensor that includes an infrared emitter and two phototransistor detectors, located face-to-face in a surface-mount package. VT172U is released for operating temperature ranges from -25 °C to +85 °C.

#### **FEATURES**

- Package type: surface-mount
- · Detector type: phototransistor
- Dimensions (L x W x H in mm): 5.5 x 4 x 5.7
- Gap (in mm): 3
- Aperture (in mm): 0.3
- Channel distance (center to center): 0.8 mm
- Typical output current under test: I<sub>C</sub> = 1.5 mA
- Emitter wavelength: 950 nm
- Lead (Pb)-free soldering released
- · Moisture sensitivity level (MSL): 1
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>



- · Accurate position sensor for encoder
- Sensor for motion, speed and direction

PRODUCT SUMMARY					
PART NUMBER GAP WIDTH (mm)		APERTURE WIDTH (mm)	TYPICAL OUTPUT CURRENT UNDER TEST (1) (mA)	DAYLIGHT BLOCKING FILTER INTEGRATED	
VT172U	3	0.3	1.5	No	

## Note

· Conditions like in table basic characteristics / coupler

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	VOLUME (1)	REMARKS		
VT172U	Tape and reel	MOQ: 1300 pcs, 1300 pcs/reel	-		

#### Note

• MOQ: minimum order quantity



ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
COUPLER					
Total power dissipation	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	37.5	mW	
Junction temperature		T <sub>j</sub>	105	°C	
Ambient temperature range		T <sub>amb</sub>	-25 to +85	°C	
Storage temperature range		T <sub>stg</sub>	-25 to +105	°C	
Soldering temperature	In accordance with Fig. 16	T <sub>sd</sub>	260	°C	
INPUT (EMITTER)					
Reverse voltage		V <sub>R</sub>	5	V	
Forward current	T <sub>amb</sub> ≤ 85 °C	l <sub>F</sub>	25	mA	
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	200	mA	
Power dissipation	T <sub>amb</sub> ≤ 85 °C	P <sub>V</sub>	37.5	mW	
OUTPUT (DETECTOR)					
Collector emitter voltage		V <sub>CEO</sub>	20	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		I <sub>C</sub>	20	mA	
Collector dark current	T <sub>amb</sub> = 85 °C, V <sub>CE</sub> = 5 V	I <sub>CEO</sub>	3.3	μA	

## **ABSOLUTE MAXIMUM RATINGS**

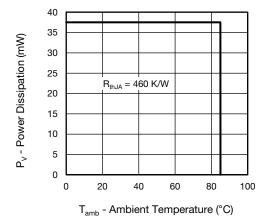


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

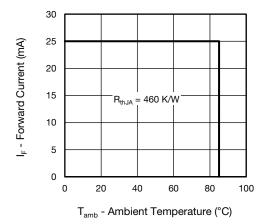


Fig. 2 - Forward Current Limit vs. Ambient Temperature



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
COUPLER						
Collector current per channel	$V_{CE} = 5 \text{ V}, I_F = 15 \text{ mA}$	Ic	0.7	1.5	-	mA
Collector emitter saturation voltage	I <sub>F</sub> = 15 mA, I <sub>C</sub> = 0.2 mA	V <sub>CEsat</sub>	-	-	0.4	V
INPUT (EMITTER)						
Forward voltage	I <sub>F</sub> = 15 mA	V <sub>F</sub>	1	1.2	1.4	V
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μA
Junction capacitance	$V_R = 0 V, f = 1 MHz$	C <sub>j</sub>	-	25	-	pF
OUTPUT (DETECTOR)						
Collector emitter voltage I <sub>C</sub>	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	20	-	-	V
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V
Collector dark current	$V_{CE} = 25 \text{ V}, I_F = 0 \text{ A}, E = 0 \text{ lx}$	I <sub>CEO</sub>	-	1	100	nA
SWITCHING CHARACTERISTIC	cs					
Rise time	$I_C$ = 0.7 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$ (see fig. 3)	t <sub>r</sub>	-	14	150	μs
Fall time	$I_{C} = 0.7 \text{ mA}, V_{CE} = 5 \text{ V},$ $R_{L} = 100 \Omega \text{ (see fig. 3)}$	t <sub>f</sub>	-	21	150	μs

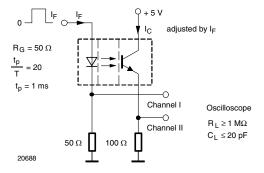


Fig. 3 - Test Circuit for  $t_{\text{r}}$  and  $t_{\text{f}}$ 

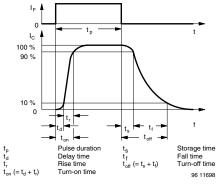


Fig. 4 - Switching Times

## **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

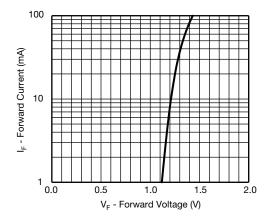


Fig. 5 - Forward Current vs. Forward Voltage

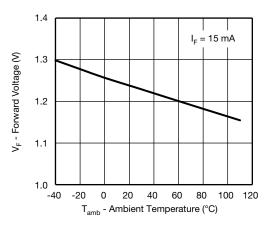


Fig. 6 - Forward Voltage vs. Ambient Temperature





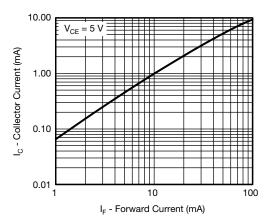


Fig. 7 - Collector Current vs. Forward Current

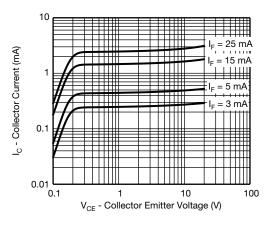


Fig. 8 - Collector Current vs. Collector Emitter Voltage

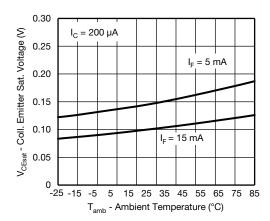


Fig. 9 - Collector Emitter Saturation Voltage vs. **Ambient Temperature** 

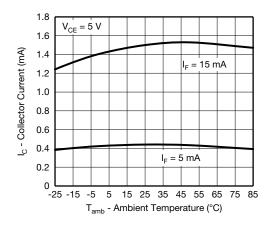


Fig. 10 - Collector Current vs. Ambient Temperature

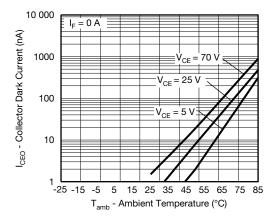


Fig. 11 - Collector Dark Current vs. Ambient Temperature

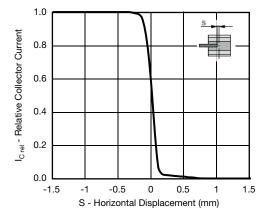


Fig. 12 - Relative Collector Current vs. Horizontal Displacement

# 1.0 1.0 1.0 0.8 0.6 0.4 0.2 0.0 -1.5 -1 -0.5 0.0 0.5 1.5 S - Vertical Displacement (mm)

Fig. 13 - Relative Collector Current vs. Vertical Displacement

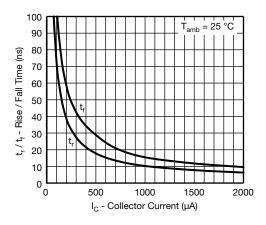


Fig. 14 - Rise / Fall Time vs. Collector Current

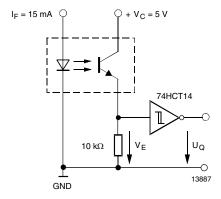


Fig. 15 - Application example

## **REFLOW SOLDER PROFILE**

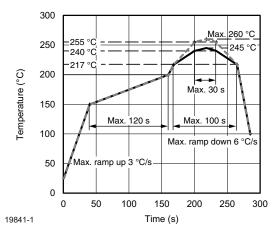
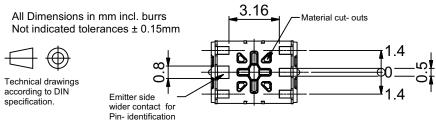


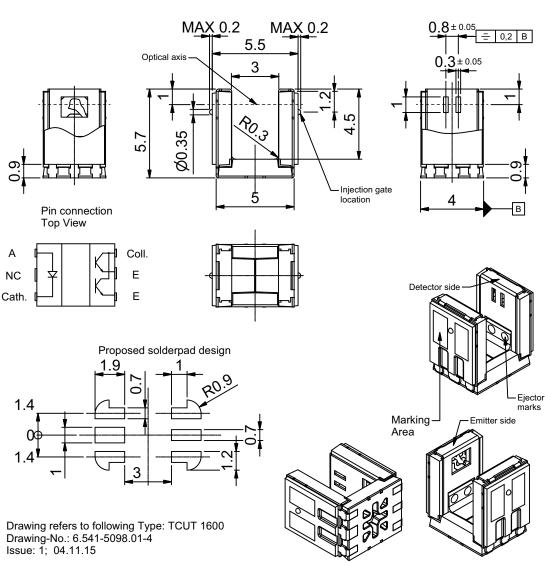
Fig. 16 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

### **FLOOR LIFE**

Level 1, according to JEDEC®, J-STD-020. No time limit.

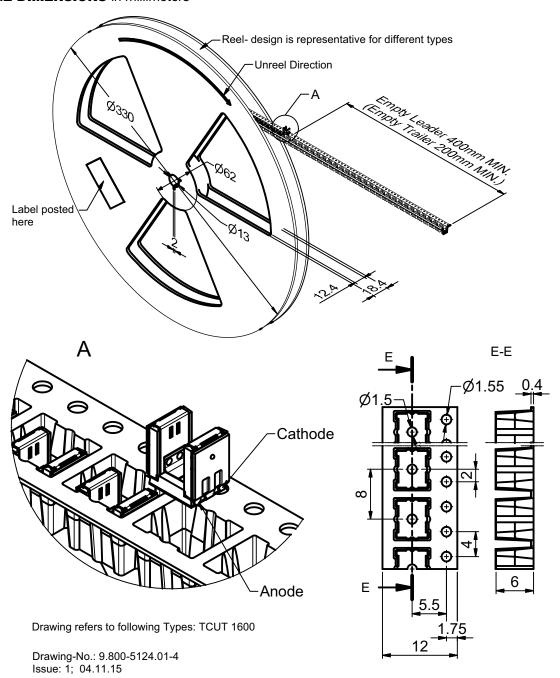
## **PACKAGE DIMENSIONS** in millimeters







## **PACKAGE DIMENSIONS** in millimeters





# **Legal Disclaimer Notice**

Vishay

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