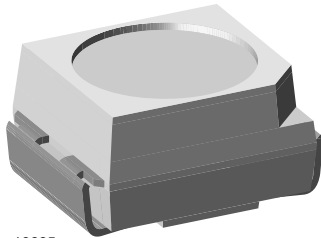


## High Intensity SMD LED PLCC-2



19225

### DESCRIPTION

The package of the VLMS31.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

### PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- Product series: standard
- Angle of half intensity:  $\pm 60^\circ$

### FEATURES

- SMD LED with exceptional brightness
- Luminous intensity categorized
- Compatible with automatic placement equipment
- EIA and ICE standard package
- Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape
- Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit  $I_{Vmax}/I_{Vmin.} \leq 1.6$
- Preconditioning according to JEDEC® level 2a
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### APPLICATIONS

- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- Indicator and backlight in office equipment
- Flat backlight for LCDs, switches, and symbols
- General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)			at I <sub>F</sub> (mA)	WAVELENGTH (nm)			at I <sub>F</sub> (mA)	FORWARD VOLTAGE (V)			at I <sub>F</sub> (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMS31J1K2-GS08	Red	4.5	9	11.2	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31J1L2-GS08	Red	4.5	9	18	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31J2L1-GS08	Red	5.6	9	14	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP
VLMS31K1L2-GS08	Red	7.1	9.4	18	10	624	630	638	10	-	1.9	2.6	20	GaAsP on GaP



ABSOLUTE MAXIMUM RATINGS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)				
VLMS31..				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage <sup>1)</sup>		$V_R$	6	V
DC forward current	$T_{amb} \leq 74\text{ }^{\circ}\text{C}$	$I_F$	20	mA
Surge forward current	$t_p \leq 10\text{ }\mu\text{s}$	$I_{FSM}$	0.34	A
Power dissipation		$P_V$	80	mW
Junction temperature		$T_J$	100	$^{\circ}\text{C}$
Operating temperature range		$T_{amb}$	-40 to +100	$^{\circ}\text{C}$
Storage temperature range		$T_{stg}$	-40 to +100	$^{\circ}\text{C}$
Soldering temperature	$t \leq 5\text{ s}$	$T_{sd}$	260	$^{\circ}\text{C}$
Thermal resistance junction to ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	$R_{thJA}$	400	K/W

**Note**

(1) Driving LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)							
VLMS31.., RED							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity <sup>1)</sup>	$I_F = 10\text{ mA}$	VLMS31J1K2	$I_V$	4.5	9	11.2	mcd
		VLMS31J1L2	$I_V$	4.5	9	18	mcd
		VLMS31J2L1	$I_V$	5.6	9	14	mcd
		VLMS31K1L2	$I_V$	7.1	9.4	18	mcd
Dominant wavelength	$I_F = 10\text{ mA}$		$\lambda_d$	624	630	638	nm
Peak wavelength	$I_F = 10\text{ mA}$		$\lambda_p$	-	643	-	nm
Angle of half intensity	$I_F = 10\text{ mA}$		$\phi$	-	$\pm 60$	-	$^{\circ}$
Forward voltage	$I_F = 20\text{ mA}$		$V_F$	-	1.9	2.6	V
Reverse voltage	$I_R = 10\text{ }\mu\text{A}$		$V_R$	5	-	-	V
Junction capacitance	$V_R = 0, f = 1\text{ MHz}$		$C_j$	-	15	-	pF

**Note**

(1) In one packing unit  $I_{Vmax}/I_{Vmin.} \leq 1.6$

LUMINOUS INTENSITY CLASSIFICATION				
GROUP	LIGHT INTENSITY (mcd)			
	STANDARD	OPTIONAL	MIN.	MAX.
J	1	4.5	5.6	
	2	5.6	7.1	
K	1	7.1	9.0	
	2	9.0	11.2	
L	1	11.2	14	
	2	14	18	

**Note**

- Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of  $\pm 11\%$ .  
The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).  
In order to ensure availability, single brightness groups will not be orderable.  
In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.  
In order to ensure availability, single wavelength groups will not be orderable

CROSSING TABLE	
VISHAY	OSRAM
VLMS31J1K2	LST670-J1K2
VLMS31J1L2	LST670-J1L2
VLMS31J2L1	LST670-J2L1
VLMS31K1L2	LST670-K1L2

**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

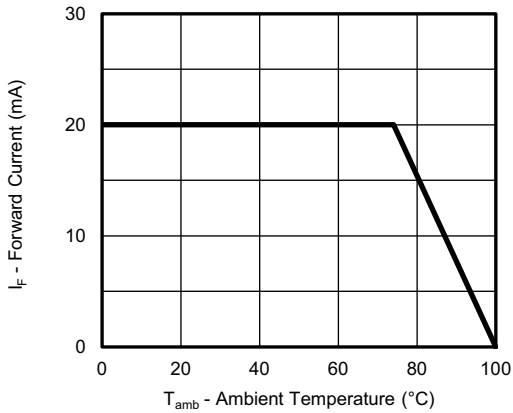


Fig. 1 - Forward Current vs. Ambient Temperature

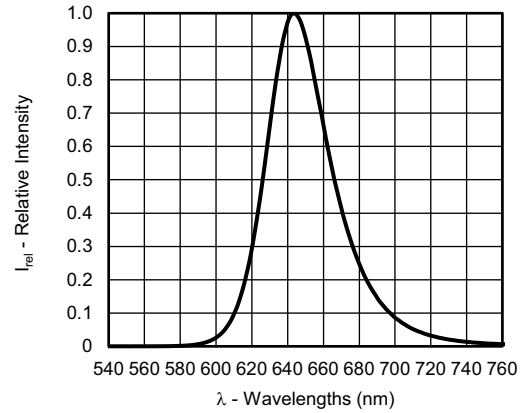


Fig. 4 - Relative Intensity vs. Wavelength

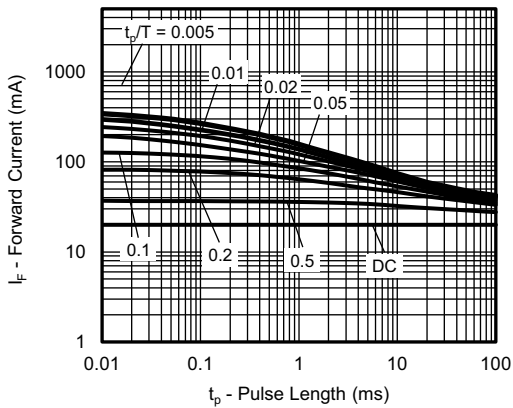


Fig. 2 - Forward Current vs. Pulse Length

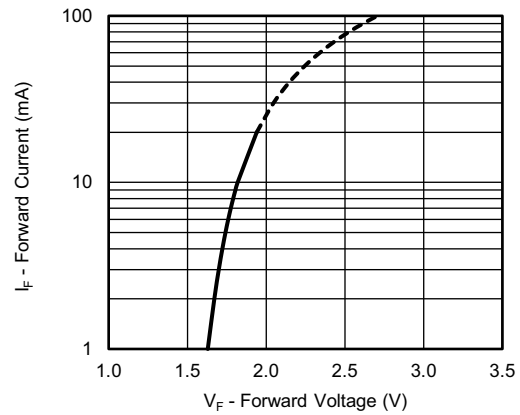


Fig. 5 - Forward Current vs. Forward Voltage

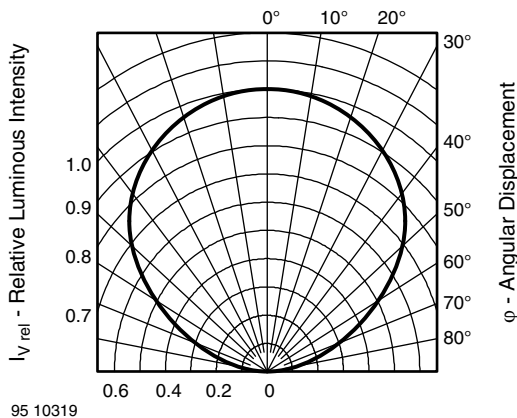


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

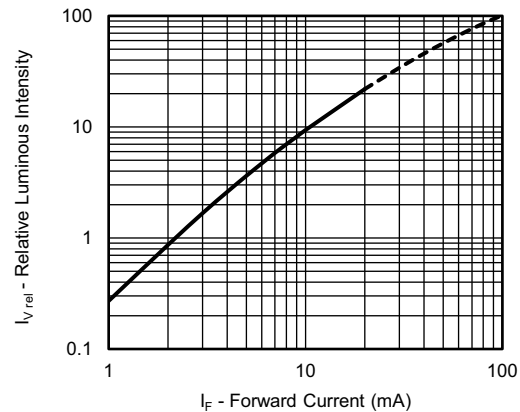


Fig. 6 - Relative Luminous Intensity vs. Forward Current

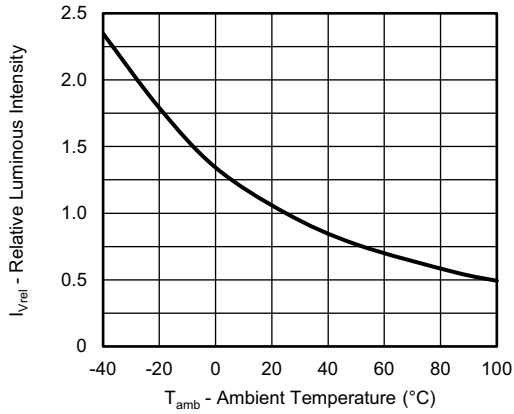


Fig. 7 - Relative Luminous Intensity vs. Ambient Temperature

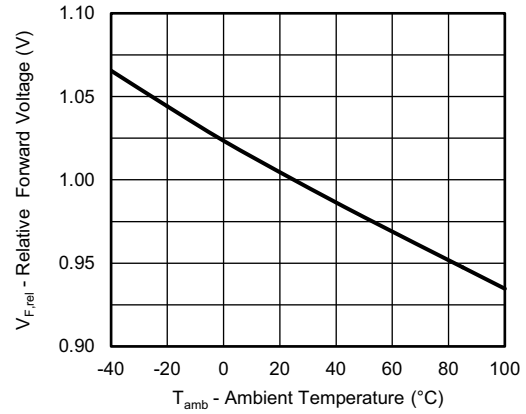
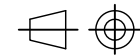
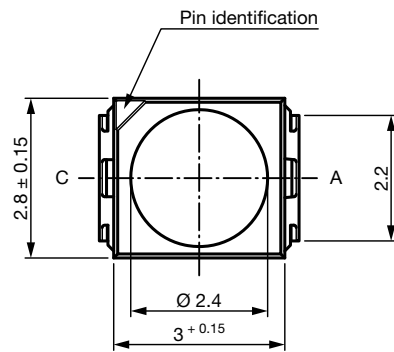
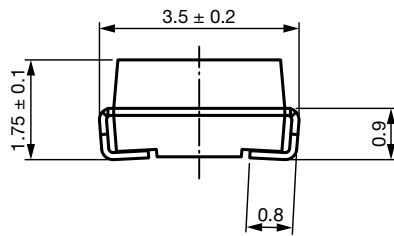


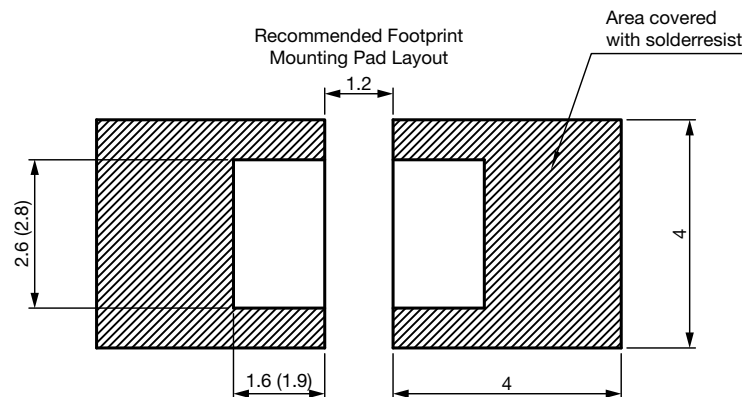
Fig. 8 - Relative Forward Voltage vs. Ambient Temperature

**PACKAGE DIMENSIONS** in millimeters



technical drawings  
according to DIN  
specifications

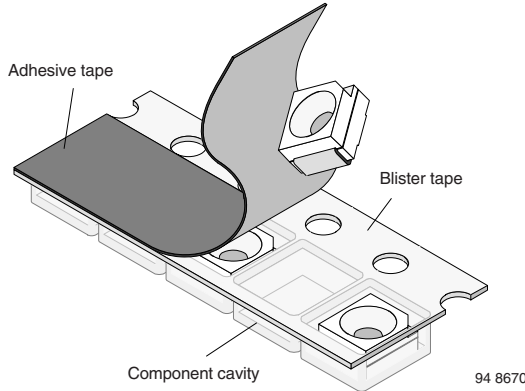
Drawing-No.: 6.541-5067.01-4  
Issue: 7; 12.03.14



**METHOD OF TAPING / POLARITY AND TAPE AND REEL**

**SMD LED (VLM3-SERIES)**

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



**TAPING OF VLM.3..**

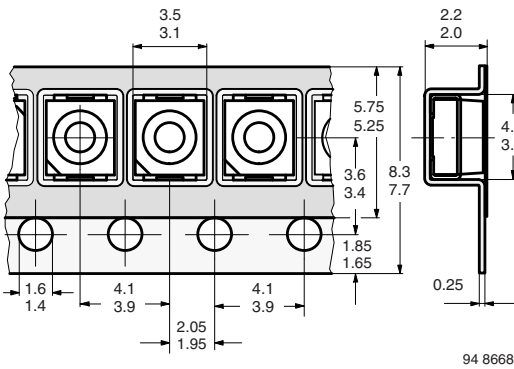


Fig. 9 - Tape Dimensions in mm for PLCC-2

**REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDs, TAPE OPTION GS08 (= 1500 PCS.)**

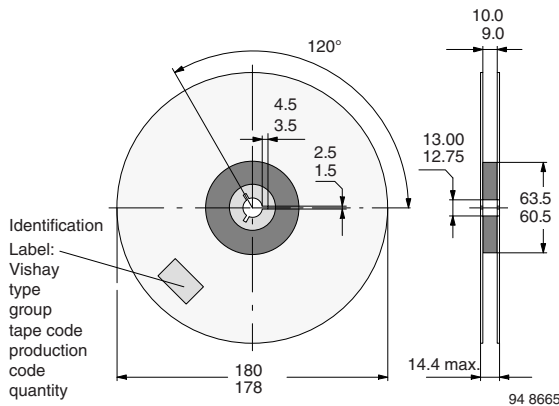


Fig. 10 - Reel Dimensions - GS08

**SOLDERING PROFILE**

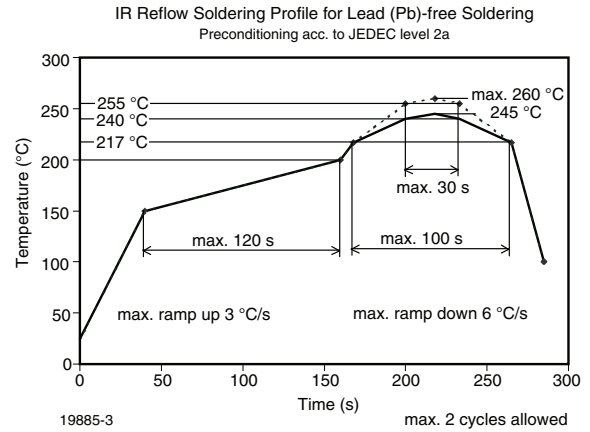


Fig. 11 - Vishay Lead (Pb)-free Reflow Soldering Profile (according to J-STD-020)

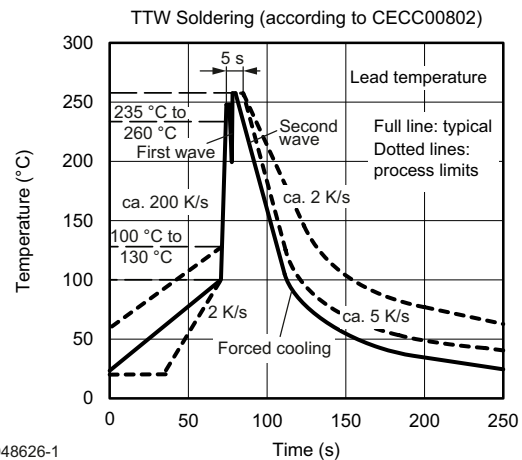
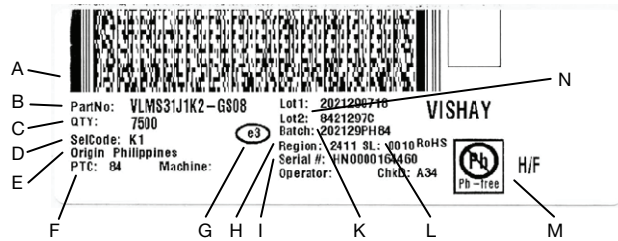


Fig. 12 - Double Wave Soldering of Opto Devices (all Packages)

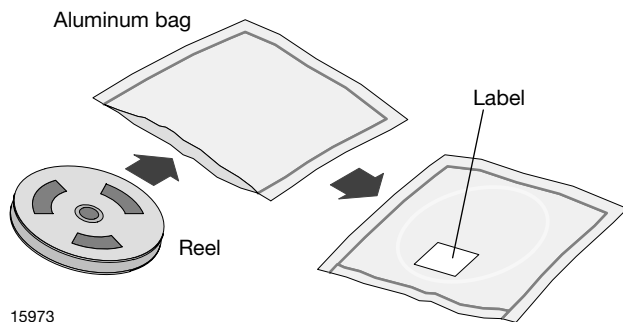
**BAR CODE PRODUCT LEVEL**



- A. 2D barcode
- B. Part No: Vishay part number
- C. QTY: quantity
- D. SelCode: selection bin code
- E. Country of origin
- F. PTC: production plant code
- G. Termination finish
- H. Region code
- I. Serial#: serial number
- K. Batch Number: year, week, country code, plant code
- L. SL: storage location
- M. Environmental Symbols: RoHS, lead (Pb)-free, halogen free
- N. Lot numbers

**DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



**FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

**RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminium bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

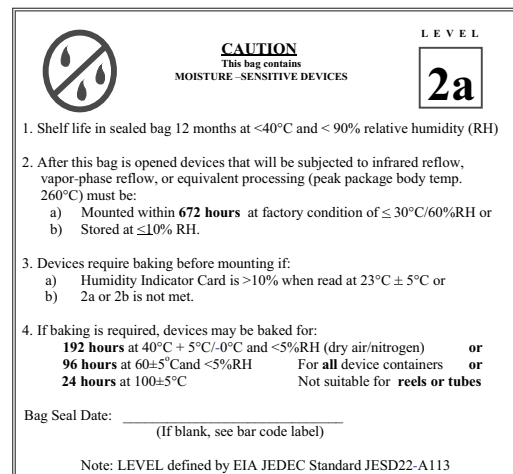
After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

**ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

**VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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