



## IR Receiver Modules for Remote Control Systems



### DESCRIPTION

This IR receiver series is optimized for long burst remote control systems in different environments. The customer can choose between different IC settings (AGC variants), to find the optimum solution for his application. The higher the AGC, the better noise is suppressed, but the lower the code compatibility.

The devices contain a PIN diode and a preamplifier assembled on a lead frame. The epoxy package contains an IR filter. The demodulated output signal can be directly connected to a microprocessor for decoding. These components have not been qualified to automotive specifications.

### FEATURES

- Individual IC settings to reach maximum performance
- Immunity against noise (lamps, LCD TV, Wi-Fi)
- Low supply current
- Photo detector and preamplifier in one package
- Supply voltage: 2.0 V to 5.5 V
- Material categorization:  
for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
(5-2008)

### LINKS TO ADDITIONAL RESOURCES

- Product Page
- Marking
- Holders
- Bends and Cuts

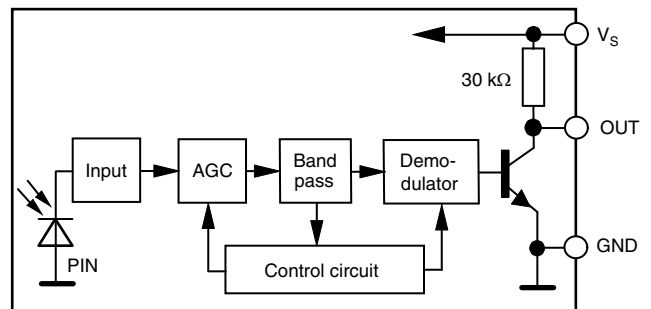
### APPLICATIONS

- Infrared remote control systems

### DESIGN SUPPORT TOOLS

- [3D models](#)
- [Window size calculator](#)

### BLOCK DIAGRAM



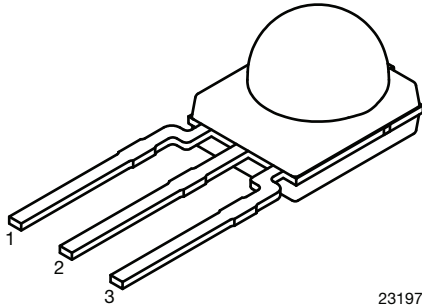
16833-22



**MECHANICAL DATA**

**Pinning for TSOP53...DF1P:**

1 = OUT, 2 = GND, 3 = V<sub>S</sub>

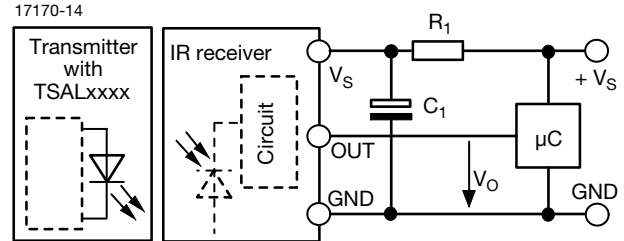


23197

**ORDERING CODE**

TSOP53...DF1P - 1100 pieces in tape and reel

**APPLICATION CIRCUIT**



R<sub>1</sub> and C<sub>1</sub> recommended in case there are strong ripple or spikes on the supply line.

PARTS TABLE			
AGC			
		LEGACY, FOR LONG BURST REMOTE CONTROLS (AGC2)	
		RECOMMENDED FOR LONG BURST CODES (AGC4)	
Carrier frequency	30 kHz	TSOP53230DF1P	TSOP53430DF1P
	33 kHz	TSOP53233DF1P	TSOP53433DF1P
	36 kHz	TSOP53236DF1P	TSOP53436DF1P (1)(2)(3)
	38 kHz	TSOP53238DF1P	TSOP53438DF1P (4)(5)(6)(9)(10)
	40 kHz	TSOP53240DF1P (8)	TSOP53440DF1P
	56 kHz	TSOP53256DF1P (11)	TSOP53456DF1P (6)(7)
Package		Minimold	
Pinning		1 = OUT, 2 = GND, 3 = V <sub>S</sub>	
Dimensions (mm)		5.4 W x 6.35 H x 4.9 D	
Mounting		Leaded	
Application		Remote control	
Best choice for		(1) RC-5 (2) RC-6 (3) Panasonic (4) NEC (5) Sharp (6) r-step (7) Thomson RCA (8) Sony (9) Mitsubishi (10) Sejin 4PPM (11) Cisco	
Special options		<ul style="list-style-type: none"> <li>Narrow optical filter: <a href="http://www.vishay.com/doc?81590">www.vishay.com/doc?81590</a></li> <li>Wide optical filter: <a href="http://www.vishay.com/doc?82726">www.vishay.com/doc?82726</a></li> </ul>	

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V <sub>S</sub>	-0.3 to +6	V
Supply current		I <sub>S</sub>	5	mA
Output voltage		V <sub>O</sub>	-0.3 to 5.5	V
Voltage at output to supply		V <sub>S</sub> - V <sub>O</sub>	-0.3 to (V <sub>S</sub> + 0.3)	V
Output current		I <sub>O</sub>	5	mA
Junction temperature		T <sub>j</sub>	100	°C
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C
Operating temperature range		T <sub>amb</sub>	-25 to +85	°C
Power consumption	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	10	mW
Soldering temperature	t ≤ 10 s, 1 mm from case	T <sub>sd</sub>	260	°C

**Note**

- Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

<b>ELECTRICAL AND OPTICAL CHARACTERISTICS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply current	$E_v = 0, V_s = 3.3\text{ V}$	$I_{SD}$	0.25	0.35	0.45	mA
	$E_v = 40\text{ klx, sunlight}$	$I_{SH}$	-	0.45	-	mA
Supply voltage		$V_s$	2.0	-	5.5	V
Transmission distance	$E_v = 0$ , test signal see Fig. 1, IR diode TSAL6200, $I_F = 50\text{ mA}$	$d$	-	24	-	m
Output voltage low	$I_{OSL} = 0.5\text{ mA}$ , $E_e = 0.7\text{ mW/m}^2$ , test signal see Fig. 1	$V_{OSL}$	-	-	100	mV
Minimum irradiance	Test signal: RC5 code	$E_e\text{ min.}$	-	0.12	0.25	$\text{mW/m}^2$
	Test signal: NEC code	$E_e\text{ min.}$	-	0.16	0.35	$\text{mW/m}^2$
Maximum irradiance	$t_{pi} - 4/f_0 < t_{po} < t_{pi} + 4/f_0$ , test signal see Fig. 1	$E_e\text{ max.}$	30	-	-	$\text{W/m}^2$
Directivity	Angle of half transmission distance	$\phi_{1/2}$	-	$\pm 45$	-	deg

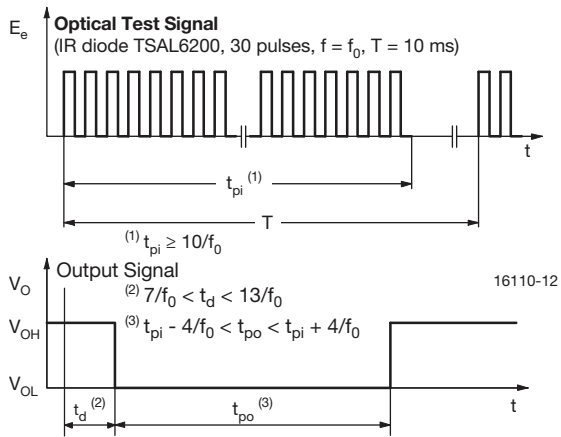
**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)


Fig. 1 - Output Active Low

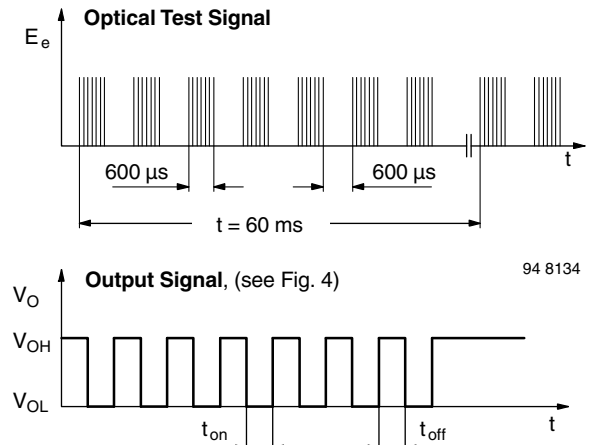


Fig. 3 - Output Function

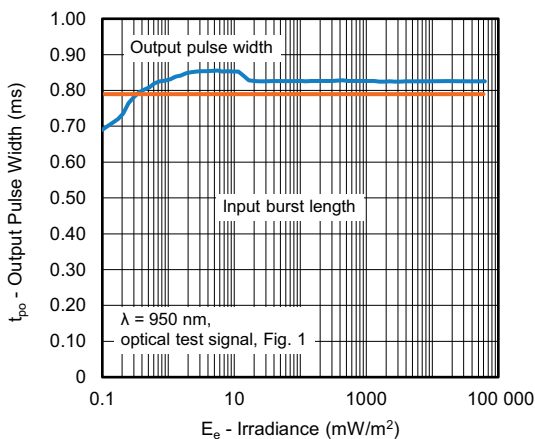


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

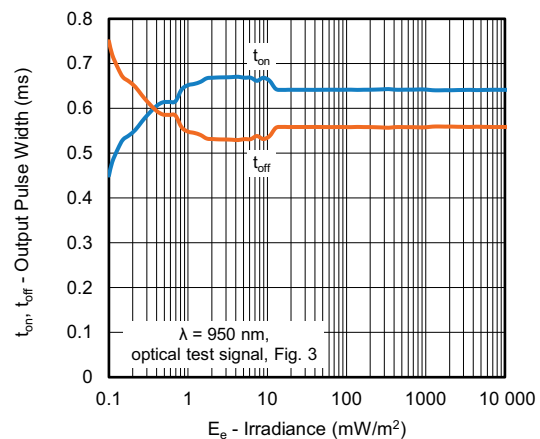


Fig. 4 - Output Pulse Diagram

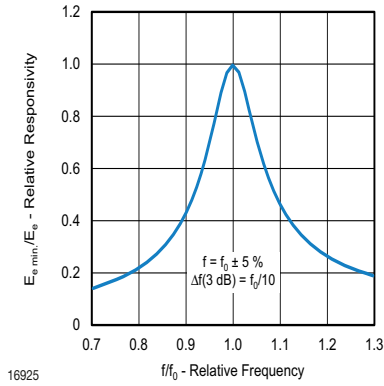


Fig. 5 - Frequency Dependence of Responsivity

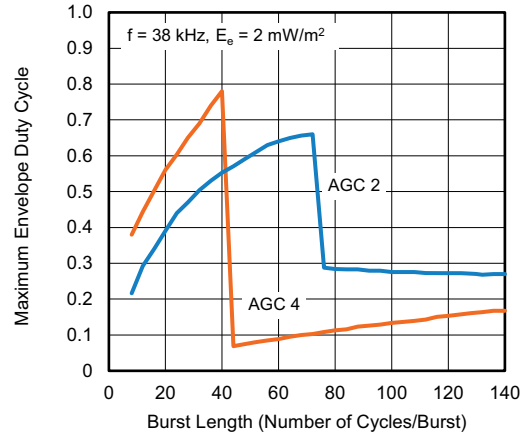


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

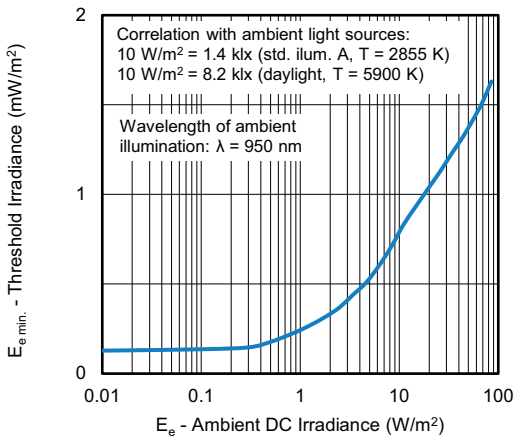


Fig. 6 - Sensitivity in Bright Ambient

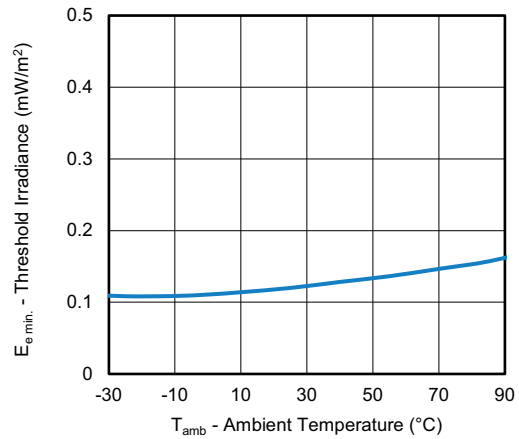


Fig. 9 - Sensitivity vs. Ambient Temperature

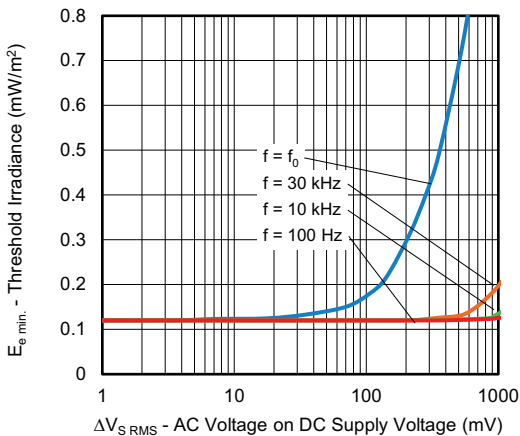


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

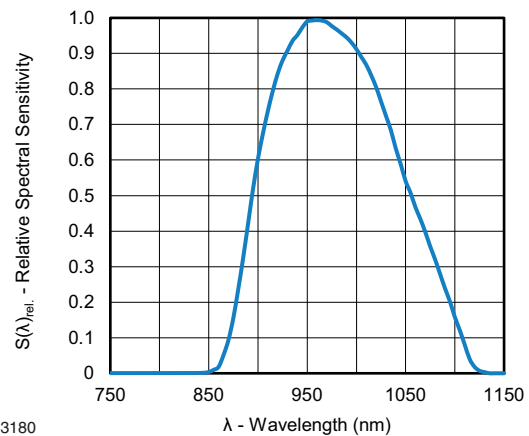


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

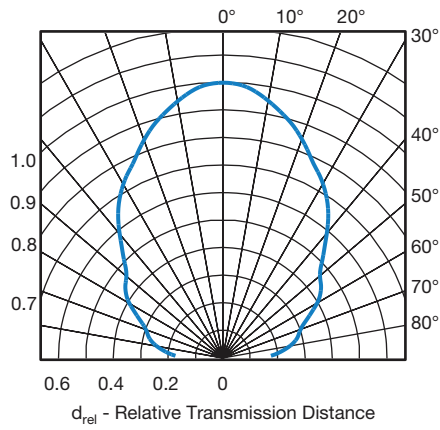


Fig. 11 - Horizontal Directivity

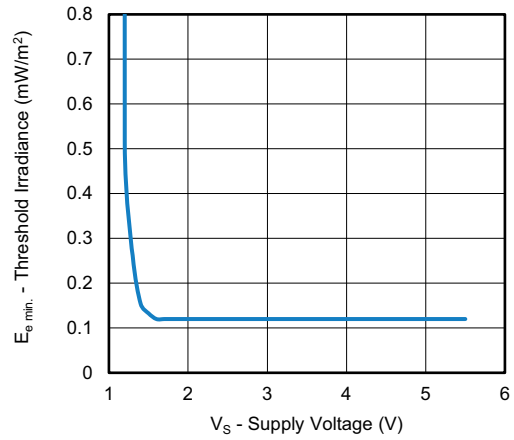


Fig. 12 - Sensitivity vs. Supply Voltage

**SUITABLE DATA FORMAT**

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14).
- 2.4 GHz and 5 GHz Wi-Fi

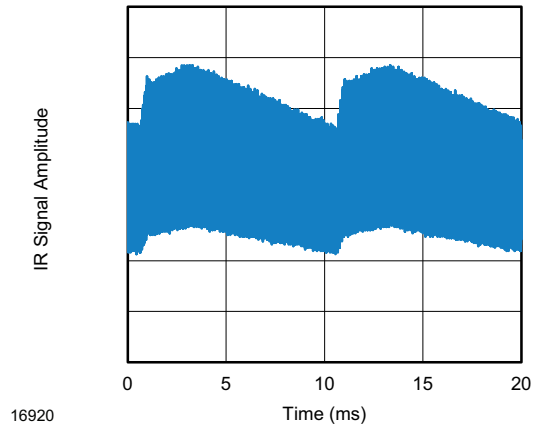


Fig. 13 - IR Disturbance from Fluorescent Lamp With Low Modulation

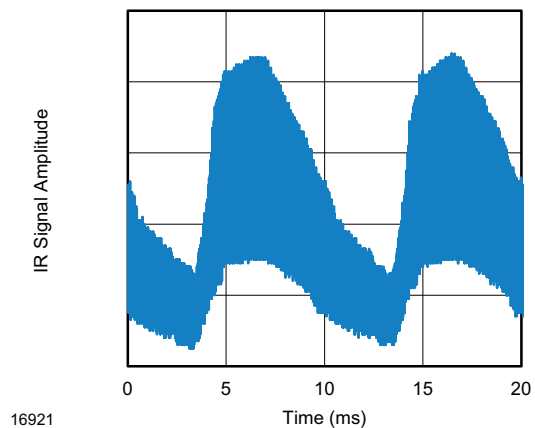


Fig. 14 - IR Disturbance from Fluorescent Lamp With High Modulation

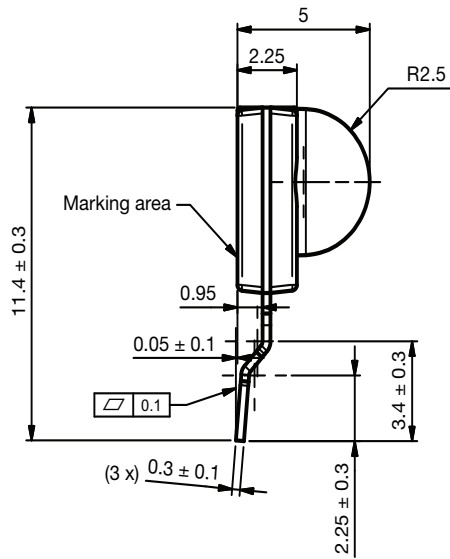
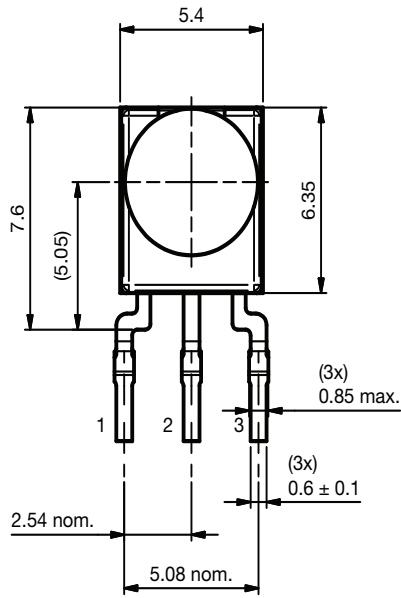
	<b>TSOP532..DF1P</b>	<b>TSOP534..DF1P</b>
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 72 cycles ≥ 10 cycles	10 to 40 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	72 cycles > 3 x burst length	40 cycles > 10 x burst length
Maximum number of continuous short bursts/second	950	1500
NEC code	Yes	Preferred
RC5/RC6 code	Yes	Preferred
Thomson RCA 56 kHz code	Yes	Preferred
Sharp code	Yes	Preferred
Sony code	Preferred	No
r-step code	Yes	Preferred
Suppression of interference from fluorescent lamps	Mild disturbance patterns are suppressed (example: signal pattern of Fig. 13)	Complex and critical disturbance patterns are suppressed (example: signal pattern of Fig. 14 or highly dimmed LCDs)

**Note**

- For data formats with short bursts please see the datasheet of TSOP531..DF1P, TSOP533..DF1P, TSOP535..DF1P

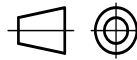
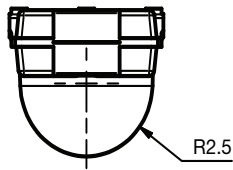


## PACKAGE DIMENSIONS in millimeters

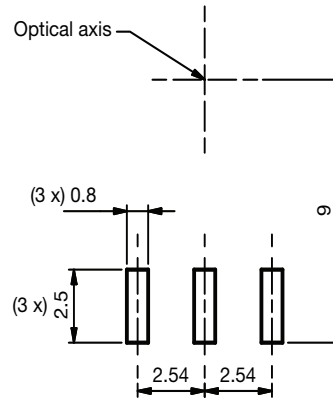


Not indicated tolerances ± 0.2

Proposed pad layout from component side (for reference only)



Technical drawings according to DIN specification



Drawing-No.: 6.550-5343.01-4  
Issue: 4; 03.02.2025



## ASSEMBLY INSTRUCTIONS

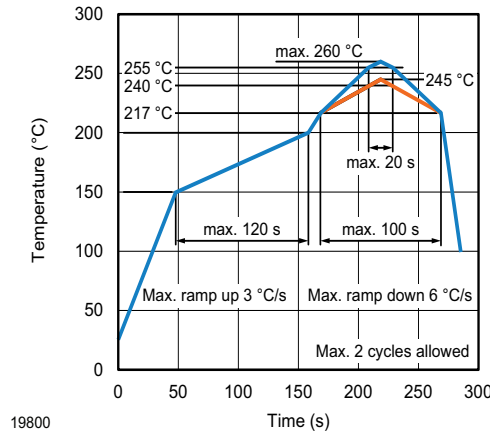
### Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

### Manual Soldering

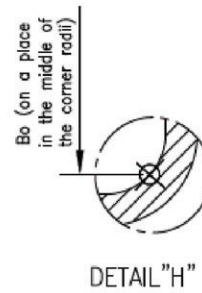
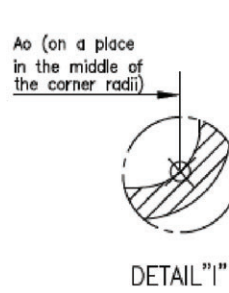
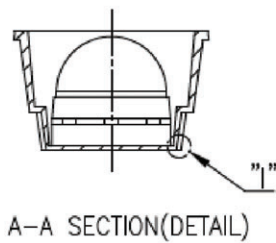
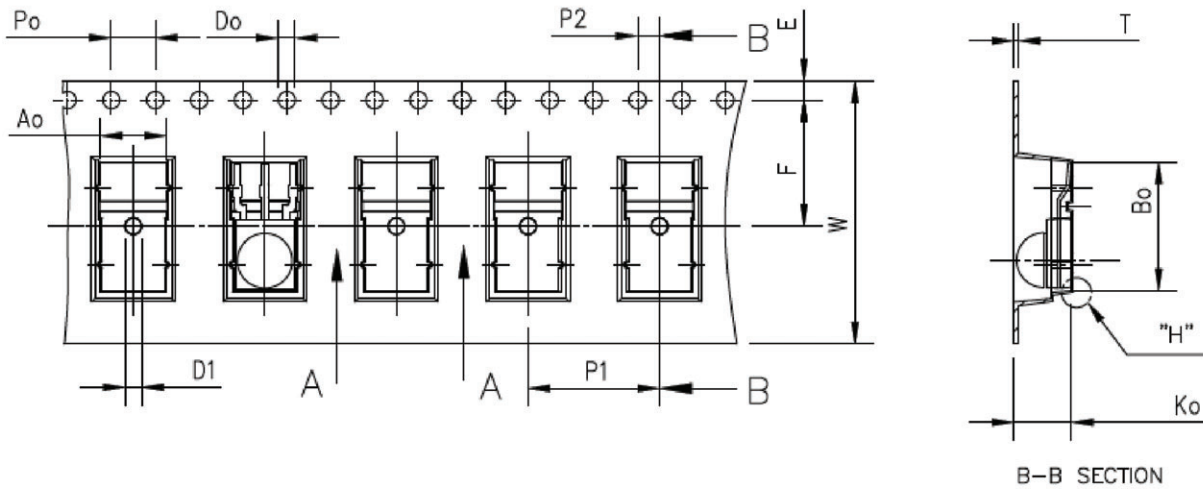
- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

## VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE





**PACKAGING DIMENSIONS** in millimeters

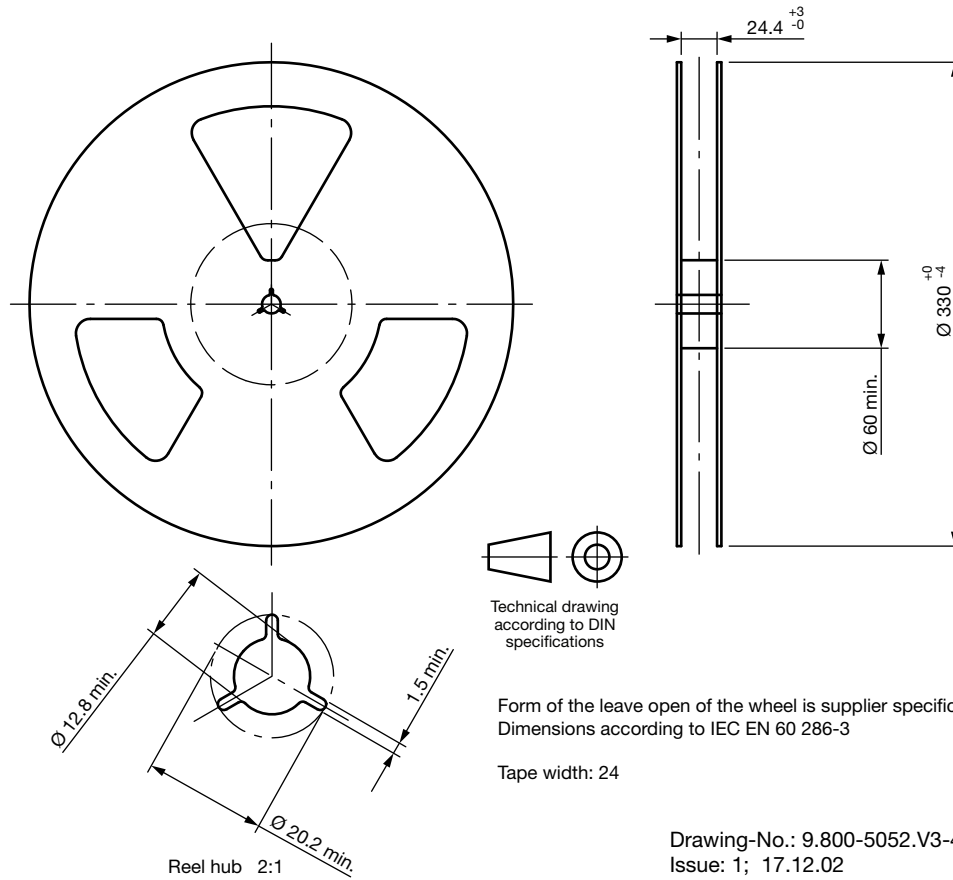


Drawing-No.: 9.700-5399.01-4  
Issue: 2; 29.06.18

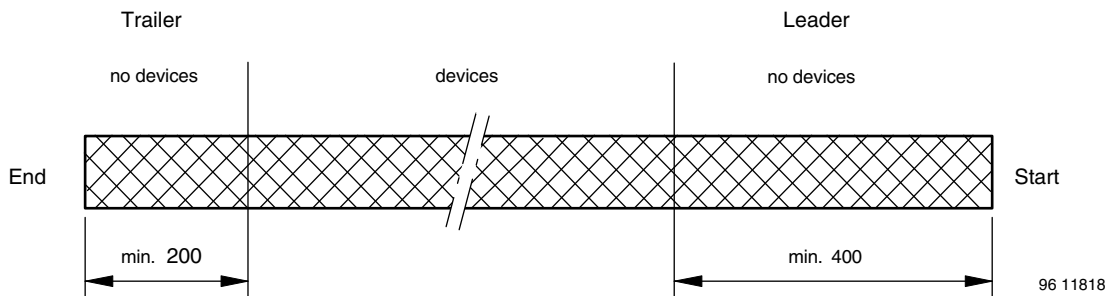
Item	A <sub>0</sub>	B <sub>0</sub>	K <sub>0</sub>	P <sub>0</sub>	P <sub>1</sub>	P <sub>2</sub>	T
Dimensions	6.08 ± 0.10	11.75 ± 0.10	5.25 ± 0.10	4.0 ± 0.10	12.0 ± 0.10	2.0 ± 0.10	0.40 ± 0.05
Item	E	F	D <sub>0</sub>	D <sub>1</sub>	W	10P <sub>0</sub>	
Dimensions	1.75 ± 0.10	11.50 ± 0.10	1.55 ± 0.05	1.5 min.	24.0 +0.30 / -0.10	40.0 ± 0.20	



## REEL DIMENSIONS in millimeters



## LEADER AND TRAILER DIMENSIONS in millimeters



## COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3

0.1 N to 1.3 N

300 mm/min. ± 10 mm/min.

165° to 180° peel angle



### ORDERING INFORMATION



#### Note

- d = "digit", please consult the list of available series on the previous page to create a valid part number

**Example: TSOP53238DF1P**

### PACKAGING QUANTITY

- 1100 pieces per reel
- 1 reel per box

### LABEL

#### Standard bar code labels for finished goods

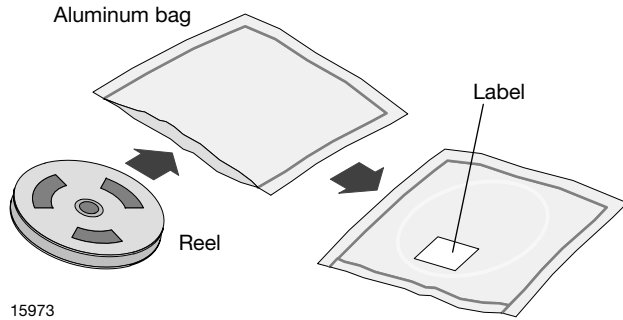
The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

<b>VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)</b>		
<b>PLAIN WRITING</b>	<b>ABBREVIATION</b>	<b>LENGTH</b>
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxxx+	Company logo
<b>LONG BAR CODE TOP</b>	<b>TYPE</b>	<b>LENGTH</b>
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
<b>SHORT BAR CODE BOTTOM</b>	<b>TYPE</b>	<b>LENGTH</b>
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17



### DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



### FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

- 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or
- 96 h at 60 °C + 5 °C and < 5 % RH for all device containers or
- 24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 4 label is included on all dry bags.

**CAUTION**  
This bag contains  
**MOISTURE-SENSITIVE DEVICES**

**LEVEL 4**

1. Shelf life in sealed bag: 12 months at < 40 °C and < 90 % relative humidity (RH)
2. After this bag is opened, devices that will be subjected to soldering reflow or equivalent processing (peak package body temp. 260 °C) must be
  - 2a. Mounted within 72 hours at factory condition of < 30 °C/60 % RH or
  - 2b. Stored at < 5 % RH
3. Devices require baking before mounting if:  
Humidity Indicator Card is > 10 % when read at 23 °C ± 5 °C or 2a. or 2b. are not met.
4. If baking is required, devices may be baked for:  
192 hours at 40 °C + 5 °C/ - 0 °C and < 5 % RH (dry air/nitrogen) or  
96 hours at 60 °C ± 5 °C and < 5 % RH for all device containers or  
24 hours at 125 °C ± 5 °C not suitable for reels or tubes

Bag Seal Date: \_\_\_\_\_  
(If blank, see barcode label)

Note: Level and body temperature defined by EIA JEDEC Standard J-STD-020

22522

EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags

### ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

### VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

PartNo: TSOP6440TR  
QTY: 1120  
SelCode: 000  
Origin PHILIPPINES  
PTC: 19 Machine: 38  
MSL: 4

Lot1: FE00JPD.02  
Lot2: 1924472A38  
Batch: 202447PH19

Region: 2410 SL: 0010  
Serial#: 0MM382283265  
Operator: 2653 ChM: F22

VISHAY  
RoHS  
H/F



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