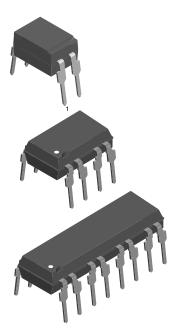


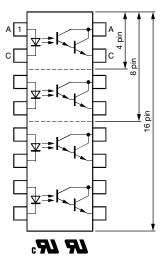
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K815P, K825P, K845P

Vishay Semiconductors

Optocoupler, Photodarlington Output





LINKS TO ADDITIONAL RESOURCES





DESCRIPTION

In the K815P, K825P, K845P parts, each channel consist of a photodarlington optically coupled to a gallium arsenide infrared-emitting diode in an 4 pin, 8 pin, and 16 pin plastic dual inline package.

The elements are mounted on one leadframe providing a fixed distance between input and output for highest safety requirements.

FEATURES

- Endstackable to 2.54 mm (0.1") spacing
- Isolation test voltage 5300 V_{RMS}
- Low temperature coefficient of CTR
- Wide ambient temperature range
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Programmable logic controllers
- Modems
- Answering machines
- General applications

AGENCY APPROVALS

- <u>UL</u>
- <u>cUL</u>



RoHS COMPLIANT

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End of Life June-2024 - Alternative Device: SFH619A



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ORDERING INFORMATION			
К 8 РА	# 5	Ρ	DIP-4/DIP-8/DIP-16
AGENCY CERTIFIED / PACKAGE		CTR (%)	
UL, cUL		> 600	
DIP-4		K815P	
DIP-8		K825P	
DIP-16		K845P	

ABSOLUTE MAXIMUM RATING	TEST CONDITION	SYMBOL	VALUE	UNIT
	TEST CONDITION	STMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	6	V
Forward current		١ _F	60	mA
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	А
Power dissipation		P _{diss}	100	mW
Junction temperature		Tj	125	°C
OUTPUT				
Collector emitter voltage		V _{CEO}	35	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		I _C	80	mA
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA
Power dissipation		P _{diss}	150	mW
Junction temperature		Tj	125	°C
COUPLER				
Total power dissipation		P _{tot}	250	mW
Operating ambient temperature		T _{amb}	-40 to +100	°C
Storage temperature range		T _{stg}	-55 to +125	°C
Soldering temperature ⁽¹⁾		T _{sld}	260	°C

Notes

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to wave profile for soldering conditions for through hole devices



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K815P, K825P, K845P

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ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT	INPUT						
Forward voltage	I _F = 20 mA	V _F	-	1.2	1.4	V	
Reverse current	V _R = 6 V	I _R	-	-	10	μA	
OUTPUT							
Collector emitter voltage	I _C = 100 μA	V _{CEO}	35	-	-	V	
Emitter collector voltage	I _E = 100 μA	V _{CEO}	7	-	-	V	
Collector dark current	$V_{CE} = 10 V$, $I_F = 0 A$, $E = 0$	I _{CEO}	-	-	100	nA	
COUPLER							
Collector emitter saturation voltage	$I_{\rm C} = 5$ mA, $I_{\rm F} = 20$ mA	V _{CEsat}	-	-	0.1	V	
Cut-off frequency	I_F = 10 mA, V_{CE} = 5 V, R_L = 100 Ω	f _c	-	10	-	kHz	
Coupling capacitance	f = 1 MHz	C _k	-	0.3	_	pF	

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	$I_{F} = 1 \text{ mA}, V_{CE} = 2 \text{ V}$	CTR	600	800	-	%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	V_{CE} = 2 V, I_{C} = 10 mA, R_{L} = 100 Ω (see Fig. 1)	t _r	-	300	-	μs
Turn-off time	V_{CE} = 2 V, I_{C} = 10 mA, R_{L} = 100 Ω (see Fig. 1)	t _{off}	-	250	-	μs

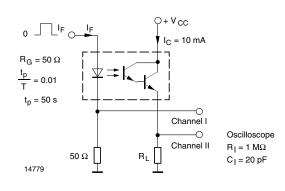


Fig. 1 - Test Circuit, Non-Saturated Operation

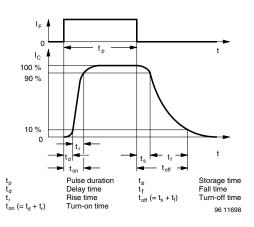


Fig. 2 - Switching Times

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SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40 / 100 / 21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}
Maximum transient isolation voltage		VIOTM	10 000	V _{peak}
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	400	mW
Input safety current		I _{SI}	275	mA
Safety temperature		T _S	175	°C
Creepage distance	Standard DIP-4		≥ 7	mm
Clearance distance	Standard DIP-4		≥7	mm
Creepage distance	400 mil DIP-4		≥ 8	mm
Clearance distance	400 mil DIP-4		≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



K815P, K825P, K845P

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

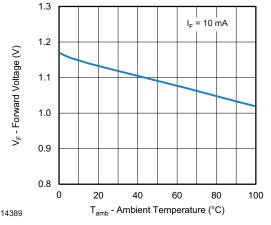


Fig. 3 - Forward Voltage vs. Ambient Temperature

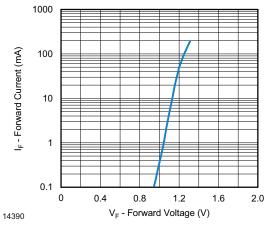


Fig. 4 - Forward Current vs. Forward Voltage

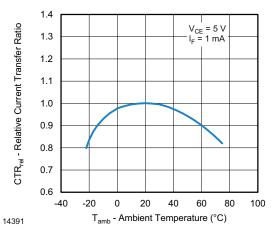


Fig. 5 - Relative Current Transfer Ratio vs. Ambient Temperature

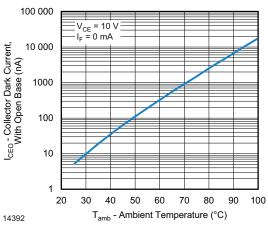


Fig. 6 - Collector Dark Current vs. Ambient Temperature

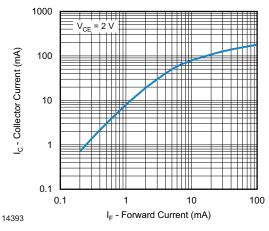
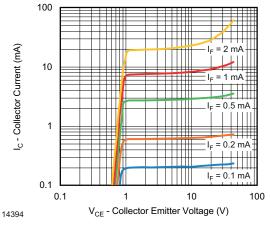
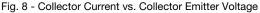


Fig. 7 - Collector Current vs. Forward Current





Rev. 2.3, 11-Jan-2024

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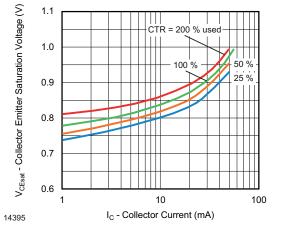
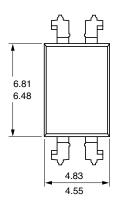
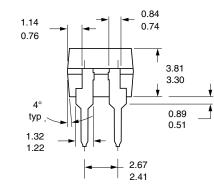


Fig. 9 - Collector Emitter Saturation Voltage vs. Collector Current









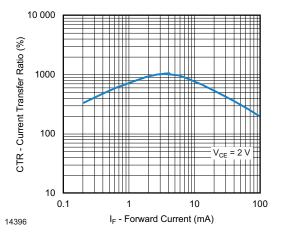
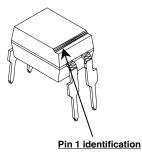
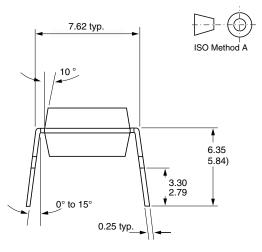


Fig. 10 - Current Transfer Ratio vs. Forward Current



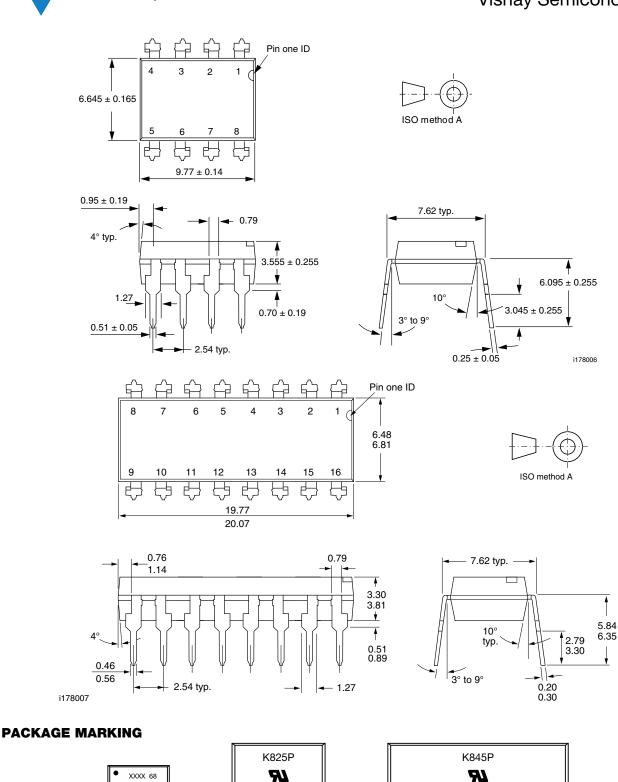


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Note

XXXX = LMC (lot marking code)

K815P

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1