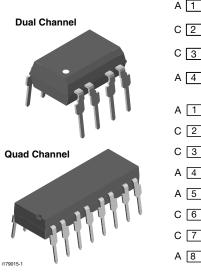


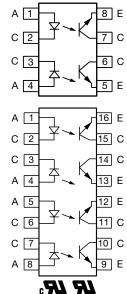


www.vishay.com

Vishay Semiconductors

# **Optocoupler, Phototransistor Output (Multichannel)**





### LINKS TO ADDITIONAL RESOURCES



#### DESCRIPTION

The CNY74-2H, CNY74-4H is an optically coupled pair with a GaAlAs infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

The CNY74-2H, CNY74-4H is especially for driving medium-speed logic, where it may be used to eliminate troublesome ground loop and noise problems. Also it can be used to replace relays and transformers in many digital interface applications, as well as analog applications such as CTR modulation.

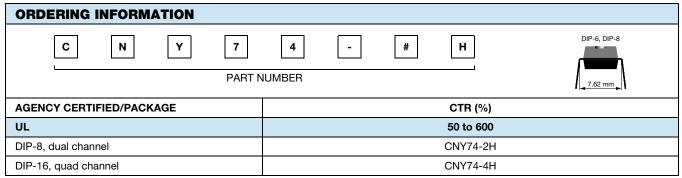
The CNY74-2H has two isolated channels in a single DIP package; the CNY74-4H has four isolated channels per package.

#### FEATURES

- CNY74-2H, CNY74-4H TTL compatible
- Transfer ratio, 35 % typical
- Coupling capacitance, 0.5 pF
- Dual and quad channel
- Industry standard DIP packages
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### AGENCY APPROVALS

- <u>UL</u>
- <u>cUL</u>



Note

• Additional options may be possible, please contact sales office

1

Pb-free





www.vishay.com

# CNY74-2H, CNY74-4H

### **Vishay Semiconductors**

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT				
INPUT									
Peak reverse voltage			V <sub>R</sub>	3	V				
Forward continuous current			I <sub>F</sub>	60	mA				
Power dissipation			P <sub>diss</sub>	100	mW				
Derate linearly from 55 %				1.33	mW/°C				
OUTPUT									
Collector emitter breakdown voltage			BV <sub>CEO</sub>	70	V				
Emitter collector breakdown voltage			BV <sub>ECO</sub>	7	V				
Power dissipation			P <sub>diss</sub>	150	mW				
Derate linearly from 25 °C				2	mW/°C				
COUPLER									
Total package dissipation		CNY74-2H	P <sub>tot</sub>	400	mW				
		CNY74-4H	Ptot	500	mW				
Derate linearly from 25 °C		CNY74-2H		5.33	mW/°C				
		CNY74-4H		6.67	mW/°C				
Storage temperature			T <sub>stg</sub>	-55 to +150	°C				
Operating temperature			T <sub>amb</sub>	-55 to +100	°C				
Lead soldering time at 260 °C				10	S				

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I <sub>F</sub> = 20 mA	V <sub>F</sub>	-	1.3	1.5	V	
Reverse current	V <sub>R</sub> = 3 V	I <sub>R</sub>	-	0.1	100	μA	
Capacitance	V <sub>R</sub> = 0 V	Co	-	25	-	pF	
OUTPUT							
Collector emitter breakdown voltage	I <sub>C</sub> = 1 mA	BV <sub>CEO</sub>	70	-	-	V	
Collector emitter leakage current	$V_{CE} = 5 V, I_F = 0 A$	I <sub>CEO</sub>	-	-	100	nA	
Capacitance collector emitter	$V_{CE} = 0 V, f = 1 Hz$	C <sub>CE</sub>	-	10		pF	
COUPLER							
Saturation voltage, collector emitter	$I_{\rm C} = 2$ mA, $I_{\rm F} = 16$ mA	V <sub>CEsat</sub>	-	0.3	0.5	V	
Resistance (input to output)		R <sub>IO</sub>	-	100	-	GΩ	
Capacitance (input to output)		C <sub>IO</sub>	-	0.5	-	pF	

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
DC current transfer ratio	$I_{F} = 5 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR	50	-	600	%
DC current transfer ratio	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR	60	-	-	%

Rev. 2.1, 11-Jan-2024

### End of Life June-2024



www.vishay.com

# CNY74-2H, CNY74-4H

## **Vishay Semiconductors**

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see Fig. 1)	t <sub>d</sub>	-	3	-	μs
Rise time	$V_{S}$ = 5 V, $I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$ (see Fig. 1)	t <sub>r</sub>	-	3	-	μs
Fall time	$V_{S}$ = 5 V, $I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$ (see Fig. 1)	t <sub>f</sub>	-	4.7	-	μs
Storage time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ (see Fig. 1)	ts	-	0.3	-	μs
Turn-on time	$V_{S}$ = 5 V, $I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$ (see Fig. 1)	t <sub>on</sub>	-	6	-	μs
Turn-off time	$V_{S}$ = 5 V, $I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$ (see Fig. 1)	t <sub>off</sub>	-	5	-	μs
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_C = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega \text{ (see Fig. 2)}$	t <sub>on</sub>	-	9	-	μs
Turn-off time	$V_{S}$ = 5 V, $I_{C}$ = 10 mA, $R_{L}$ = 1 k $\Omega$ (see Fig. 2)	t <sub>off</sub>	-	18	-	μs

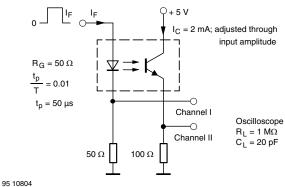


Fig. 1 - Test Circuit, Non-Saturated Operation

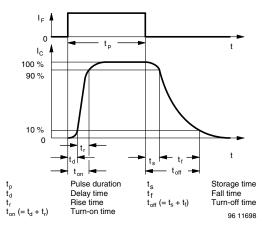


Fig. 3 - Switching Times

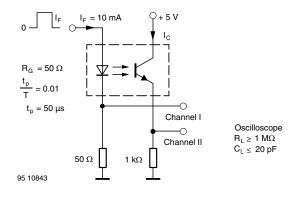


Fig. 2 - Test Circuit, Saturated Operation

### End of Life June-2024



www.vishay.com

# CNY74-2H, CNY74-4H

### **Vishay Semiconductors**

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		55 / 100 / 21			
Comparative tracking index		CTI	175			
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	4420	V <sub>RMS</sub>		
Maximum transient isolation voltage		VIOTM	10 000	V <sub>peak</sub>		
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	890	V <sub>peak</sub>		
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω		
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω		
Output safety power		P <sub>SO</sub>	400	mW		
Input safety current		I <sub>SI</sub>	275	mA		
Safety temperature		T <sub>S</sub>	175	°C		
Creepage distance			≥7	mm		
Clearance distance			≥ 7	mm		
Insulation thickness		DTI	≥ 0.4	mm		

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

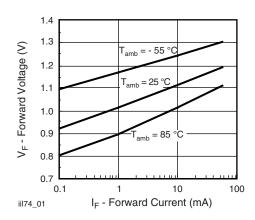


Fig. 4 - Forward Voltage vs. Forward Current

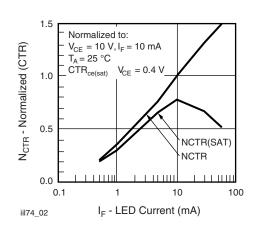


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

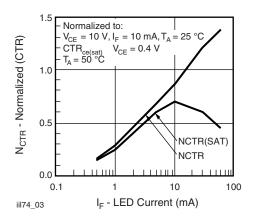


Fig. 6 - Normalized Non-Saturated and Saturated CTR vs. LED Current

4

For technical questions, contact: <u>optocoupleranswers@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



# CNY74-2H, CNY74-4H

**Vishay Semiconductors** 

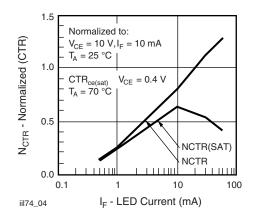


Fig. 7 - Normalized Non-Saturated and Saturated CTR vs. LED Current

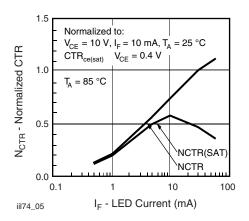


Fig. 8 - Normalized Non-Saturated and Saturated CTR vs. LED Current

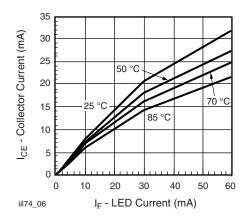


Fig. 9 - Collector Emitter Current vs. Temperature and LED Current

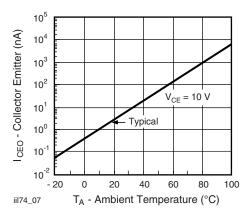


Fig. 10 - Collector Emitter Leakage Current vs.Temperature

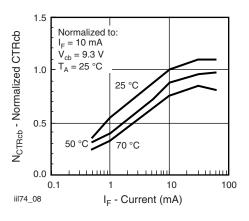


Fig. 11 - Normalized CTR<sub>cb</sub> vs. LED Current and Temperature

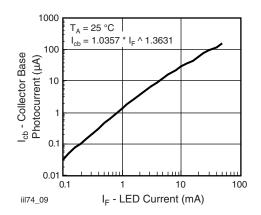


Fig. 12 - Collector Base Photocurrent vs. LED Current

Rev. 2.1, 11-Jan-2024

5

Document Number: 83526

For technical questions, contact: <u>optocoupleranswers@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



# CNY74-2H, CNY74-4H

**Vishay Semiconductors** 

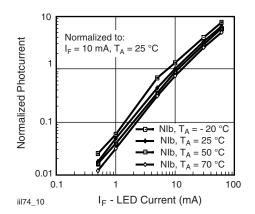


Fig. 13 - Normalized Photocurrent vs. I<sub>F</sub> and Temperature

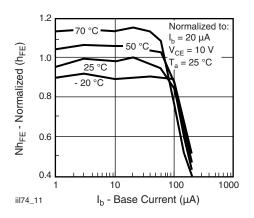


Fig. 14 - Normalized Non-Saturated  $h_{\mbox{FE}}$  vs. Base Current and Temperature

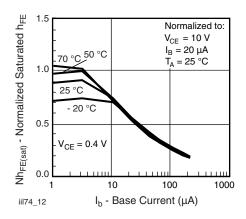


Fig. 15 - Normalized Saturated  $h_{\mbox{\scriptsize FE}}$  vs. Base Current and Temperature

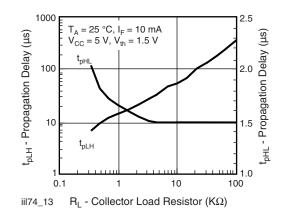


Fig. 16 - Propagation Delay vs. Collector Load Resistor

### End of Life June-2024

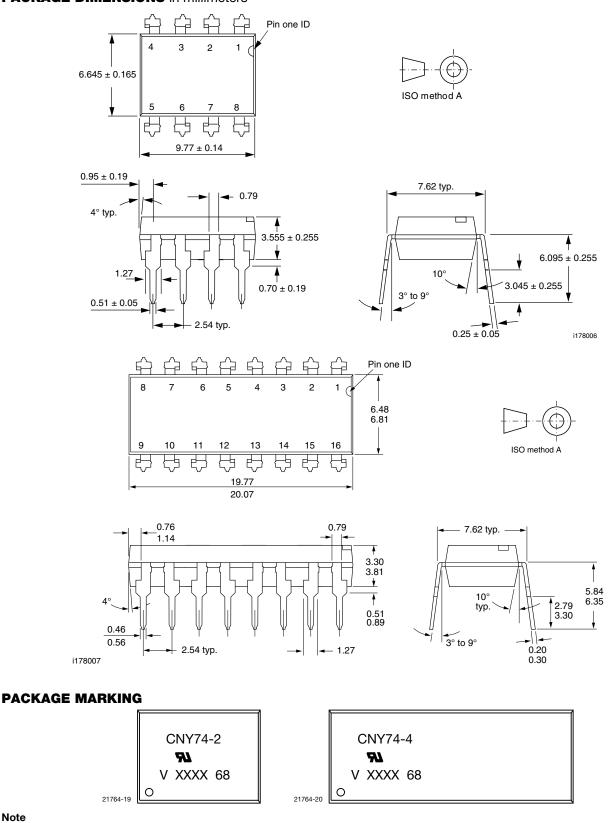




www.vishay.com

**Vishay Semiconductors** 

#### **PACKAGE DIMENSIONS** in millimeters



CNY74-2H and CNY74-4H are marked as CNY74-2 and CNY74-4 respectively

XXXX = LMC (lot marking code)

Note

Rev. 2.1, 11-Jan-2024

7

Document Number: 83526

For technical questions, contact: optocoupleranswers@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025