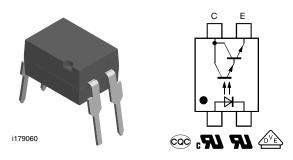


## **TCED1100**

## Vishay Semiconductors

# Optocoupler, Photodarlington Output, High Gain



### **LINKS TO ADDITIONAL RESOURCES**



#### **DESCRIPTION**

The TCED1100 consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4-lead plastic dual inline package.

#### **VDE STANDARDS**

voltage ≤ 400 V<sub>RMS</sub>)

These couplers perform safety functions according to the following equipment standards:

- DIN EN 60747-5-5
   Optocoupler for electrical safety requirements
- IEC EN 60950
  Office machines (applied for reinforced isolation for mains
- VDE 0804

Telecommunication apparatus and data processing

• IEC60065

Safety for mains-operated electronic and related household apparatus

#### **FEATURES**

- Extra low coupling capacity typical 0.2 pF
- High common mode rejection
- Available in single or four channels
- Rated impulse voltage (transient overvoltage)
   V<sub>IOTM</sub> = 10 kV<sub>peak</sub>



- Isolation test voltage (partial discharge test voltage) V<sub>pd</sub> = 1.67 kV<sub>peak</sub>
- Rated isolation voltage (RMS includes DC)  $V_{IORM} = 800 V_{peak}$
- Rated recurring peak voltage (repetitive)
   V<sub>IORM</sub> = 890 V<sub>P</sub>
- Thickness though insulation ≥ 0.4 mm
- Creepage current resistance according to VDE 0303/ IEC60112 comparative tracking index: CTI ≥ 175
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **AGENCY APPROVALS**

- <u>UL</u>
- cUL
- DIN EN 60747-5-5 (VDE 0804)
- CQC GB4943.1
- CQC GB8898

ORDERING INFORMATION					
T C E D 1  PART NUMBER	1 0 0 #				
AGENCY CERTIFIED / PACKAGE	CTR (%)				
UL, cUL, VDE, CQC	600				
DIP-4	TCED1100				



## **TCED1100**

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
INPUT									
Reverse voltage		$V_{R}$	6	V					
Forward current		I <sub>F</sub>	60	mA					
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	Α					
Power dissipation		P <sub>diss</sub>	70	mW					
Junction temperature		Tj	125	°C					
OUTPUT									
Collector emitter voltage		$V_{CEO}$	35	V					
Emitter collector voltage		V <sub>ECO</sub>	7	V					
Collector current		Ic	80	mA					
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA					
Power dissipation		P <sub>diss</sub>	70	mW					
Junction temperature		Tj	125	°C					
COUPLER									
Isolation test voltage (RMS)	t = 1 min	V <sub>ISO</sub>	4420	$V_{RMS}$					
Isolation voltage		V <sub>IORM</sub>	890	$V_{P}$					
Total power dissipation		P <sub>tot</sub>	200	mW					
Operating ambient temperature range		T <sub>amb</sub>	-55 to +100	°C					
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C					
Soldering temperature (1)	2 mm from case, t ≤ 10 s	T <sub>sld</sub>	260	°C					

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to wave profile for soldering conditions for through hole devices

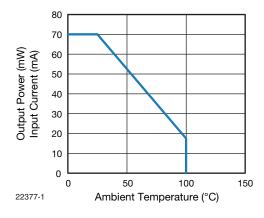


Fig. 1 - Abs. max. Power Dissipation (mW) Abs. max. Input Current (mA)



## **TCED1100**

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	I <sub>F</sub> = 20 mA	V <sub>F</sub>	-	1.15	1.4	V		
Junction capacitance	$V_R = 0 V, f = 1 MHz$	C <sub>j</sub>	-	50	-	pF		
OUTPUT	OUTPUT							
Collector emitter voltage	$I_C = 1 \text{ mA}$	V <sub>CEO</sub>	32	-	-	V		
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V		
Collector ermitter cut-off current	$V_{CE} = 10 \text{ V}, I_F = 0, E = 0$	I <sub>CEO</sub>	-	15	100	nA		
COUPLER								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 5 \text{ mA}$	V <sub>CEsat</sub>	-	-	1	V		
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>	-	10	-	kHz		
Coupling capacitance	f = 1 MHz	C <sub>k</sub>	-	0.6	-	pF		

#### Note

Minimum and maximum values are tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
I <sub>C</sub> /I <sub>F</sub>	$V_{CE} = 2 \text{ V}, I_F = 1 \text{ mA}$	CTR	600	800	-	%	

MAXIMUM SAFETY RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
input							
Forward current		I <sub>F</sub>	-	-	275	mA	
output							
Power dissipation		P <sub>diss</sub>	-	-	400	mW	
Coupler							
Rated impulse voltage		V <sub>IOTM</sub>	-	-	10	kV	
Safety temperature		T <sub>SI</sub>	-	-	175	°C	
Safety output power		P <sub>SO</sub>	-	-	400	mW	
Safety input current		I <sub>SI</sub>	-	-	275	mA	

### Note

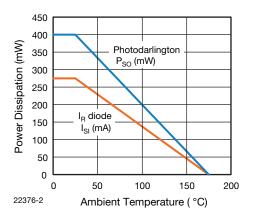
According to DIN EN 60747-5-2 (see fig. 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance
with the safety ratings shall be ensured by means of suitable protective circuits.

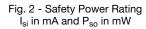
INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	$V_{pd}$	1.67	-	-	kV <sub>peak</sub>	
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$	$V_{IOTM}$	10	-	-	kV <sub>peak</sub>	
	(see fig. 2)	$V_{pd}$	1.42	-	-	kV <sub>peak</sub>	
Insulation resistance	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>	-	-	Ω	
	$V_{IO} = 500 \text{ V}, T_{amb} = 110  ^{\circ}\text{C}$	R <sub>IO</sub>	10 <sup>11</sup>	-	-	Ω	
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 175 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>	-	-	Ω	





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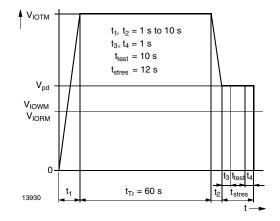


Fig. 3 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-2; IEC60747-5-5

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	$V_{CC}$ = 2 V, $I_C$ = 10 mA, $R_L$ = 100 $\Omega$ , (see Fig. 3)	t <sub>r</sub>	-	300	-	μs
Fall time	$V_{CC} = 2 \text{ V}, I_{C} = 10 \text{ mA}, R_{L} = 100 \Omega, (\text{see Fig. 3})$	t <sub>f</sub>	-	250	-	μs

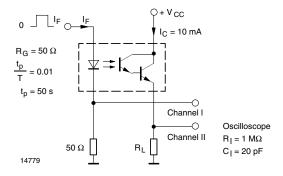


Fig. 4 - Test Circuit, Non-Saturated Operation

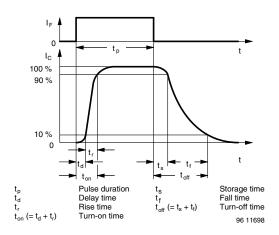


Fig. 5 - Switching Times



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### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

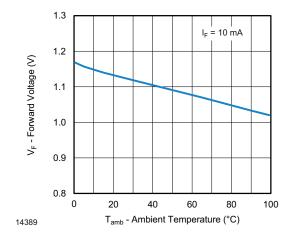


Fig. 6 - Forward Voltage vs. Ambient Temperature

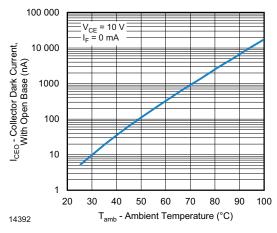


Fig. 9 - Collector Dark Current vs. Ambient Temperature

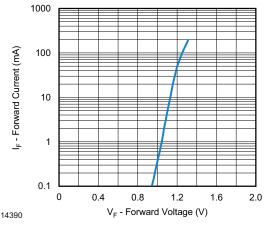


Fig. 7 - Forward Current vs. Forward Voltage

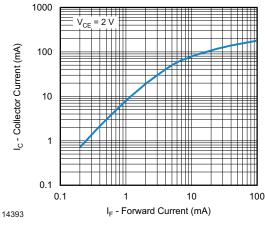


Fig. 10 - Collector Current vs. Forward Current

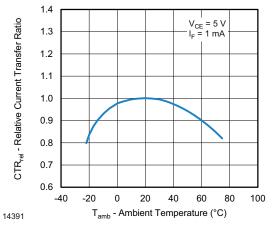


Fig. 8 - Relative Current Transfer Ratio vs. Ambient Temperature

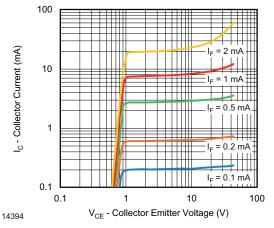
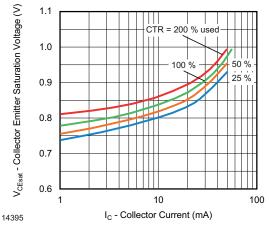


Fig. 11 - Collector Current vs. Collector Emitter Voltage



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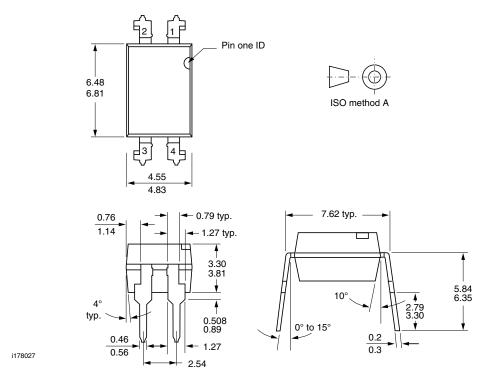


10 000 (%) 1000 (%) 1000 (%) 1

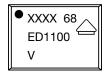
Fig. 12 - Collector Emitter Saturation Voltage vs. Collector Current

Fig. 13 - Current Transfer Ratio vs. Forward Current

### **PACKAGE DIMENSIONS** in millimeters



### **PACKAGE MARKING** (example)



### Note

• XXXX = LMC (lot marking code)



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