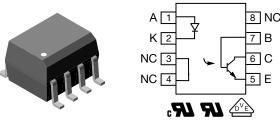


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Vishay Semiconductors

Optocoupler, Phototransistor Output, With Base Connection in SOIC-8 Package



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The IL215AT, IL216AT, IL217AT are optically coupled pairs with a gallium arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The IL215AT, IL216AT, IL217AT comes in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space. In addition to eliminating through hole requirements, this package conforms to standards for surface mounted devices.

The high CTR at low input current is designed for low power consumption requirements such as CMOS microprocessor interfaces.

FEATURES

- · High current transfer ratio
- Isolation test voltage, 4000 V_{RMS}
- Industry standard SOIC-8 surface mountable package



- Compatible with dual wave, vapor phase and IR reflow soldering
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

AGENCY APPROVALS

- UL
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1

ORDERING INFORMATION									
	L	2	1	#	Α	Т	SOIC-8		
				PART NUMBI	ER		6.1 mm		
ACENCY CERTIF	AGENCY CERTIFIED / PACKAGE			CTR (%)					
AGENCY CENTIF	TED / PACKA	IGE	1 mA						
UL, cUL	UL, cUL			> 20 > 50		> 50	> 100		
SOIC-8			II	L215AT		IL216AT	IL217AT		



PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Peak reverse voltage		V _R	6	V
Forward continuous current		I _F	60	mA
Power dissipation		P _{diss}	90	mW
Derate linearly from 25 °C			1.2	mW/°C
OUTPUT				
Collector emitter breakdown voltage		BV _{CEO}	30	V
Emitter collector breakdown voltage		BV _{ECO}	7	V
Collector base breakdown voltage		V _{CBO}	70	V
I _{CMAX. DC}		I _{CMAX. DC}	50	mA
I _{CMAX} .	t < 1 ms	I _{CMAX} .	100	mA
Power dissipation		P _{diss}	150	mW
Derate linearly from 25 °C			2	mW/°C
COUPLER	•			
Isolation test voltage	1 s	V _{ISO}	4000	V _{RMS}
Total package dissipation	LED and detector	P _{tot}	240	mW
Derate linearly from 25 °C			3.2	mW/°C
Storage temperature		T _{stg}	-55 to +150	°C
Operating temperature		T _{amb}	-55 to +100	°C
Soldering time	At 260 °C		10	S

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION SYMBOL		MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	I _F = 1 mA	V_{F}	-	1.0	1.5	V		
Reverse current	V _R = 6 V	I _R	-	0.1	100	μA		
Capacitance	$V_R = 0 V$	Co	-	13	-	pF		
OUTPUT								
Collector emitter breakdown voltage	I _C = 10 μA	BV _{CEO}	30	-	-	V		
Emitter collector breakdown voltage	I _E = 10 μA	BV _{ECO}	7	-	-	V		
Collector dark current	V _{CE} = 10 V, I _F = 0 A	I _{CEO}	-	5	50	nA		
Collector emitter capacitance	V _{CE} = 0 V	C _{CE}	-	10		pF		
COUPLER								
Saturation voltage, collector emitter	$I_F = 1.0 \text{ mA}, I_C = 0.1 \text{ mA}$	V _{CEsat}	-	-	0.4	V		
Isolation test voltage	1 s	V _{ISO}	4000	-	-	V _{RMS}		
Capacitance (input to output)		C _{IO}	-	0.5	-	pF		
Resistance (input to output)		R _{IO}	-	100	-	GΩ		

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.



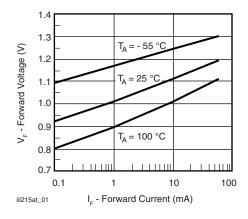
CURRENT TRANSFER RATIO								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
DC current transfer ratio	I _F = 1.0 mA, V _{CE} = 5.0 V	IL215AT	CTR _{DC}	20	50	-	%	
		IL216AT	CTR _{DC}	50	80	-	%	
		IL217AT	CTR _{DC}	100	130	-	%	

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Switching time	I_F = 2.0 mA, R_L = 100 Ω , V_{CC} = 10 V	t _{on} , t _{off}	-	3.0	-	μs	

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)			-	55 / 100 / 21	-			
Comparative tracking index		CTI	175	-	399			
V _{IOTM}			6000	-	-	V		
V _{IORM}			560	-	-	V		
P _{SO}			-	-	350	mW		
I _{SI}			-	-	150	mA		
T _{SI}			-	-	165	°C		
Creepage distance			4	-	-	mm		
Clearance distance			4	-	-	mm		
Insulation thickness			0.2	-	-	mm		

Note

TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)





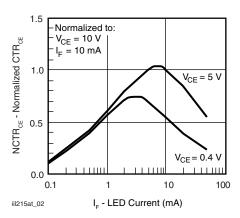


Fig. 2 - Normalized Non-Saturated and Saturated CTR $_{\rm CE}$ vs. LED Current

[•] As per IEC 60747-5-5, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



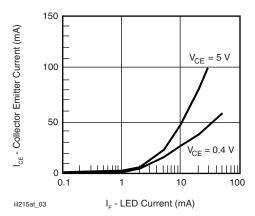


Fig. 3 - Collector Emitter Current vs. LED Current

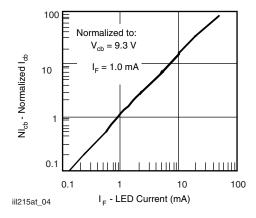


Fig. 4 - Normalized Collector Base Photocurrent vs. LED Current

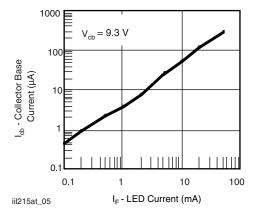


Fig. 5 - Collector Base Photocurrent vs. LED Current

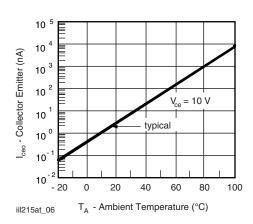


Fig. 6 - Collector Emitter Leakage Current vs.Temperature

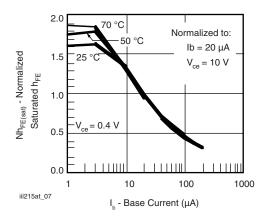


Fig. 7 - Normalized Saturated h_{FE} vs. Base Current and Temperature

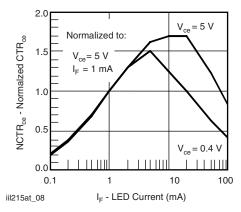


Fig. 8 - Normalized Non-Saturated and Saturated CTR_{CE} vs. LED Current



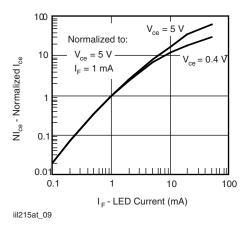


Fig. 9 - Normalized Non-Saturated and Saturated Collector Emitter Current vs. LED Current

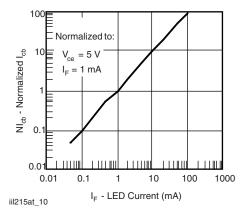


Fig. 10 - Normalized Collector Base Photocurrent vs. LED Current

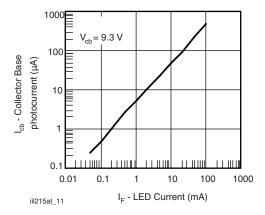


Fig. 11 - Collector Base Photocurrent vs. LED Current

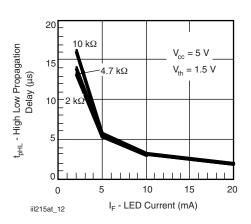


Fig. 12 - High to Low Propagation Delay vs. LED Current and Load Resistor

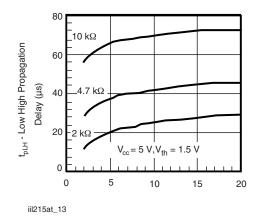


Fig. 13 - Low to High Propagation Delay vs. LED Current and Load Resistor

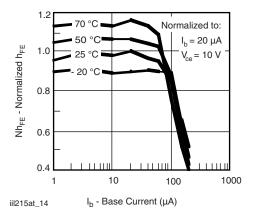


Fig. 14 - Normalized Non-Saturated h_{FE} vs. Base Current and Temperature

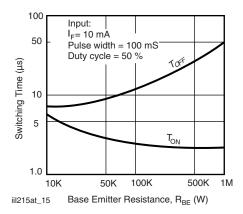


Fig. 15 - Typical Switching Characteristics vs. Base Resistance (Saturated Operation)

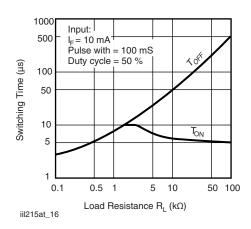
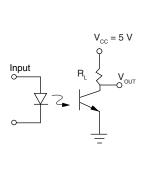


Fig. 16 - Typical Switching Times vs. Load Resistance



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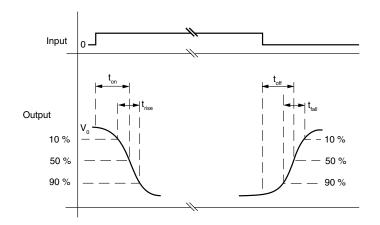
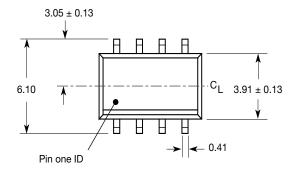
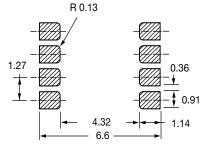
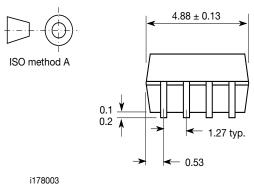


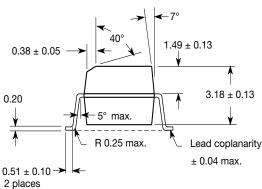
Fig. 17 - Switching Test Circuit

PACKAGE DIMENSIONS in millimeters









PACKAGE MARKING (example)

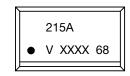


Fig. 18 - Example of IL215AT

Notes

- XXXX = LMC (lot marking code)
- Tape and reel suffix (T) is not part of the package marking



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