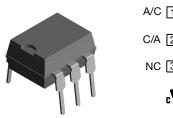
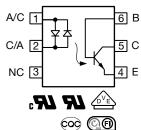


Optocoupler, Phototransistor Output, AC Input, With Base Connection





FEATURES

- · AC or polarity insensitive inputs
- Built-in reverse polarity input protection
- Improved CTR symmetry
- Industry standard DIP package
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





LINKS TO ADDITIONAL RESOURCES







DESCRIPTION

The IL250 and IL252 are bidirectional input optically coupled isolators consisting of two gallium arsenide infrared LEDs coupled to a silicon NPN phototransistor per channel.

The IL250 has a minimum CTR of 50 % and the IL252 has a minimum CTR of 100 %.

The IL250 and IL252 are single channel optocouplers.

APPLICATIONS

• Ideal for AC signal detection and monitoring

AGENCY APPROVALS

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-), available with option 1
- BSI
- CQC GB4943.1
- CQC GB8898
- FIMKO

ORDERING INFORMATION					
I L x 2 5	CTR PACKAGE OPTION	# TAPE AND REEL Option 7 Option 9			
AGENCY CERTIFIED / PACKAGE	CTR	(%)			
AGENCY CERTIFIED / PACKAGE	SINGLE CHANNEL, 6 PIN				
UL, cUL, CQC, CSA, BSI	≥ 50	≥ 100			
DIP-6	IL250	IL252			
SMD-6, option 7	-	IL252-X007T			

Notes

- Additional options may be possible, please contact sales office
- (1) Also available in tubes; do not add "T" to end



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Forward continuous current		I _F	60	mA	
Power dissipation		P _{diss}	100	mW	
Derate linearly from 25 °C			1.33	mW/°C	
OUTPUT					
Collector emitter breakdown voltage		BV _{CEO}	30	V	
Emitter base breakdown voltage		BV _{EBO}	5	V	
Collector base breakdown voltage		BV _{CBO}	70	V	
Power dissipation single channel		P _{diss}	200	mW	
Power dissipation dual channel		P _{diss}	150	mW	
Derate linearly from 25 °C single channel			2.6	mW/°C	
Derate linearly from 25 °C dual channel			2	mW/°C	
COUPLER					
Total dissipation single channel		P _{tot}	250	mW	
Total dissipation dual channel		P _{tot}	400	mW	
Derate linearly from 25 °C single channel			3.3	mW/°C	
Derate linearly from 25 °C dual channel			5.3	mW/°C	
Storage temperature		T _{stg}	-55 to +150	°C	
Operating temperature		T _{amb}	-55 to +100	°C	
Lead soldering time at 260 °C			10	S	

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	$I_F = \pm 10 \text{ mA}$	V_{F}	-	1.2	1.5	V
ОИТРИТ	ОИТРИТ					
Collector emitter breakdown voltage	I _C = 1 mA	BV _{CEO}	30	50	-	V
Emitter base breakdown voltage	I _E = 100 μA	BV _{EBO}	7	10	-	V
Collector base breakdown voltage	I _C = 10 μA	BV _{CBO}	70	90	-	V
Collector emitter leakage current	V _{CE} = 10 V	I _{CEO}	-	5	50	nA
COUPLER						
Collector emitter saturation voltage	$I_F = \pm 16 \text{ mA}, I_C = 2 \text{ mA}$	V _{CEsat}	-	-	0.4	V

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
1. //	1 . 10 10	IL250	CTR _{DC}	50	-	-	%
$I_{\text{C}}/I_{\text{F}}$ $I_{\text{F}} = \pm 10 \text{ mA},$	$I_F = \pm 10 \text{ mA}, V_{CE} = 10 \text{ V}$	IL252	CTR _{DC}	100	-	-	%
Symmetry	I _F = ± 10 mA			0.50	1	2	

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER TEST CONDITION SYMBOL MIN. TYP. MAX. UNIT						
Turn-on time		t _{on}	-	TBD	-	μs
Turn-off time		t _{off}	-	TBD	-	μs

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		55/100/21			
Comparative tracking index		CTI	175			
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}		
Maximum transient isolation voltage		V _{IOTM}	10 000	V _{peak}		
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}		
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω		
	$V_{IO} = 500 \text{ V}, T_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω		
Output safety power		P _{SO}	400	mW		
Input safety current		I _{SI}	275	mA		
Safety temperature		T _S	175	°C		
Creepage distance			≥ 7	mm		
Clearance distance			≥ 7	mm		
Insulation thickness		DTI	≥ 0.4	mm		

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

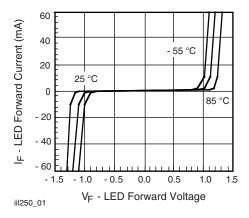


Fig. 1 - LED Forward Current vs.Forward Voltage

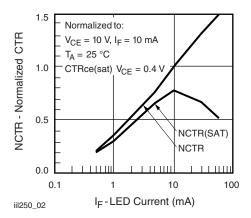


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

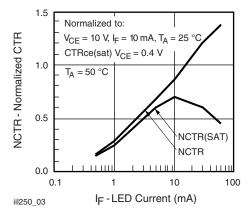


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

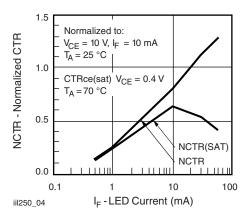


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

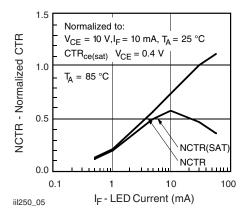


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

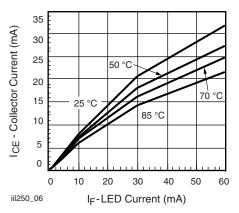


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current





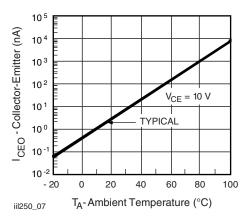


Fig. 7 - Collector Emitter Leakage Current vs.Temperature

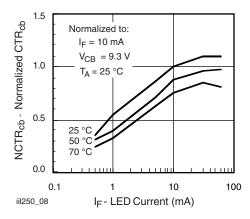


Fig. 8 - Normalized CTR_{CB} vs. LED Current and Temperature

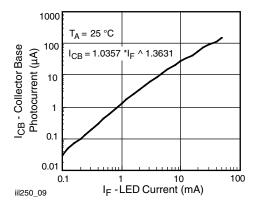


Fig. 9 - Collector Base Photocurrent vs. LED Current

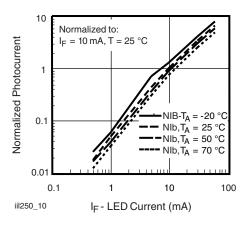


Fig. 10 - Normalized Photocurrent vs. I_{F} and Temperature

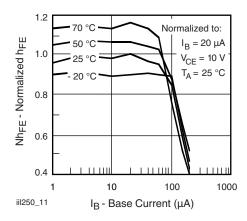


Fig. 11 - Normalized Non Saturated h_{FE} vs. Base Current and Temperature

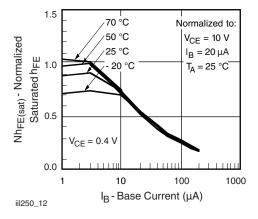


Fig. 12 - Normalized Saturated h_{FE} vs. Base Current and Temperature



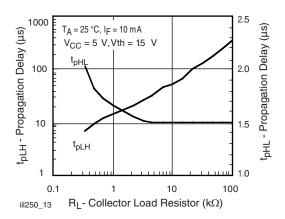


Fig. 13 - Propagation Delay vs. Collector Load Resistor

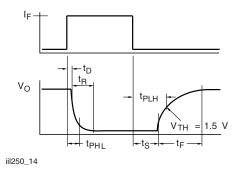
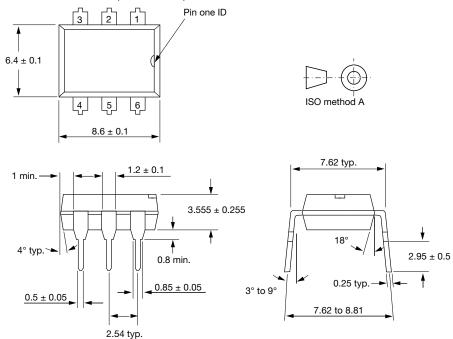


Fig. 14 - Switching Timing

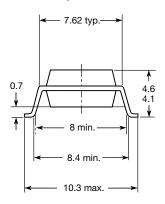
Fig. 15 - Switching Schematic



PACKAGE DIMENSIONS in inches (millimeters)



Option 7



PACKAGE MARKING



Note

• XXXX = LMC (lot marking code)



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