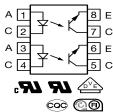


# Vishay Semiconductors

# **Optocoupler, Phototransistor Output, Dual Channel**





#### **FEATURES**

- Dual version of SFH610 series
- Isolation rated voltage 4420 V<sub>RMS</sub>
- $V_{CEsat}$  0.25 ( $\leq$  0.4) V at  $I_F$  = 10 mA,  $I_C$  = 2.5 mA
- V<sub>CEO</sub> = 70 V
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





# ROHS

### **DESCRIPTION**

The ILD610 series is a dual channel optocoupler series for high density applications. Each channel consists of an optically coupled pair with a gallium arsenide infrared LED and silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The ILD610 series is the dual version of SFH610 series and uses a repetitive pin-out configuration instead of the more common alternating pin-out used in most dual couplers.

### **AGENCY APPROVALS**

- <u>UL 1577</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884), available with option 1
- CQC
- FIMKO

ORDERING INFORMATION							
I L D 6		# DIP-8					
AGENCY CERTIFIED / PACKAGE							
UL, cUL, CQC, FIMKO	40 to 80	100 to 200					
DIP-8	ILD610-1	ILD610-3					

#### Note

Additional options may be possible, please contact sales office

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		$V_R$	6.0	V				
Surge forward current	t ≤ 1.0 ms	I <sub>FSM</sub>	1.5	Α				
Power dissipation		P <sub>diss</sub>	100	mW				
Derate linearly from 25 °C			1.3	mW/°C				
Forward continuous current		I <sub>F</sub>	60	mA				
OUTPUT								
Collector emitter voltage		$V_{CEO}$	70	V				
Collector current		I <sub>C</sub>	50	mA				
Collector current	t ≤ 1.0 ms	I <sub>C</sub>	100	mA				
Power dissipation		P <sub>diss</sub>	150	mW				
Derate linearly from 25 °C			2.0	mW/°C				
COUPLER								
Storage temperature		T <sub>stg</sub>	-55 to +150	°C				
Operating temperature		T <sub>amb</sub>	-55 to +100	°C				
Junction temperature		Tj	100	°C				
Lead soldering time at 260 °C			10	S				

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.



# Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 60 \text{ mA}$		$V_{F}$	-	1.25	1.65	V
Reverse current	$V_{R} = 6.0 \text{ V}$		I <sub>R</sub>	-	0.01	10	μΑ
Capacitance	$V_R = 0 V, f = 1.0 MHz$		Co	-	25	-	pF
OUTPUT							
Collector emitter breakdown	$I_{C} = 10 \text{ mA}, I_{E} = 10 \mu\text{A}$		BV <sub>CEO</sub>	70	90	-	V
voltage			BV <sub>CEO</sub>	6.0	7.0	-	V
Collector emitter dark current	V <sub>CE</sub> = 10 V		I <sub>CEO</sub>	-	2.0	50	nA
Collector emitter capacitance	$V_{CE} = 5.0 \text{ V}, f = 1.0 \text{ MHz}$		C <sub>CE</sub>	-	7.0	-	pF
Collector emitter leakage	V <sub>CF</sub> = 10 V	ILD610-1	I <sub>CEO</sub>	-	2.0	50	nA
current	v <sub>CE</sub> = 10 v	ILD610-3	I <sub>CEO</sub>	-	5.0	100	nA
COUPLER							
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 2.5 mA		V <sub>CEsat</sub>	-	0.25	0.40	V
Coupling capacitance			C <sub>C</sub>	-	0.35	-	pF

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub> <sup>(1)</sup>	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 5.0 V	ILD610-1	CTR	40	-	80	%
		ILD610-3	CTR	100	-	200	%
	$I_F = 1.0 \text{ mA}, V_{CE} = 5.0 \text{ V}^{\land}$	ILD610-1	CTR	13	-	-	%
		ILD610-3	CTR	34	-	-	%

### Note

(1) CTR will match within a ratio of 1.7:1

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED							
Rise time	$V_{CC} = 5.0 \text{ V}, R_{I} = 75 \Omega, I_{F} = 10 \text{ mA}$	ILD610-1			2.0	_	116
nise time	V <sub>CC</sub> = 3.0 V, H <sub>L</sub> = 73 22, H <sub>F</sub> = 10 HIA	ILD610-3	- t <sub>r</sub>	-	2.9	-	μs
Fall time	$V_{CC} = 5.0 \text{ V}, R_{I} = 75 \Omega, I_{F} = 10 \text{ mA}$	ILD610-1	+.		2.0		110
r all tillle	V <sub>CC</sub> = 3.0 V, H <sub>L</sub> = 73 22, H <sub>F</sub> = 10 HIA	ILD610-3	- t <sub>f</sub>	-	3.1	-	μs
Turn-on time	$V_{CC} = 5.0 \text{ V}, R_1 = 75 \Omega, I_F = 10 \text{ mA}$	ILD610-1	+	-	3.0	-	μs
rum-on time	V <sub>CC</sub> = 5.0 V, N <sub>L</sub> = 75 Ω, I <sub>F</sub> = 10 IIIA	ILD610-3	t <sub>on</sub>		3.6		
Turn-off time	V 50VD 750 L 10 mA	ILD610-1	t <sub>off</sub>	-	2.9	_	μs
rum-on time	$V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega, I_F = 10 \text{ mA}$	ILD610-3			3.7		
SATURATED	·						
Rise time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$	ILD610-1		t <sub>r</sub> -	2.0		μs
rise time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3	t <sub>r</sub>		2.8	1 -	
Call time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$	ILD610-1			11		
Fall time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3	t <sub>f</sub>		14		μs
Turn-on time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$ ILD610-1			3.0			
rum-on time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3	t <sub>on</sub>		4.3		μs
Turn off times	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$	ILD610-1	t <sub>off</sub>		18		
Turn-off time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3			25		μs

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# Vishay Semiconductors

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Comparative tracking index		CTI	175				
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	4420	$V_{RMS}$			
Maximum transient isolation voltage		V <sub>IOTM</sub>	10 000	$V_{peak}$			
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	890	$V_{peak}$			
Isolation resistance	$V_{IO} = 500 \text{ V}, T_{amb} = 25  ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
Output safety power		P <sub>SO</sub>	400	mW			
Input safety current		I <sub>SI</sub>	275	mA			
Safety temperature		T <sub>S</sub>	175	°C			
Creepage distance			≥ 7	mm			
Clearance distance			≥ 7	mm			
Insulation thickness		DTI	≥ 0.4	mm			

#### Note

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

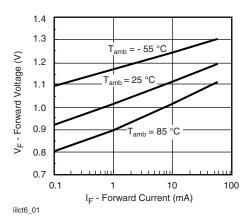


Fig. 1 - Forward Voltage vs. Forward Current

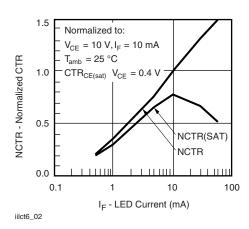


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

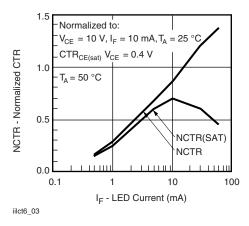


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

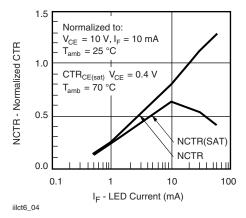


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

<sup>•</sup> As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



# Vishay Semiconductors

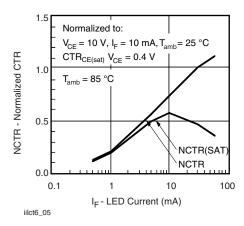


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

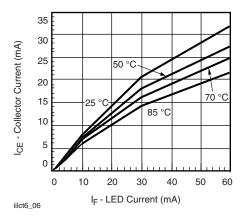


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current

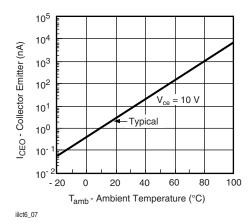


Fig. 7 - Collector Emitter Leakage Current vs.Temperature

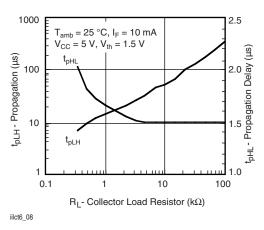


Fig. 8 - Propagation Delay vs. Collector Load Resistor

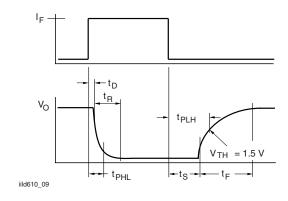


Fig. 9 - Switching Timing

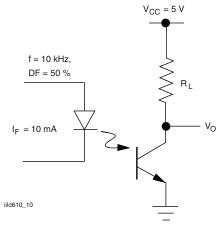


Fig. 10 - Non-Saturated Switching Schematic



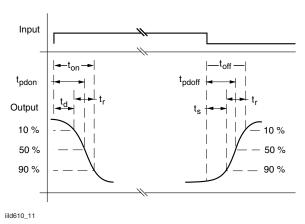
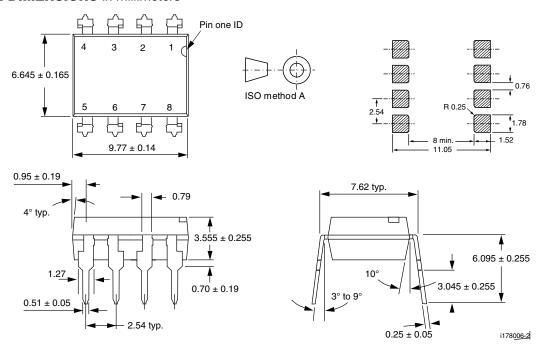


Fig. 11 - Saturated Switching Time Test Waveform

### **PACKAGE DIMENSIONS** in millimeters



### **PACKAGE MARKING**



### **Notes**

- XXXX = LMC (lot marking code)
- Option 1 is reflected in the package marking
- Tape and reel suffix (T) is not part of the package marking



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